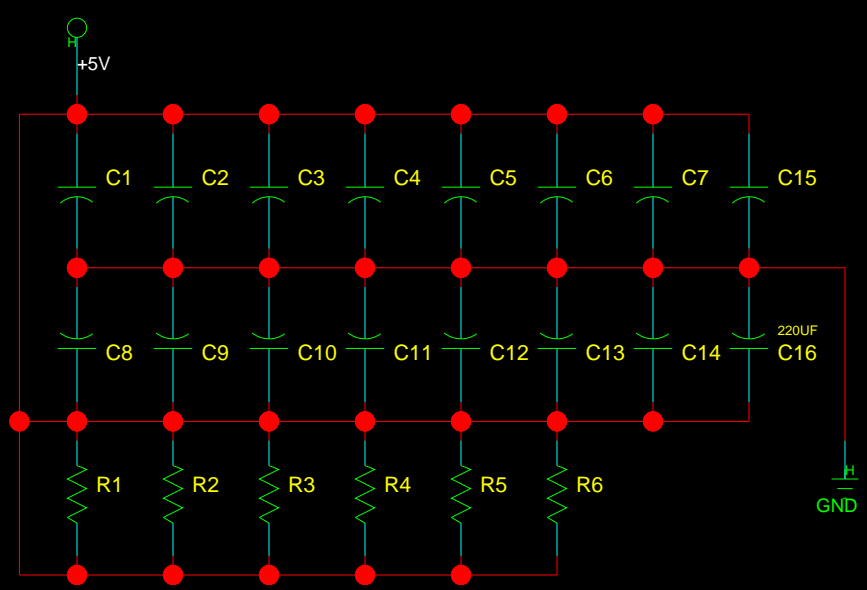
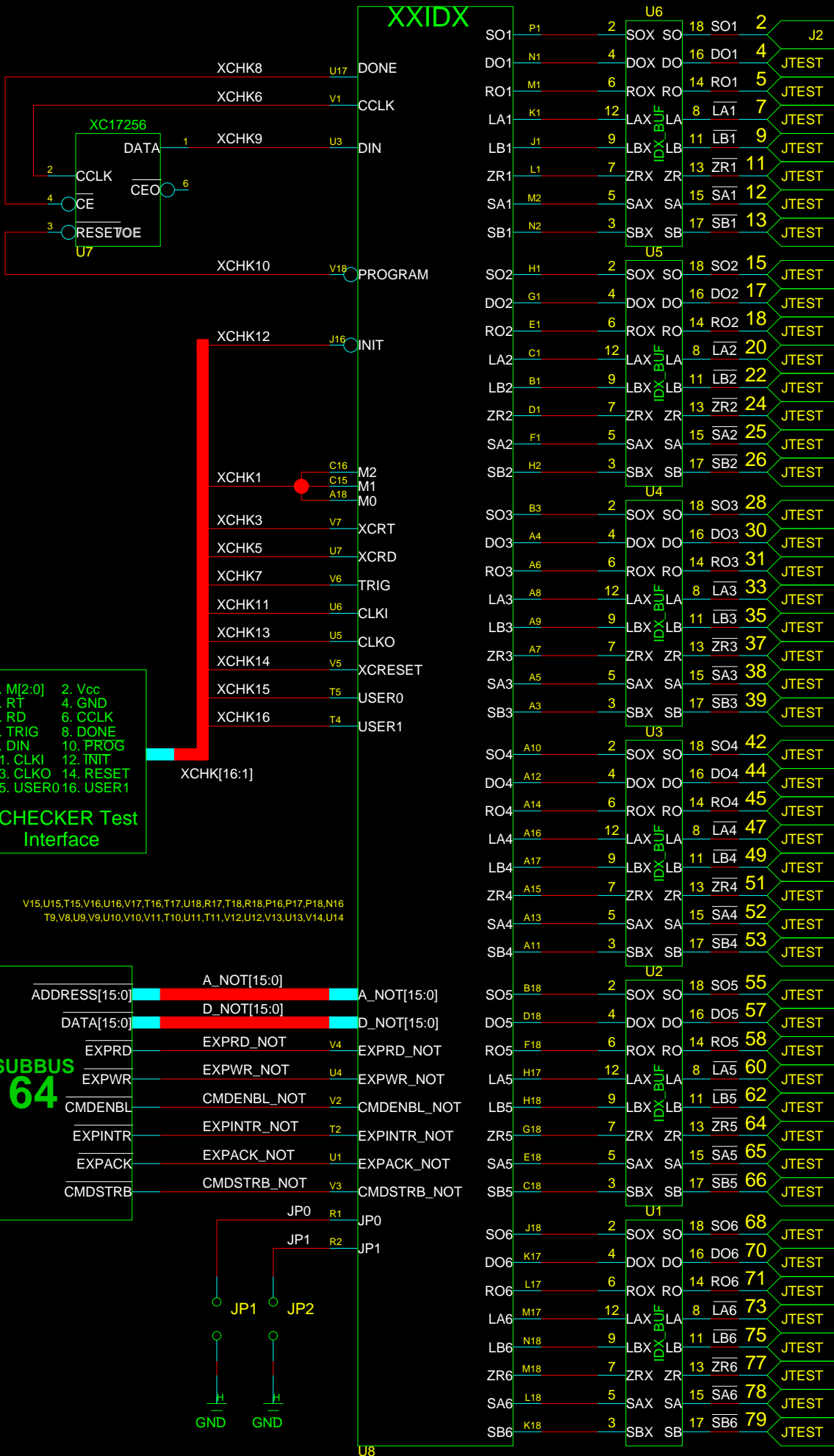


Revisions				
Zone	Itr	Description	Date	Approved



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ATMOSPHERIC RESEARCH PROJECT

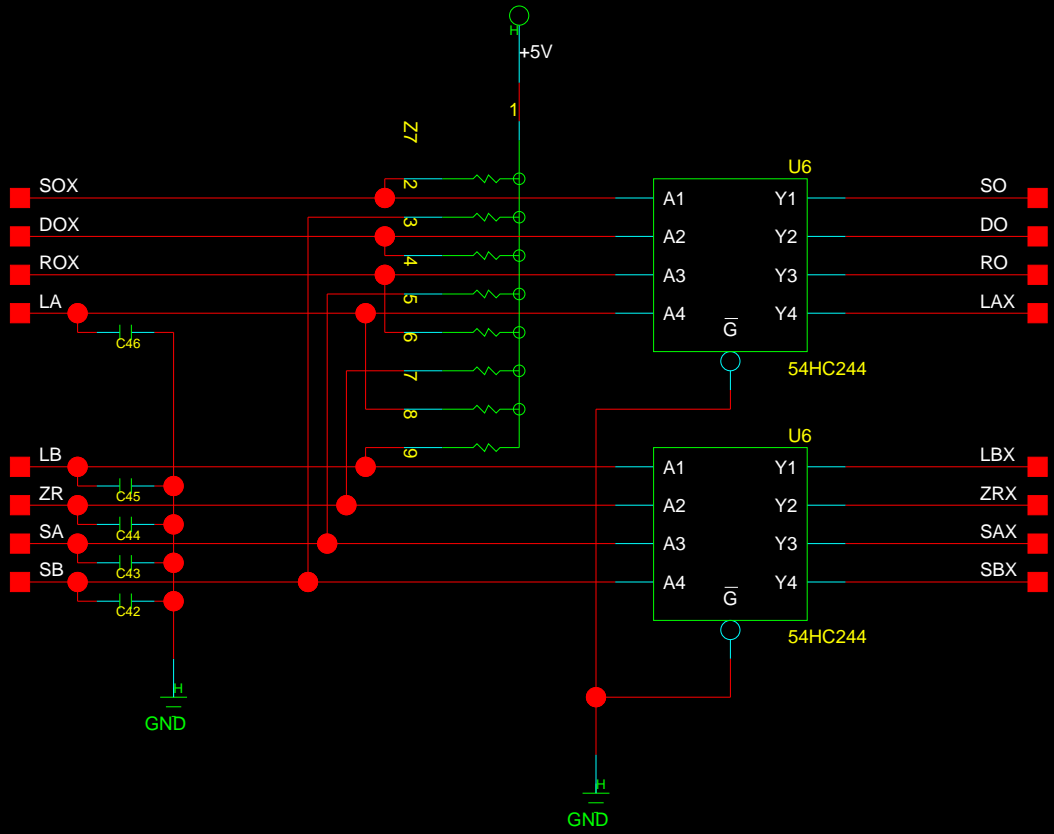
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Indexer Board Schematic

PREPARED	NTA		
CHECKED			
ENGINEER			

size	Code ID No.	Drawing No.	rev
Bp		34-7005	A.1

1-19-2006_15:12

Scale: sheet 1 of 1



BOARD MODIFICATIONS:

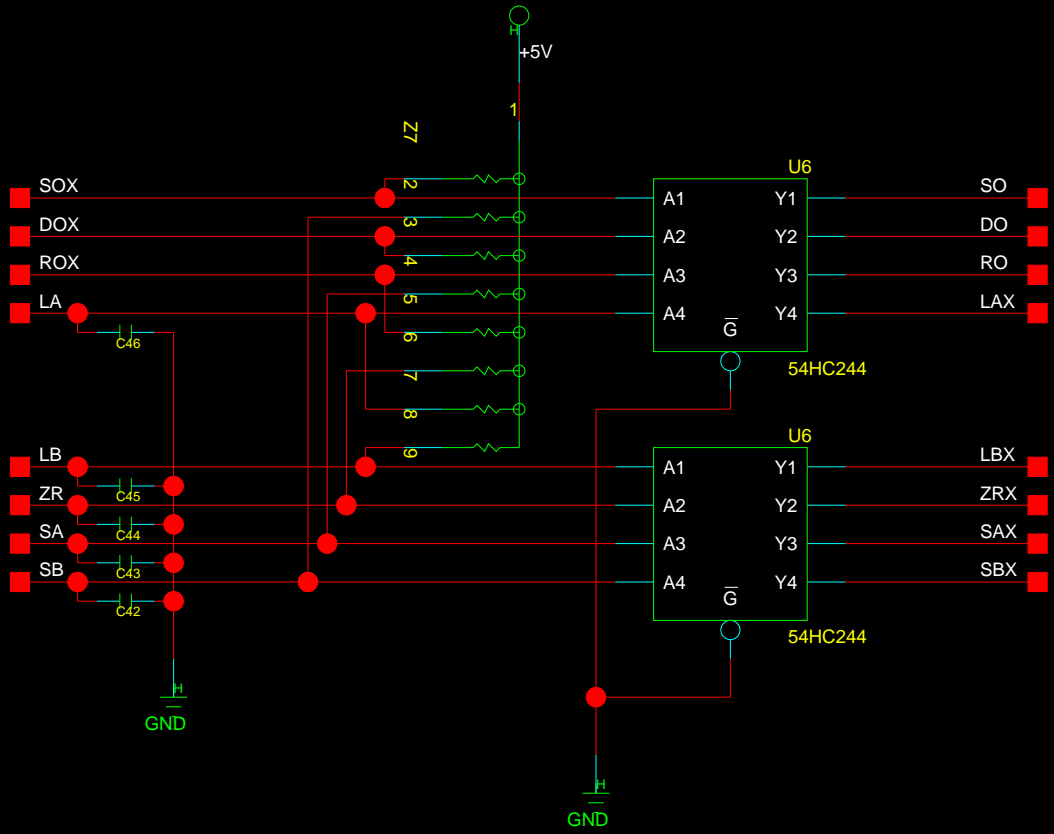
Cut Pin 6 off Z1-Z6 pullups, and install 10k pulldown to GND.

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TITLE
IDX_BUF.1
Buffer Block for Indexer

PREPARED	
CHECKED	
ENGINEER	

size	Code ID No.	Drawing No.	rev
A			A
1-19-2006_15:22		Scale	sheet 1 of 1



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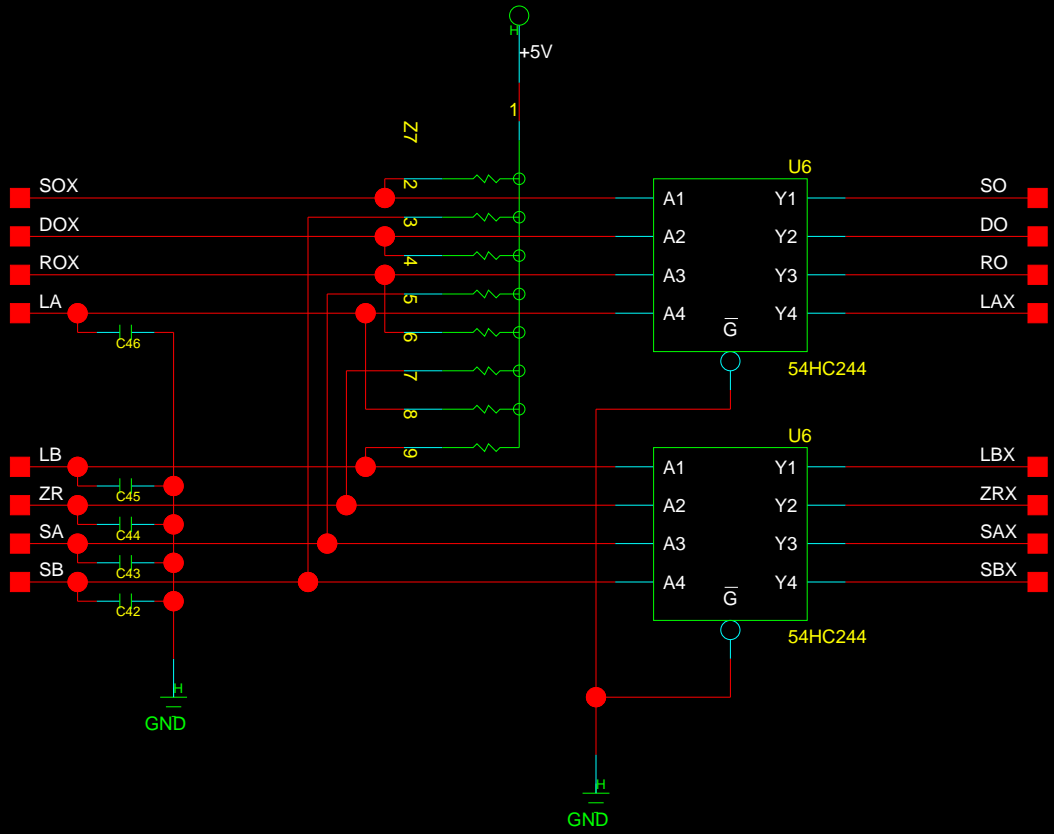
Cut Pin 6 off Z1-Z6 pullups, and install 10k pulldown to GND.

HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE
IDX_BUF.1
Buffer Block for Indexer

PREPARED	
CHECKED	
ENGINEER	

size	Code ID No.	Drawing No.	rev
A			A
1-19-2006_15:22		Scale	sheet 1 of 1



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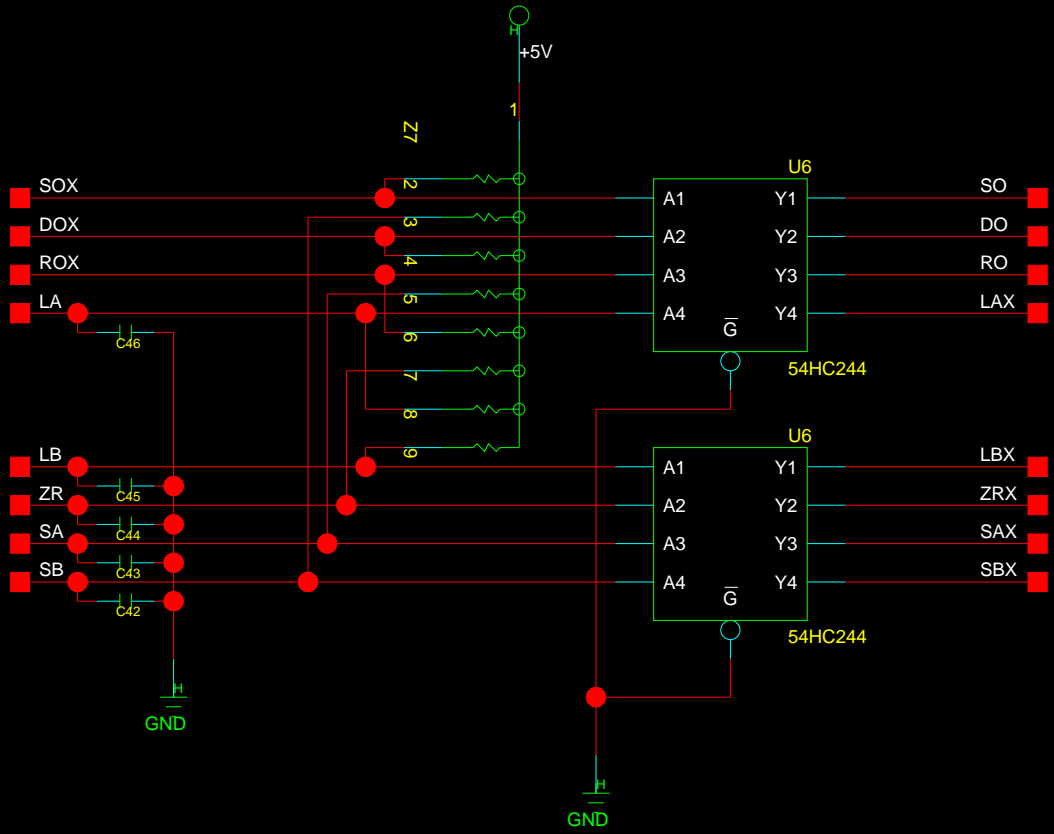
Cut Pin 6 off Z1-Z6 pullups, and install 10k pulldown to GND.

HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE
IDX_BUF.1
Buffer Block for Indexer

PREPARED	
CHECKED	
ENGINEER	

size	Code ID No.	Drawing No.	rev
A			A
1-19-2006_15:22		Scale	sheet 1 of 1



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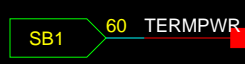
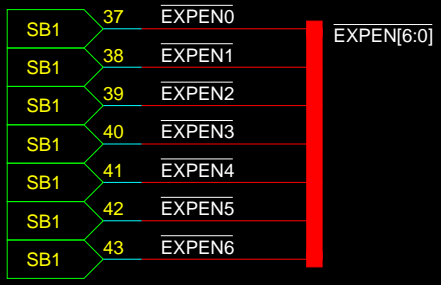
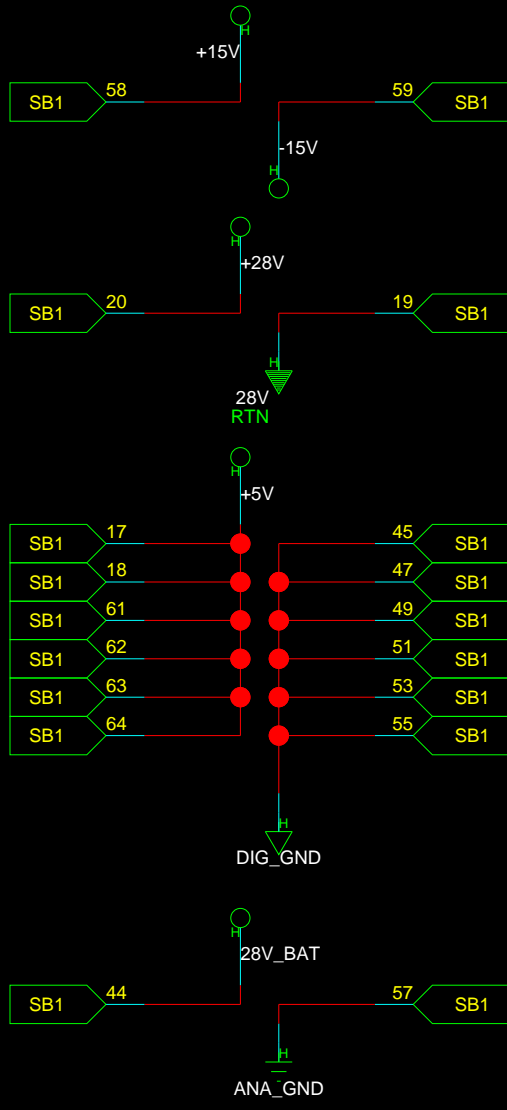
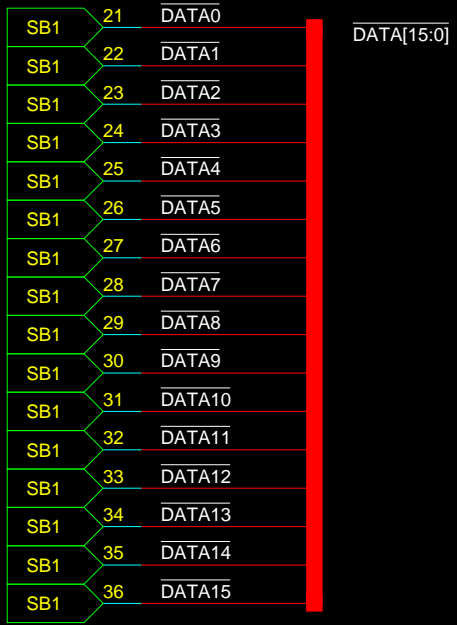
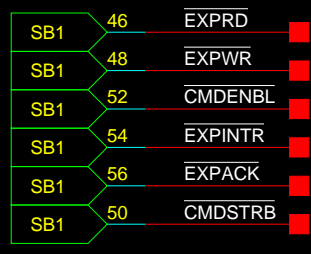
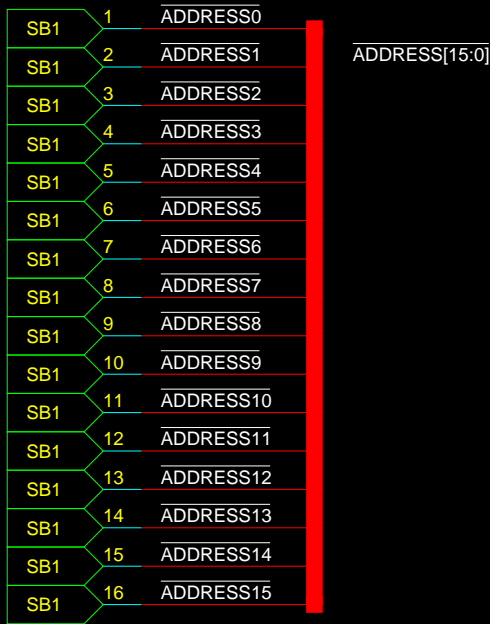
Cut Pin 6 off Z1-Z6 pullups, and install 10k pulldown to GND.

HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE
IDX_BUF.1
Buffer Block for Indexer

PREPARED	
CHECKED	
ENGINEER	

size	Code ID No.	Drawing No.	rev
A			A
1-19-2006_15:22		Scale	sheet 1 of 1

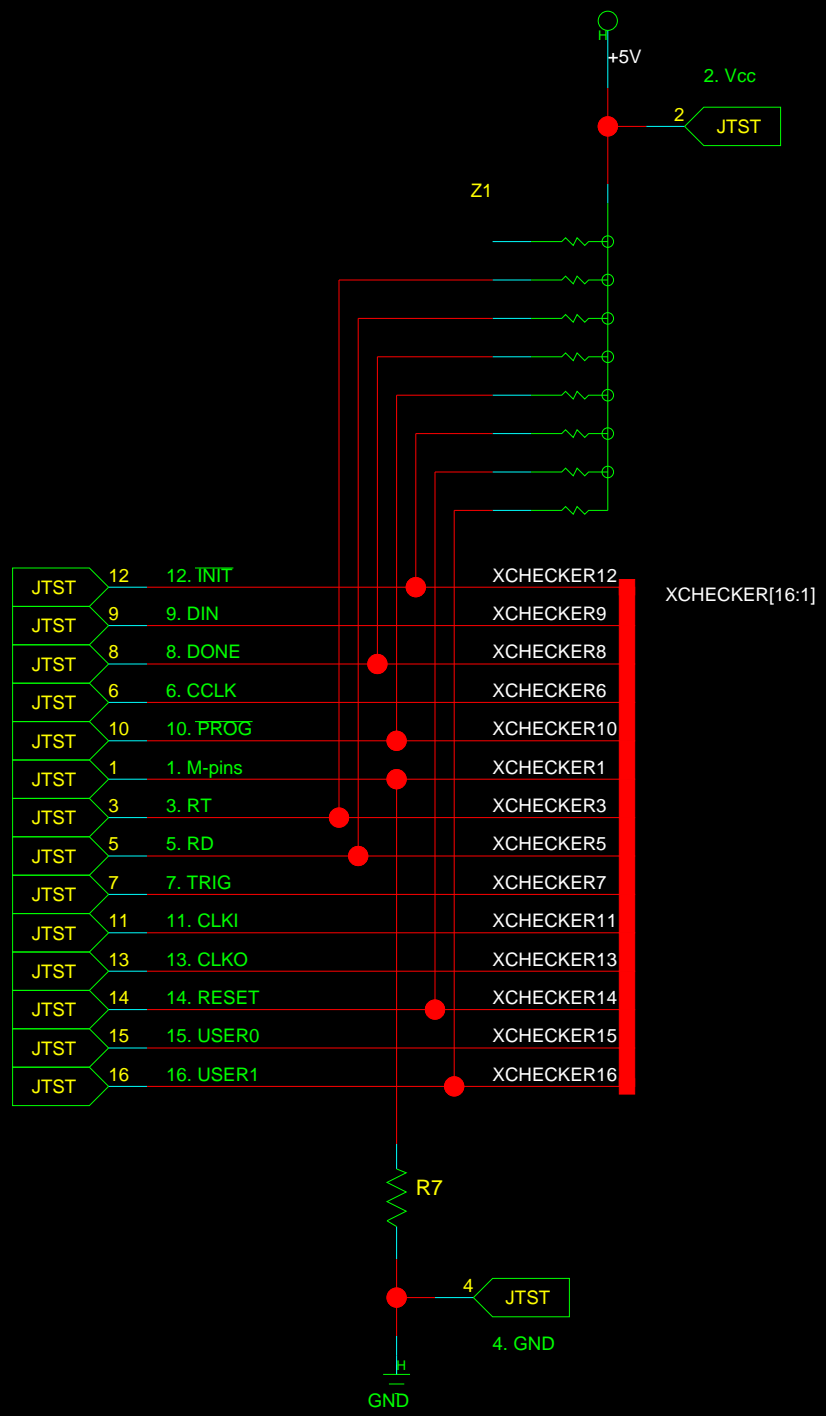


HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE
SUBBUS Connector Definition

PREPARED	MR	
CHECKED		
ENGINEER	MR	

SUBBUS		size	Code ID No.	Drawing No.	rev
1-19-2006_15:25		A			A
Scale			sheet 1 of 1		

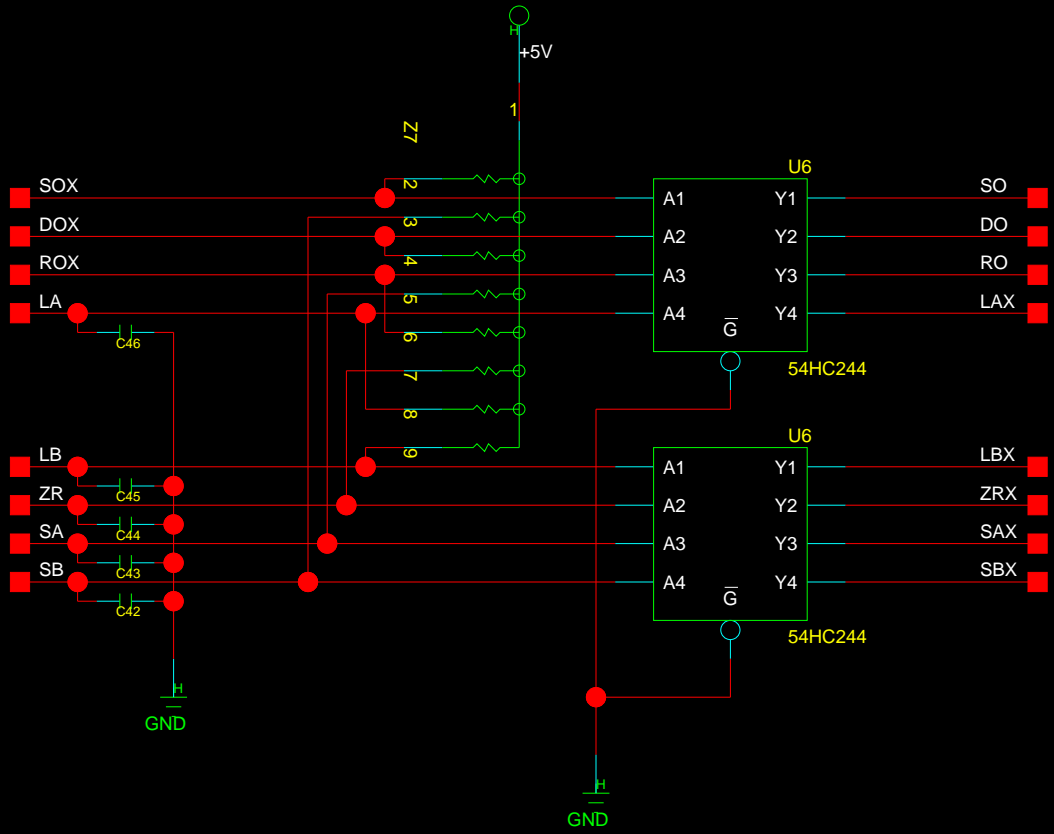


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TITLE
XCHECKER Test Interface
Connector Definition

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CHECKED		
ENGINEER	MR	

XCHECKER		size	Code ID No.	Drawing No.	rev
1-19-2006_15:24		A			A
Scale		NO	sheet 1 of 1		



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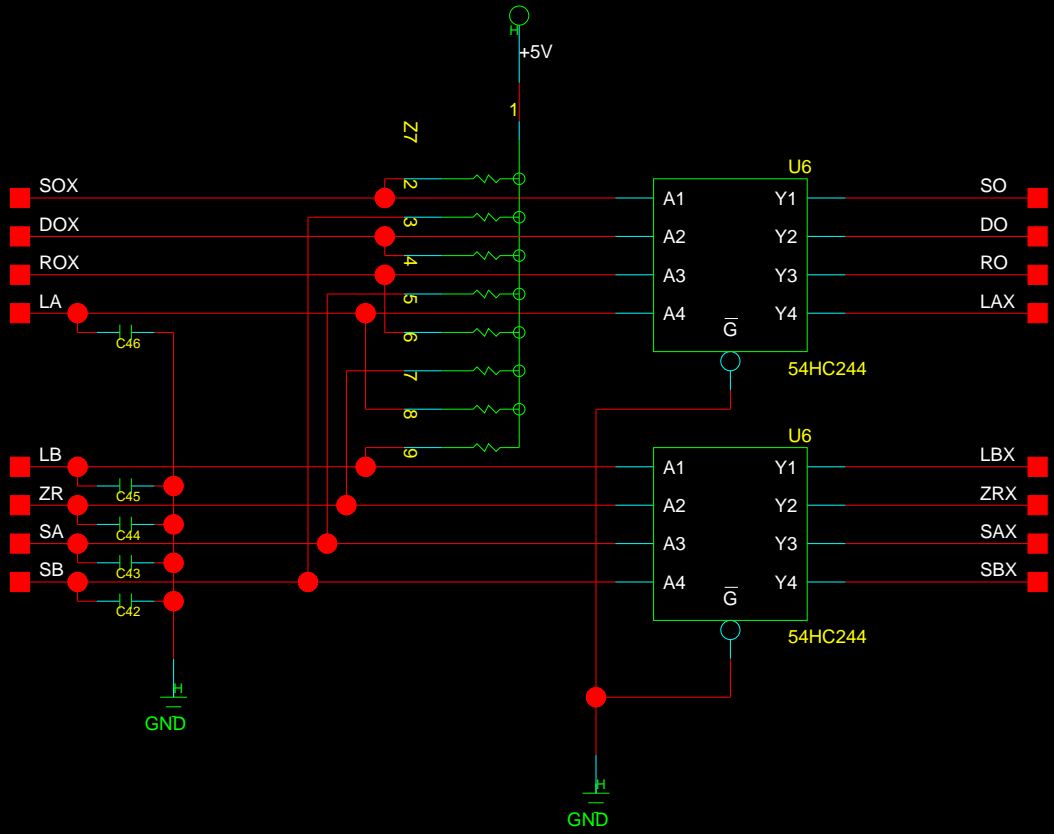
Cut Pin 6 off Z1-Z6 pullups, and install 10k pulldown to GND.

HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE
IDX_BUF.1
Buffer Block for Indexer

PREPARED	
CHECKED	
ENGINEER	

size	Code ID No.	Drawing No.	rev
A			A
1-19-2006_15:22		Scale	sheet 1 of 1



BOARD MODIFICATIONS:

Cut Pin 6 off Z1-Z6 pullups, and install 10k pulldown to GND.

HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE
IDX_BUF.1
Buffer Block for Indexer

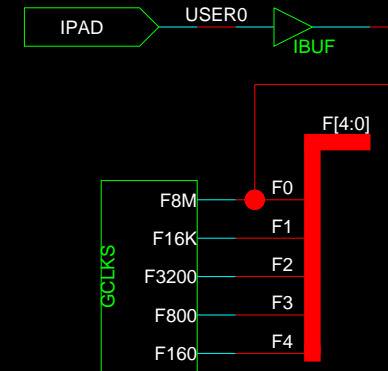
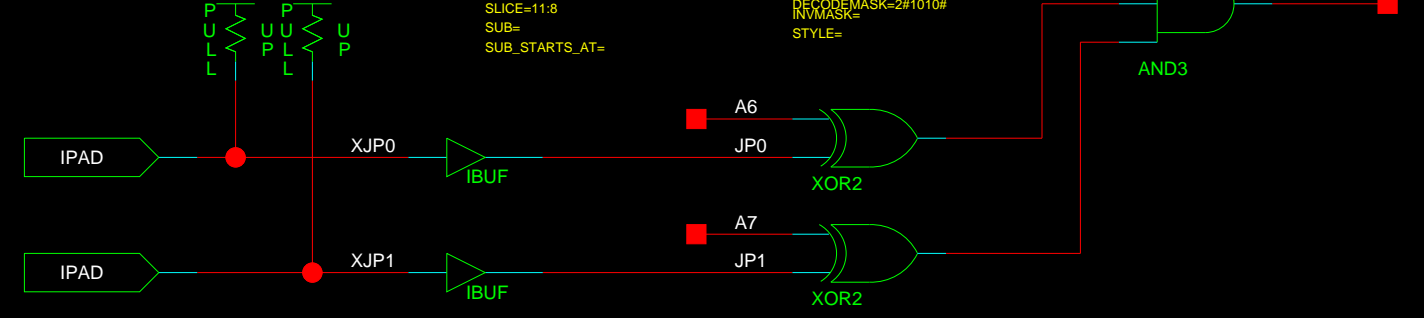
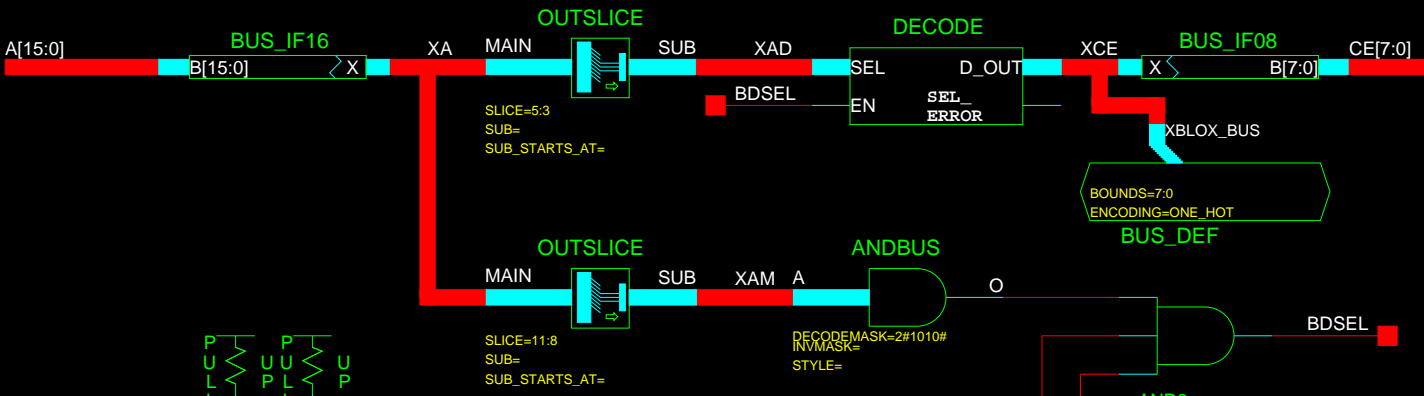
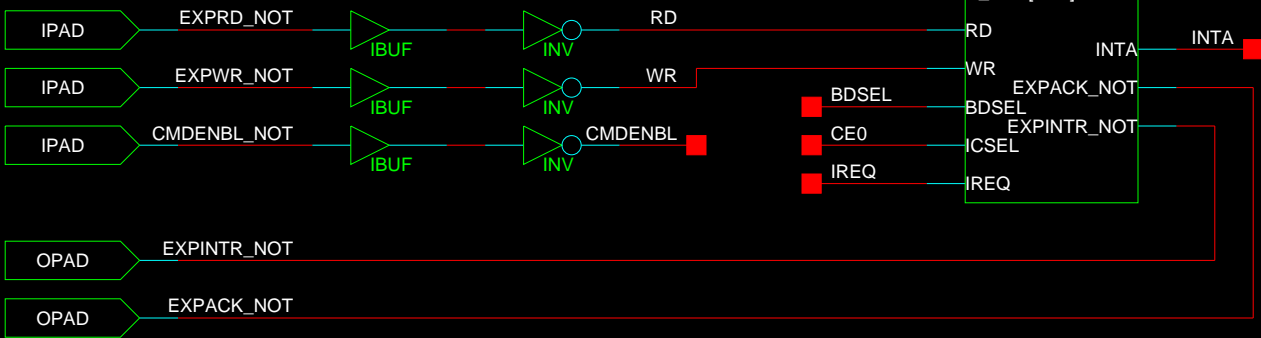
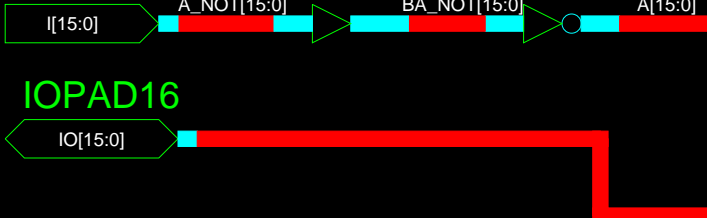
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CHECKED	
ENGINEER	

size	Code ID No.	Drawing No.	rev
A			A
1-19-2006_15:22		Scale	sheet 1 of 1

IPAD16

IBUF16

INV16



See also B34-7005

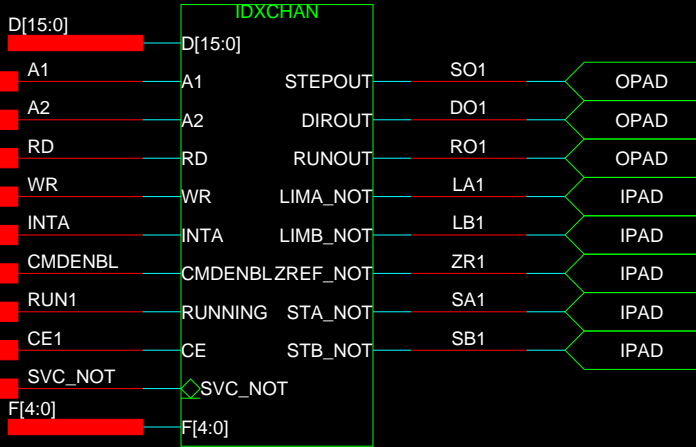
HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

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XXIDX.1
 Indexer Xilinx Chip

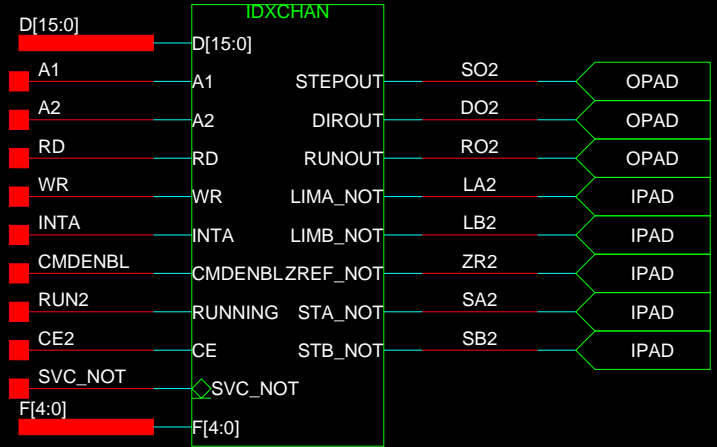
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CHECKED	
ENGINEER	NTA
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1-19-2006_15:12	

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Scale		sheet	1 of 183

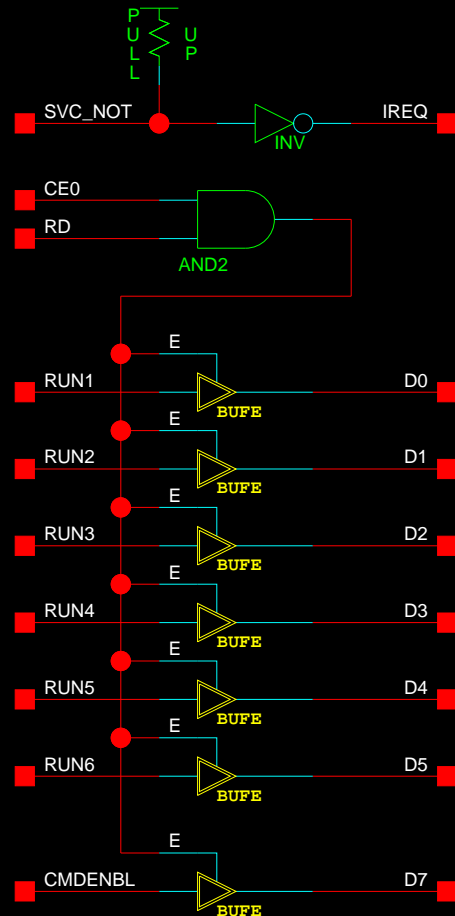
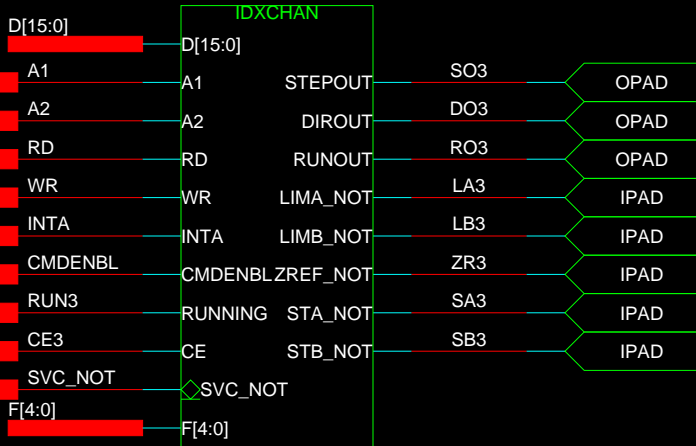
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Channel 2



Channel 3



**HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT**

TITLE

XXIDX.2

Individual Channels

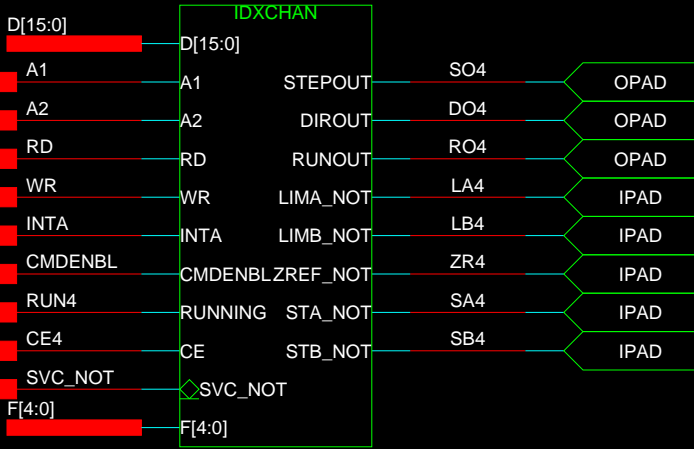
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ENGINEER	NTA

XXIDX

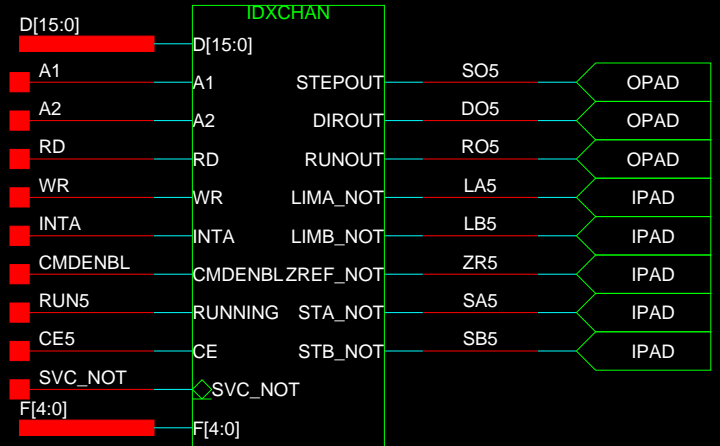
1-19-2006_15:28

size	Code ID No.	Drawing No.	rev
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Scale	sheet 2 of 3		2 of 18

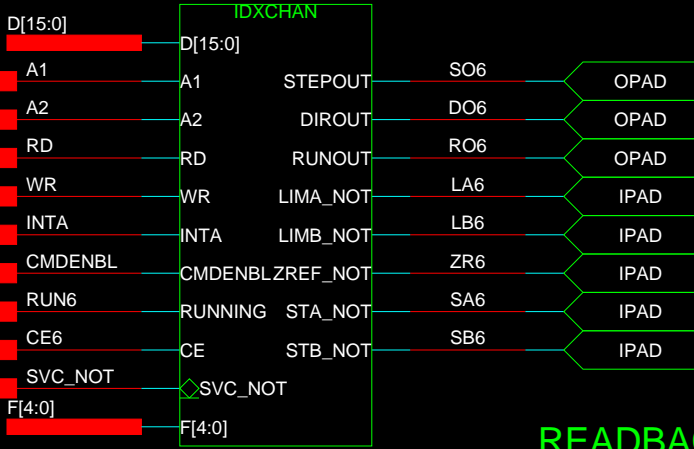
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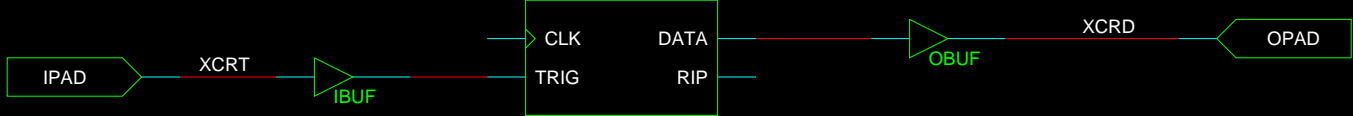
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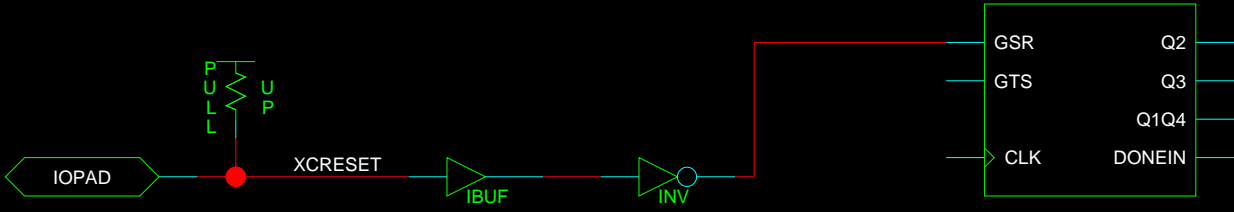
Channel 6



READBACK



STARTUP



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TITLE

XXIDX.3

PREPARED	
CHECKED	
ENGINEER	NTA

XXIDX

1-19-2006_15:30

size	Code ID No.	Drawing No.	rev
A		A34-7005	A
Scale		sheet	3 of 3



Title:	XC4000 Family IOPAD16 Macro		
Comments:	16-bit Input-Output Pad		
Date:	23rd February 1993	Ver:	1
Sheet Size: A			Rev: A



Title: XBLOX Family BUS_IF08

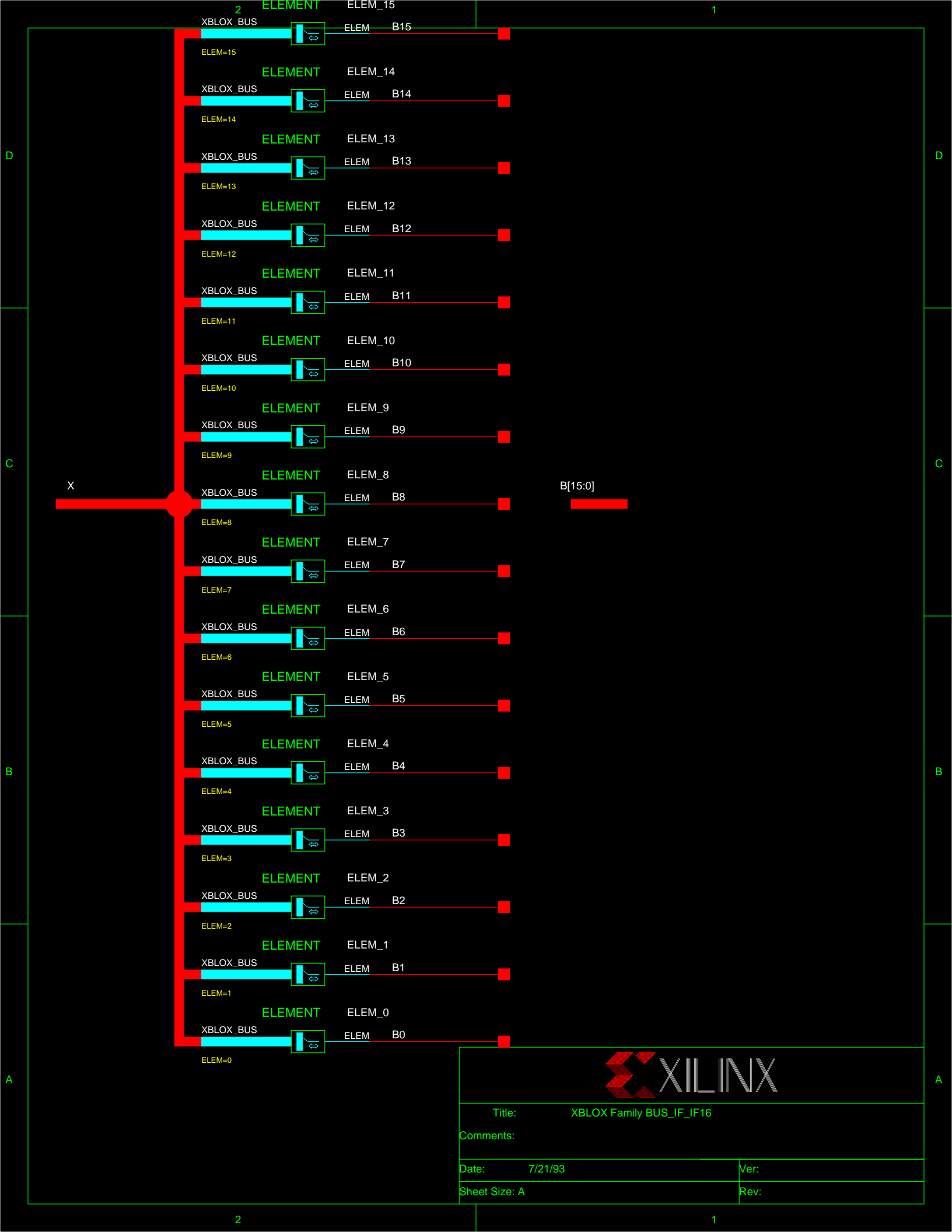
Comments:

Date: 7/21/93

Ver: 1

Sheet Size: A

Rev: A



2

ELEMENT

ELEM_15

XBLOX_BUS

ELEM B15

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ELEMENT

ELEM_14

XBLOX_BUS

ELEM B14

ELEM=14

ELEMENT

ELEM_13

XBLOX_BUS

ELEM B13

ELEM=13

ELEMENT

ELEM_12

XBLOX_BUS

ELEM B12

ELEM=12

ELEMENT

ELEM_11

XBLOX_BUS

ELEM B11

ELEM=11

ELEMENT

ELEM_10

XBLOX_BUS

ELEM B10

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ELEM_9

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ELEM B9

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ELEM_8

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ELEM B0

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Title: XBLOX Family BUS_IF_IF16

Comments:

Date: 7/21/93

Ver:

Sheet Size: A

Rev:

2

1

D

D

C

C

B

B

A

A

X

D

D

C

C

B

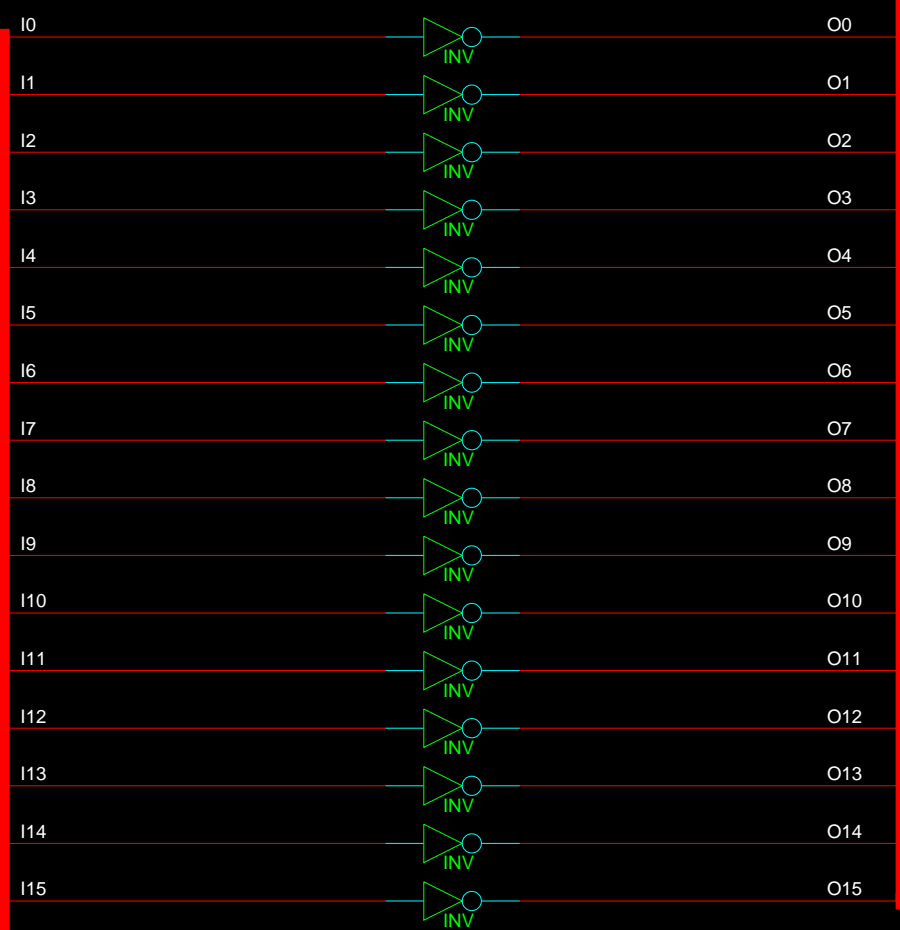
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A

A

O[15:0]

I[15:0]



Title: XC4000 Family INV16 Macro

Comments: 16-bit Inverter

Date:	22nd February 1993	Ver:	1
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Sheet Size: A	Rev:	A
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D

D

C

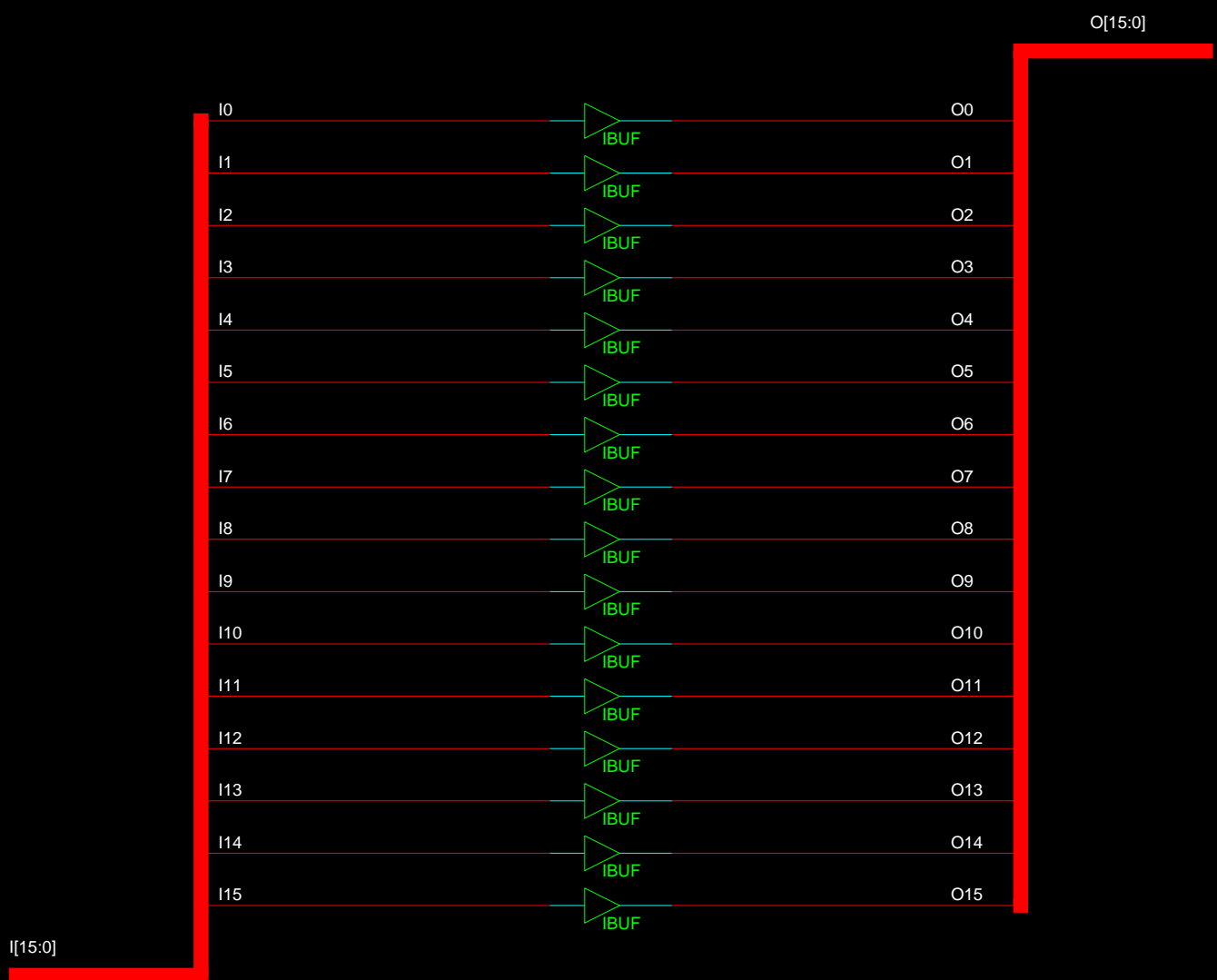
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A

A

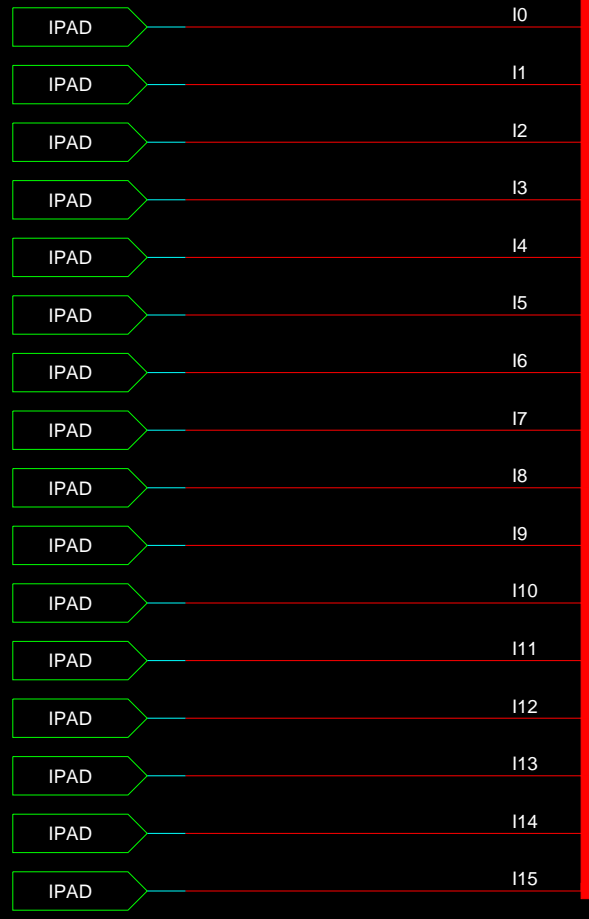


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Date:	25th January 1993	Ver:	1
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C

C

B

B

A

A



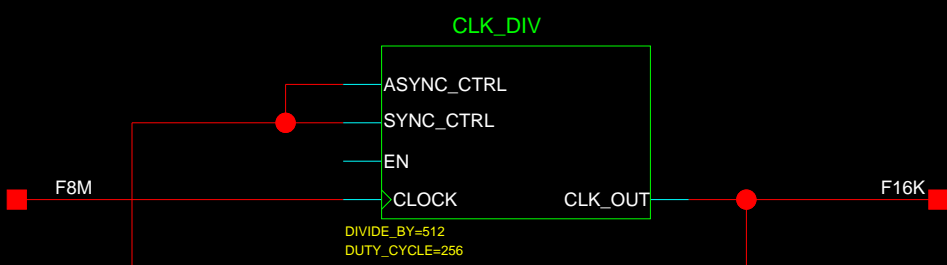
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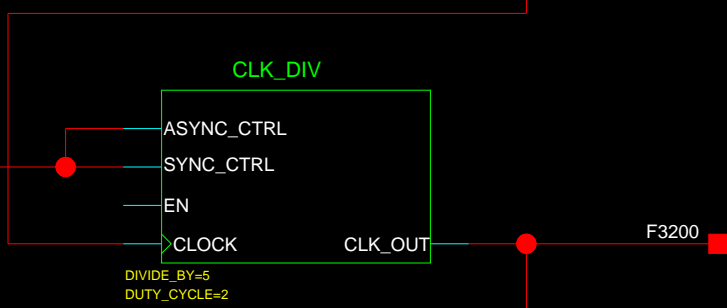
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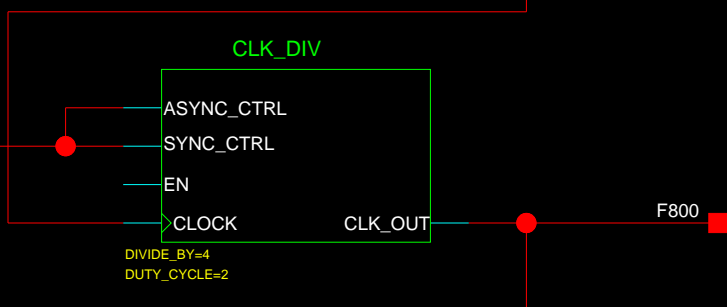
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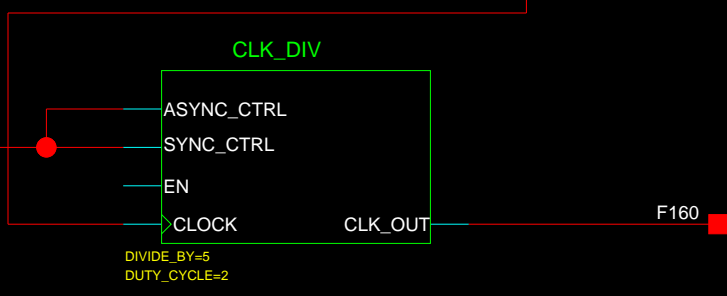
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4

4



GND

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 ATMOSPHERIC RESEARCH PROJECT

TITLE
GCLKS.1
 Global Rate Clocks

PREPARED		
CHECKED		
ENGINEER	NTA	

GCLKS

size	Code ID No.	Drawing No.	rev
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1-19-2006_15:26

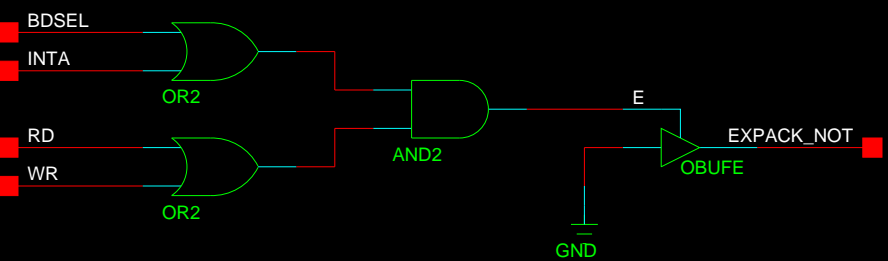
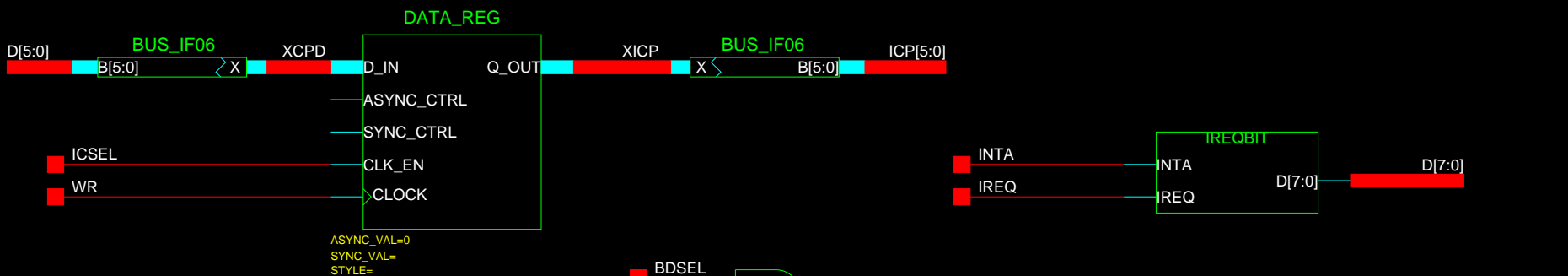
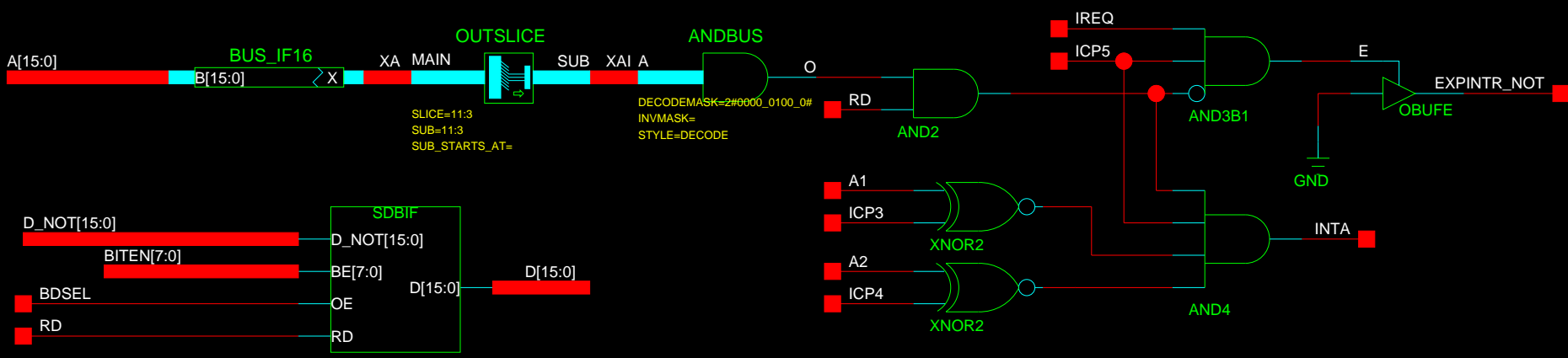
Scale sheet 1 of 1

A

B

5

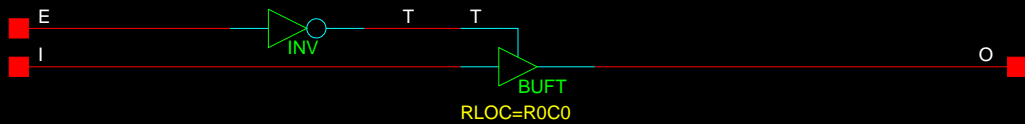
5



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TITLE
SBIF.1
Subbus Interface

PREPARED			
CHECKED			
ENGINEER	NTA		
SBIF		size	Code ID No.
1-19-2006_15:38		AI	A34-7005
		Drawing No.	rev
		sheet 1 of 1	A
		5 of 18	



Title: XC4000 Family BUFE Macro

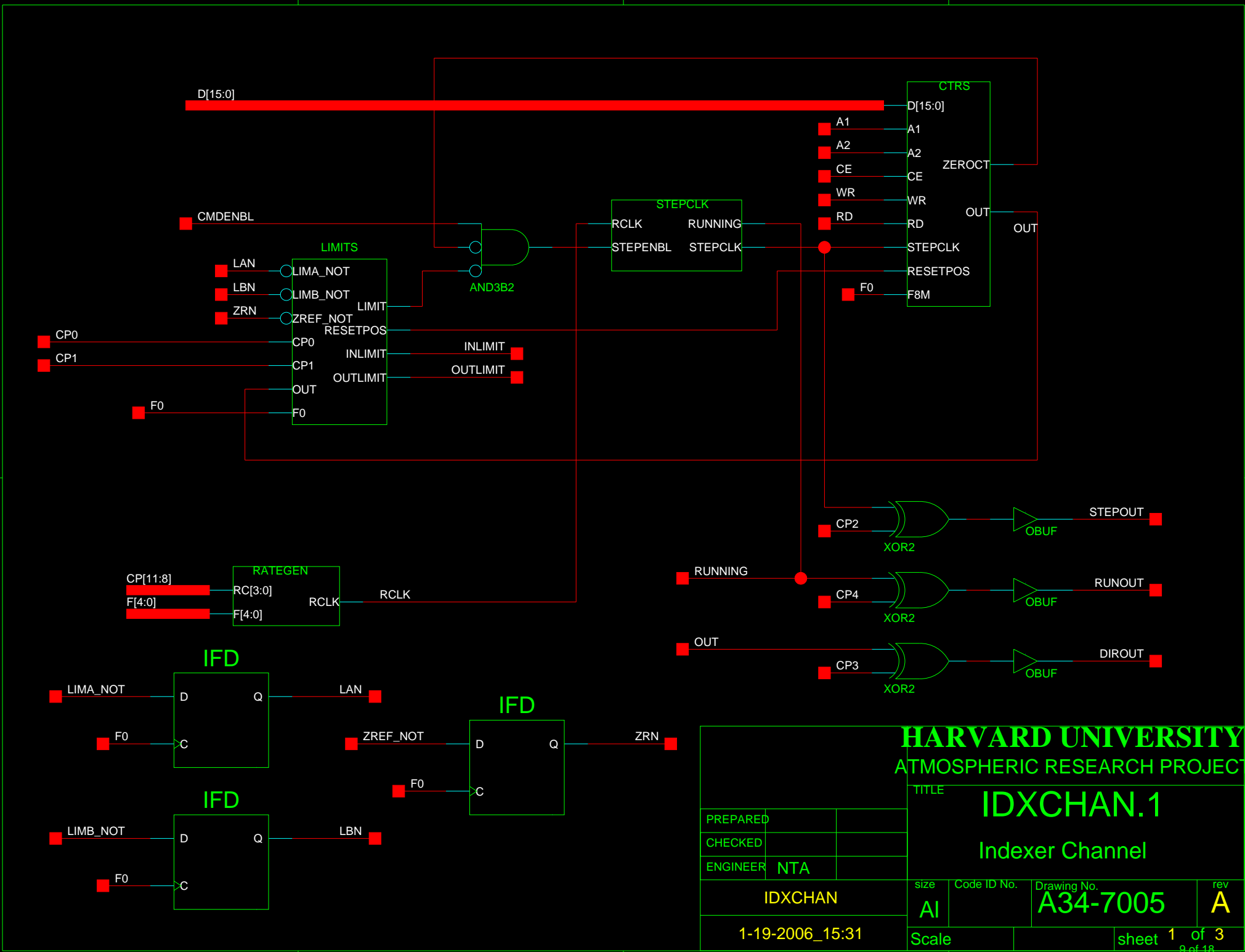
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Date: 3rd March 1993

Ver: 1

Sheet Size: A

Rev: A



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ATMOSPHERIC RESEARCH PROJECT

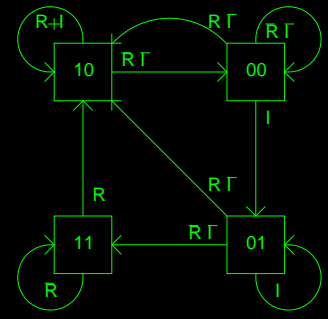
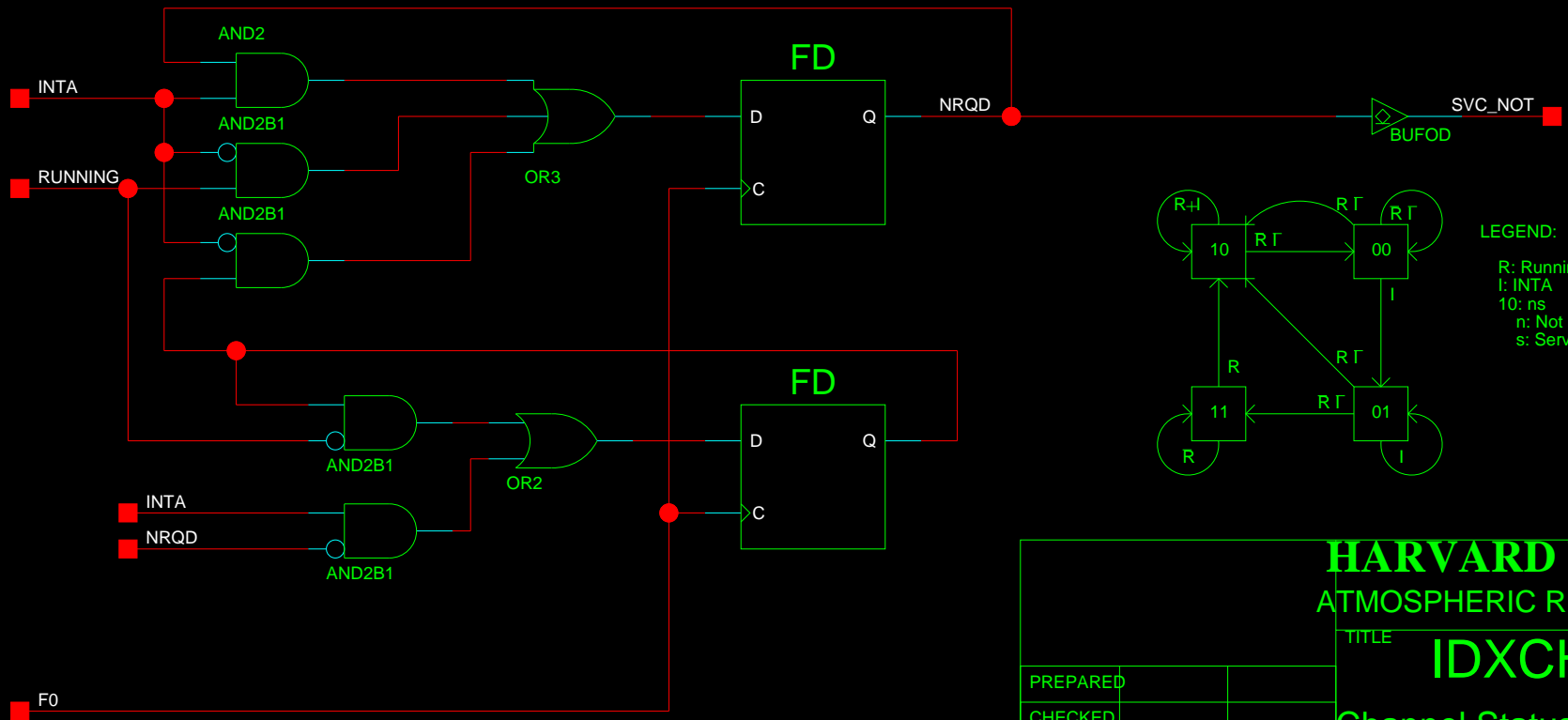
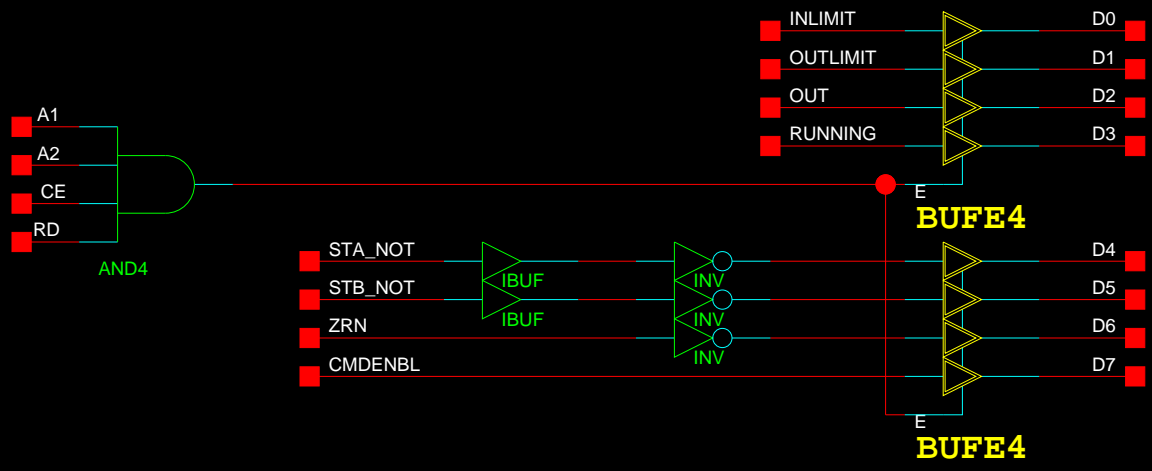
TITLE
IDXCHAN.1

Indexer Channel

PREPARED	
CHECKED	
ENGINEER	NTA

size	Code ID No.	Drawing No.	rev
AI		A34-7005	A
1-19-2006_15:31		sheet 1 of 3	9 of 18

size	Code ID No.	Drawing No.	rev
AI		A34-7005	A
1-19-2006_15:31		sheet 1 of 3	9 of 18



LEGEND:
 R: Running
 I: INTA
 n: Not Requested (NRQD)
 s: Serviced

HARVARD UNIVERSITY
 ATMOSPHERIC RESEARCH PROJECT

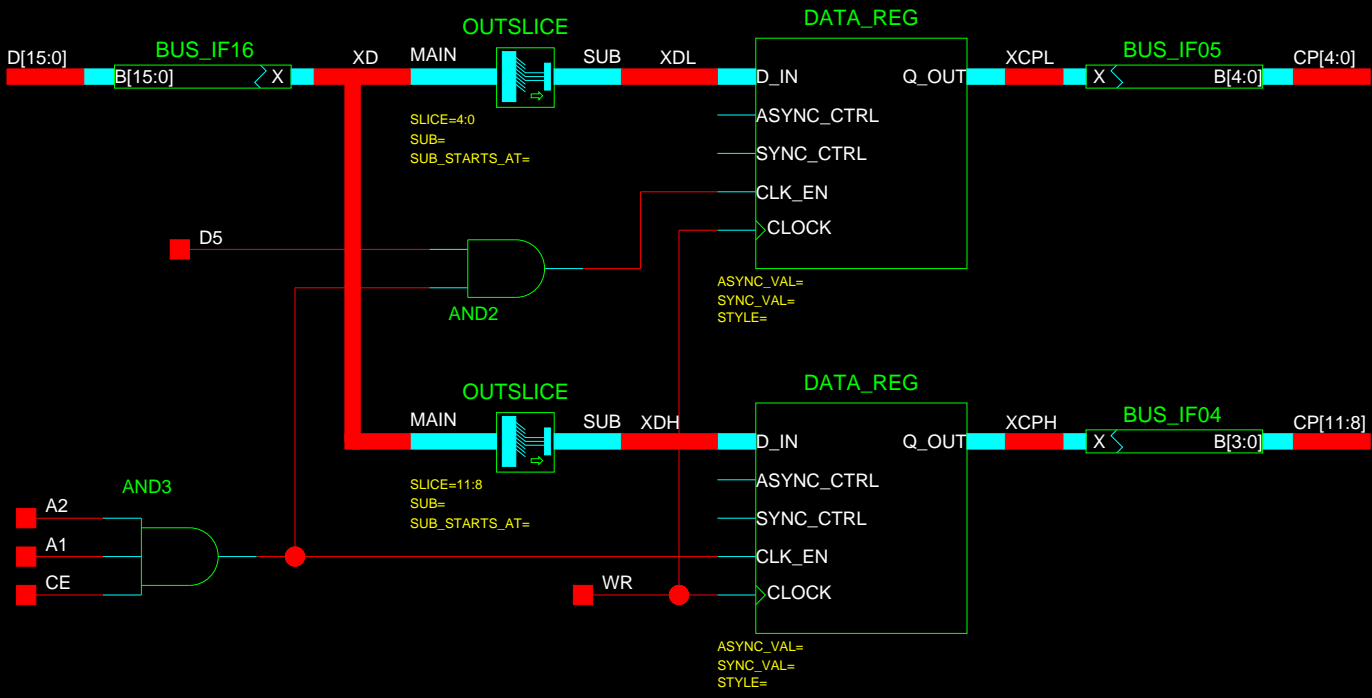
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IDXCHAN.2

Channel Status, Service Req.

PREPARED	
CHECKED	
ENGINEER	NTA

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1-19-2006_15:32		sheet 2 of 3	10 of 18

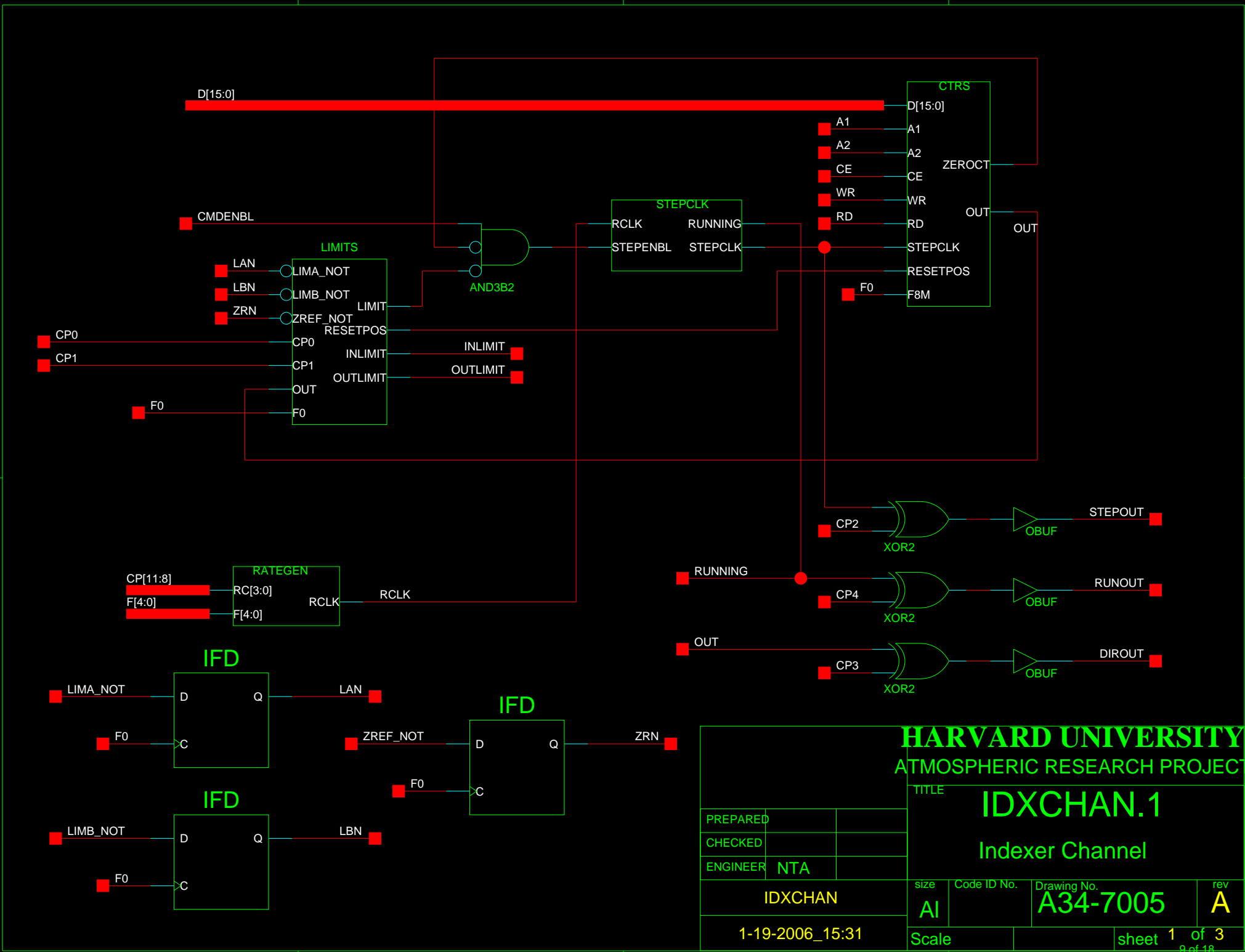
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AI		A34-7005	A
1-19-2006_15:32		sheet 2 of 3	10 of 18



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ATMOSPHERIC RESEARCH PROJECT

PREPARED	
CHECKED	
ENGINEER	NTA
IDXCHAN	
1-19-2006_15:33	

TITLE			
IDXCHAN.3			
Channel Configuration Port			
size	Code ID No.	Drawing No.	rev
A		A34-7005	A
Scale		sheet 3 of 3	
		11 of 18	



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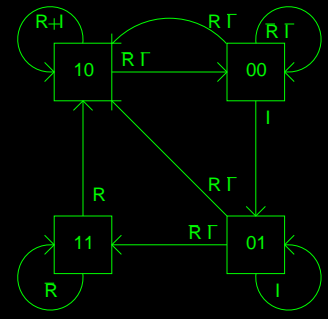
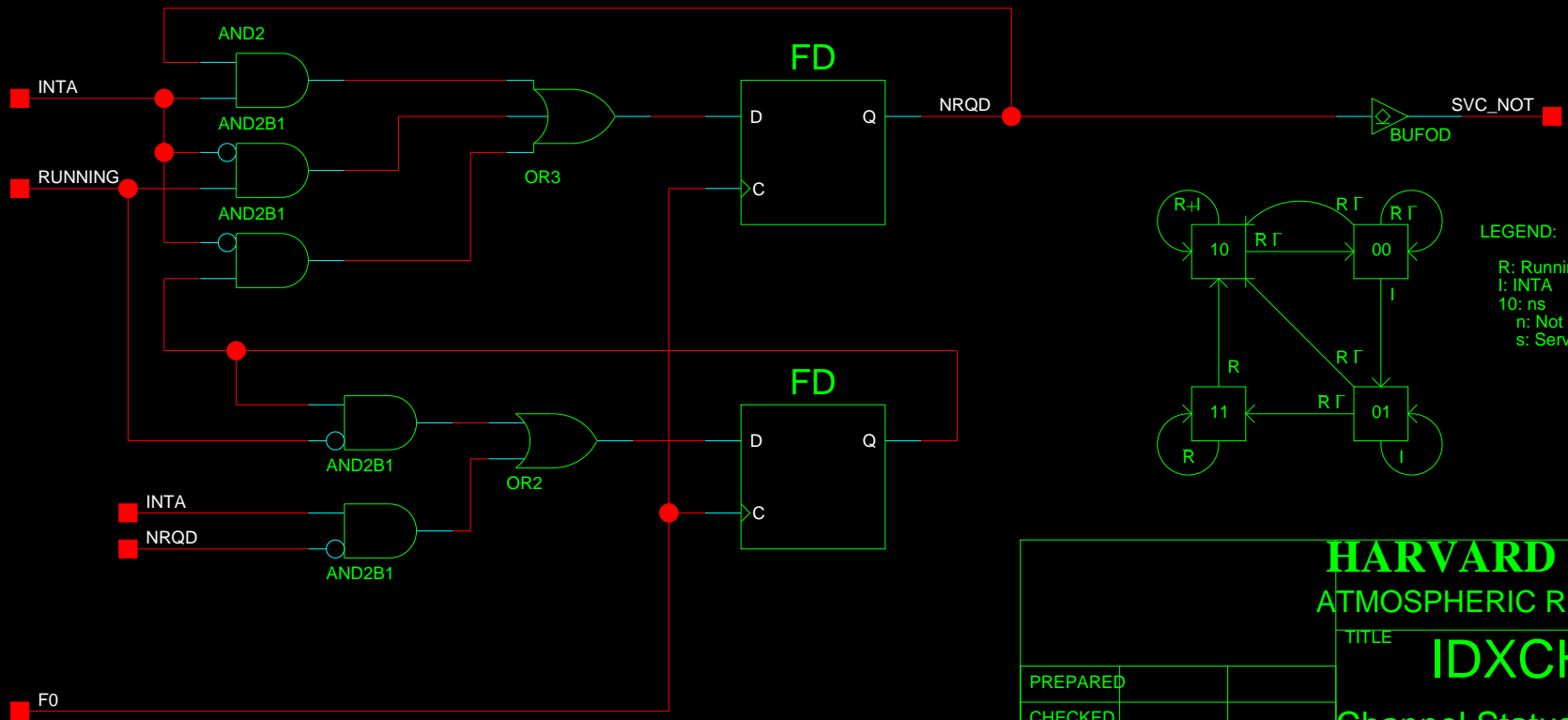
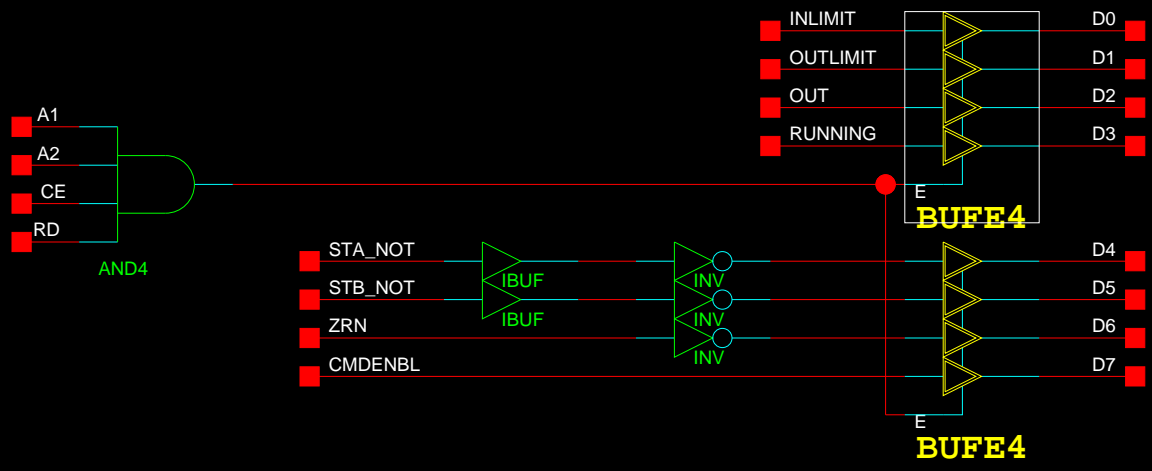
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Indexer Channel

PREPARED	
CHECKED	
ENGINEER	NTA

size	Code ID No.	Drawing No.	rev
AI		A34-7005	A
1-19-2006_15:31		sheet 1 of 3	9 of 18

size	Code ID No.	Drawing No.	rev
AI		A34-7005	A
1-19-2006_15:31		sheet 1 of 3	9 of 18



LEGEND:
 R: Running
 I: INTA
 10: ns
 n: Not Requested (NRQD)
 s: Serviced

HARVARD UNIVERSITY
 ATMOSPHERIC RESEARCH PROJECT

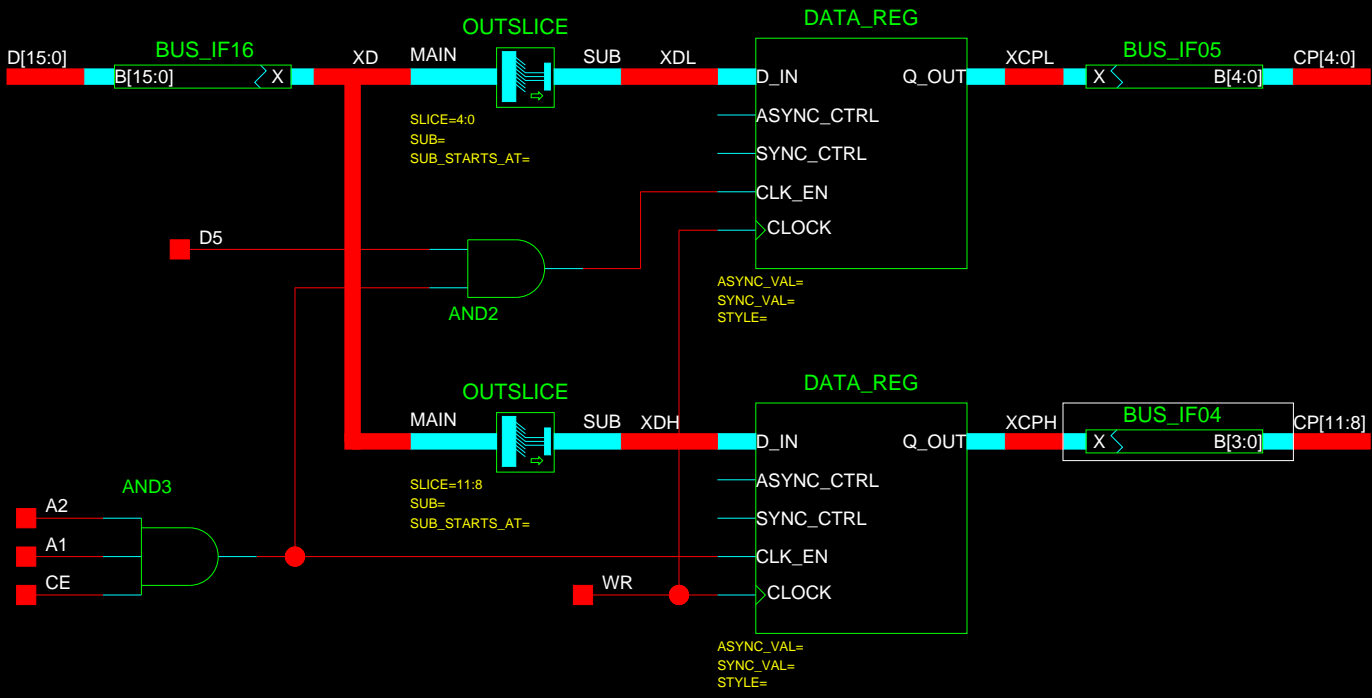
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IDXCHAN.2

Channel Status, Service Req.

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CHECKED	
ENGINEER	NTA

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1-19-2006_15:32		sheet 2 of 3	10 of 18

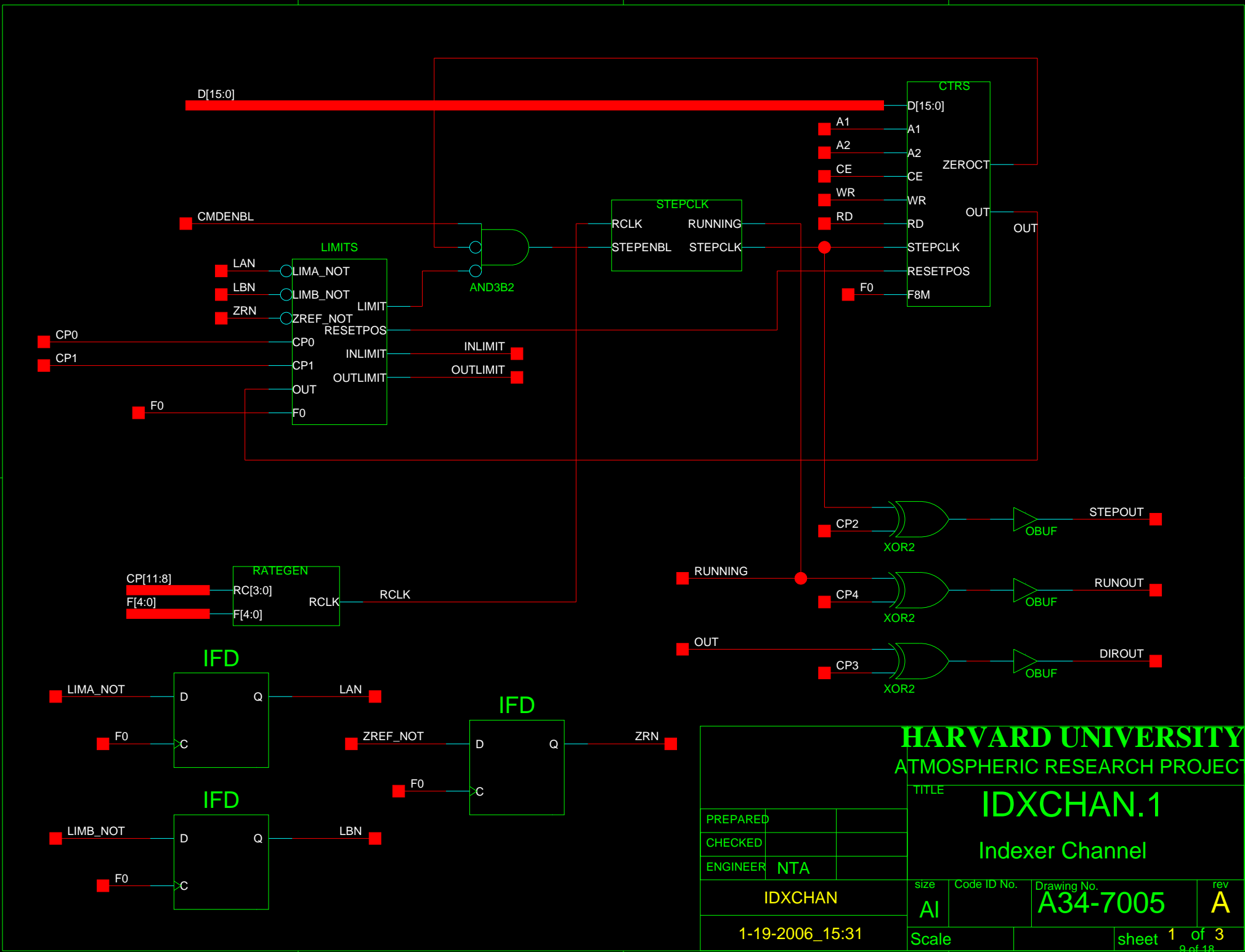
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1-19-2006_15:32		sheet 2 of 3	10 of 18



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ATMOSPHERIC RESEARCH PROJECT

PREPARED	
CHECKED	
ENGINEER	NTA
IDXCHAN	
1-19-2006_15:33	

TITLE			
IDXCHAN.3			
Channel Configuration Port			
size	Code ID No.	Drawing No.	rev
A		A34-7005	A
Scale		sheet 3 of 3	
11 of 18			



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ATMOSPHERIC RESEARCH PROJECT

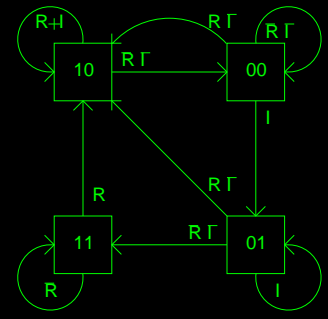
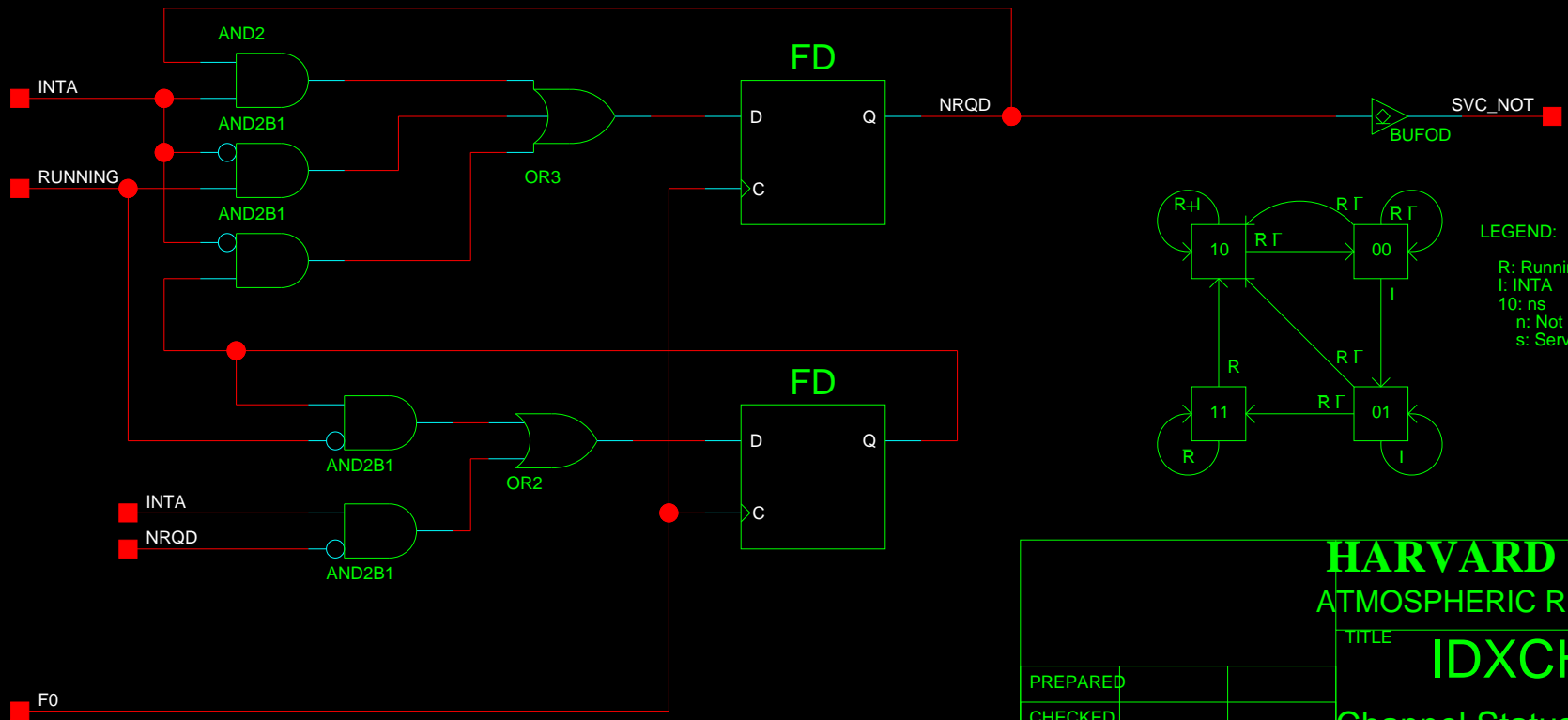
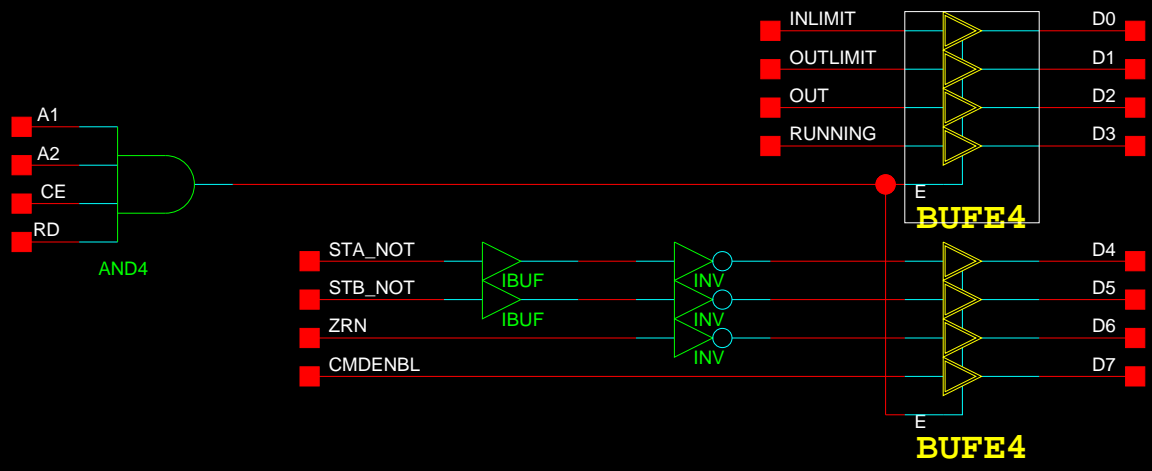
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IDXCHAN.1

Indexer Channel

PREPARED	
CHECKED	
ENGINEER	NTA

size	Code ID No.	Drawing No.	rev
AI		A34-7005	A
1-19-2006_15:31		sheet 1 of 3	9 of 18

size	Code ID No.	Drawing No.	rev
AI		A34-7005	A
1-19-2006_15:31		sheet 1 of 3	9 of 18



LEGEND:
 R: Running
 I: INTA
 n: Not Requested (NRQD)
 s: Serviced

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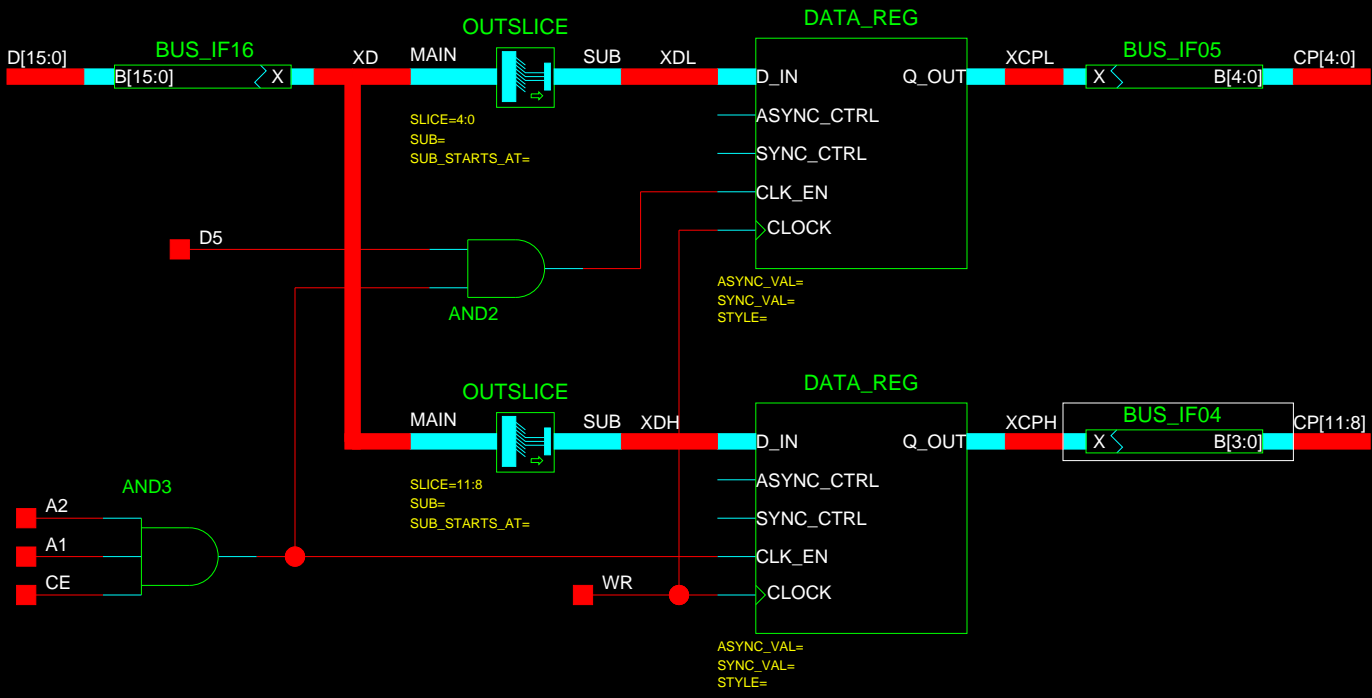
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Channel Status, Service Req.

PREPARED	
CHECKED	
ENGINEER	NTA

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1-19-2006_15:32		sheet 2 of 3	10 of 18

size	Code ID No.	Drawing No.	rev
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1-19-2006_15:32		sheet 2 of 3	10 of 18



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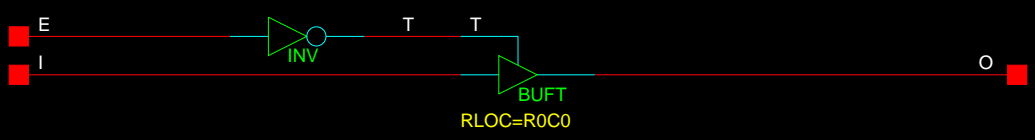
TITLE
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 Channel Configuration Port

PREPARED	
CHECKED	
ENGINEER	NTA

1-19-2006_15:33	size A	Code ID No.	Drawing No. A34-7005	rev A
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Scale	sheet 3 of 3
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11 of 18



Title: XC4000 Family BUFE Macro

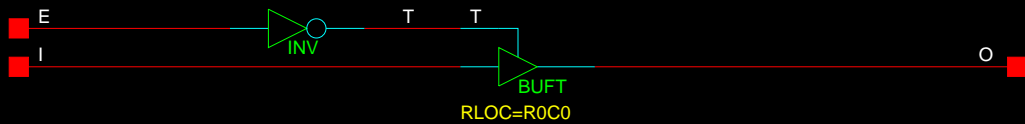
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Date: 3rd March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family BUFE Macro

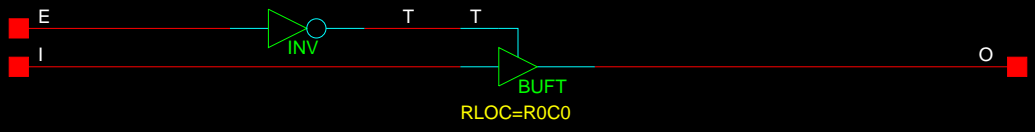
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Date: 3rd March 1993

Ver: 1

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Rev: A



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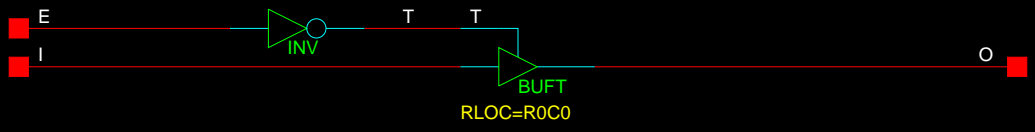
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Date: 3rd March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family BUFE Macro

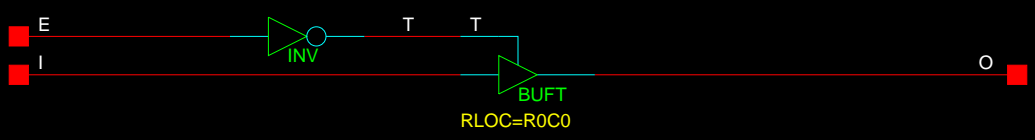
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Date: 3rd March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family BUFE Macro

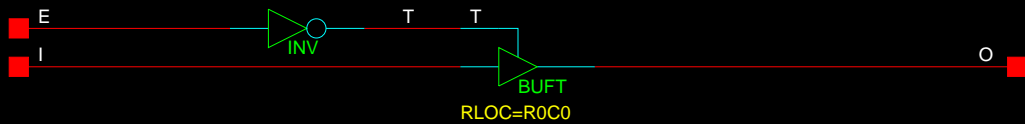
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Rev: A



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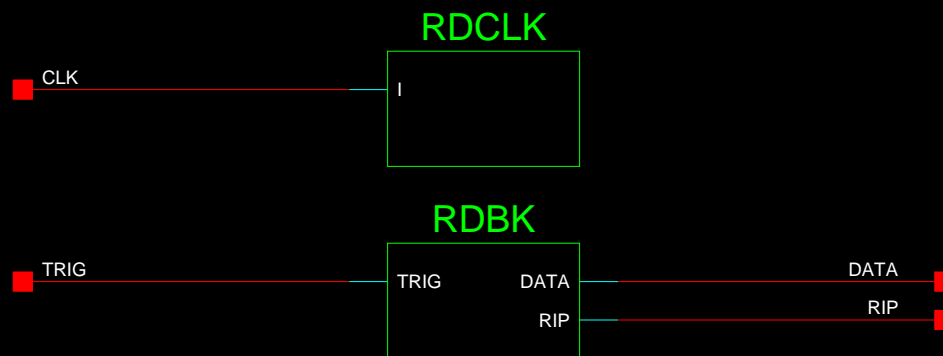
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Date: 3rd March 1993

Ver: 1

Sheet Size: A

Rev: A



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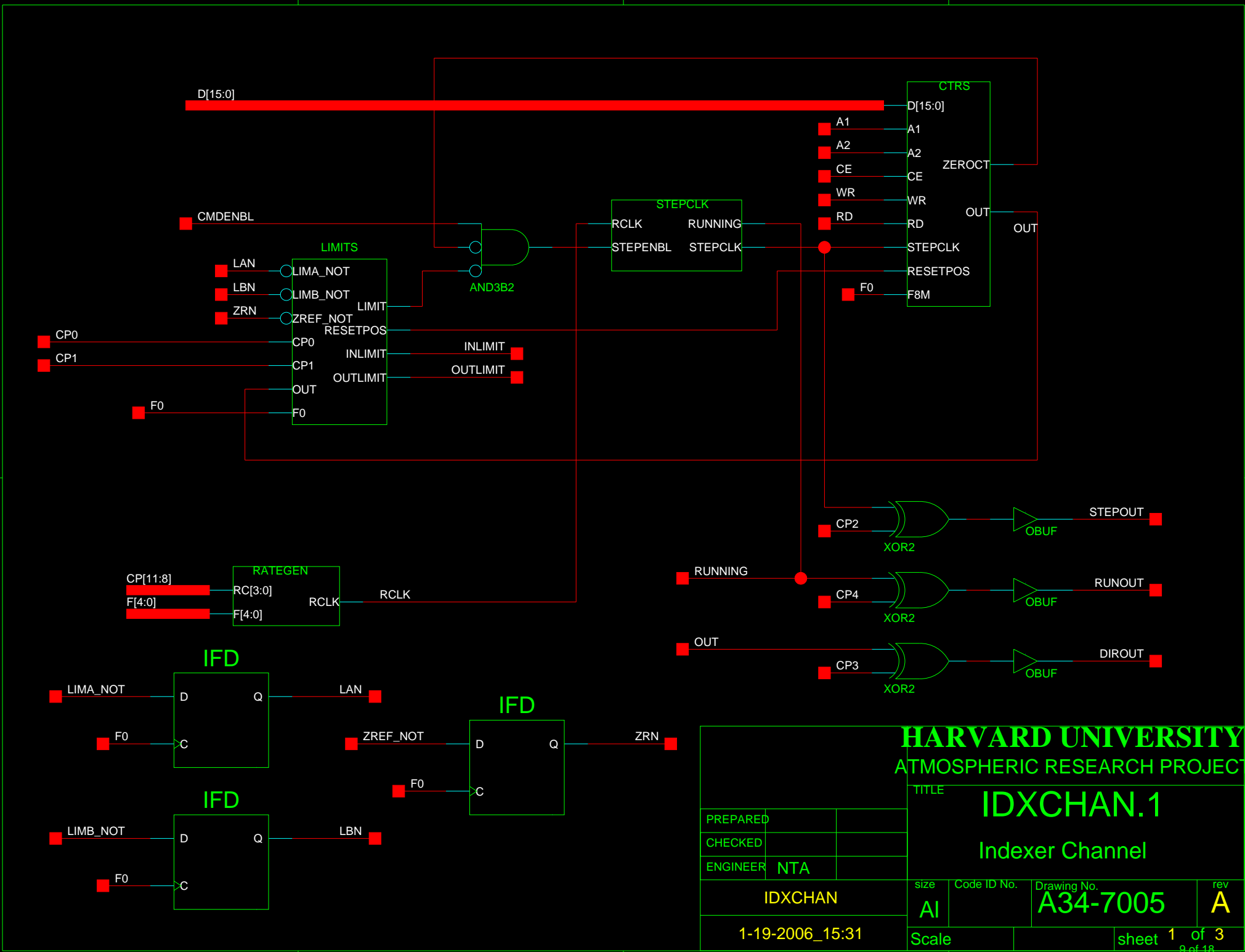
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Date: 29th March 1993

Ver: 1

Sheet Size: A

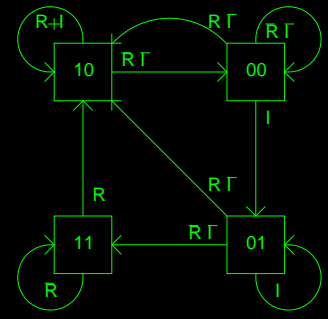
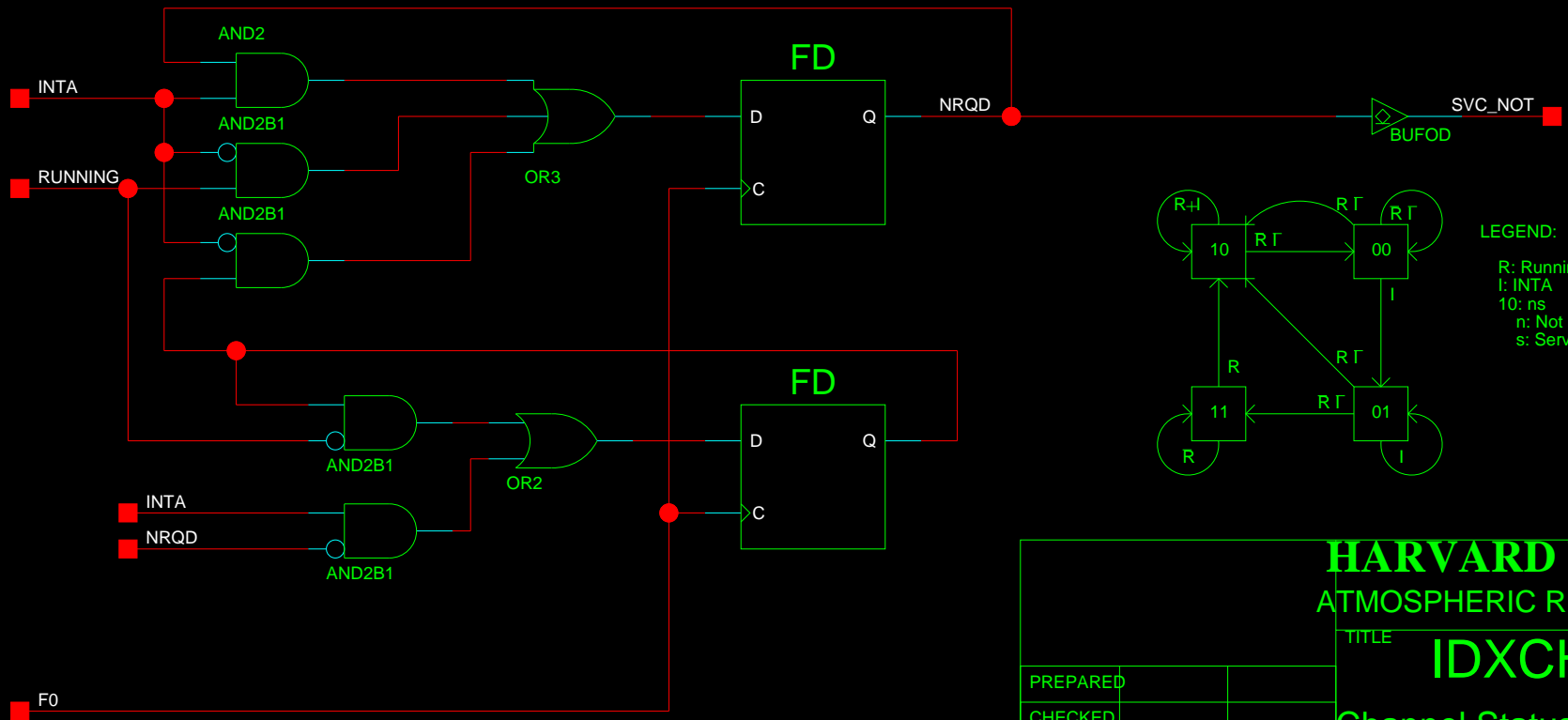
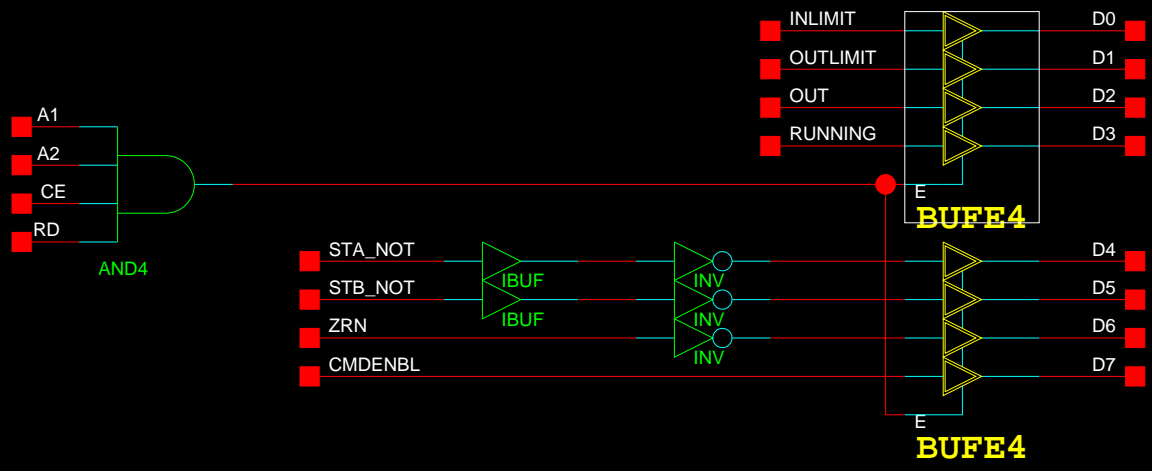
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TITLE			
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size	Code ID No.	Drawing No.	rev
AI		A34-7005	A
1-19-2006_15:31		sheet 1 of 3	9 of 18

PREPARED	
CHECKED	
ENGINEER	NTA
IDXCHAN	



LEGEND:
 R: Running
 I: INTA
 10: ns
 n: Not Requested (NRQD)
 s: Serviced

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 ATMOSPHERIC RESEARCH PROJECT

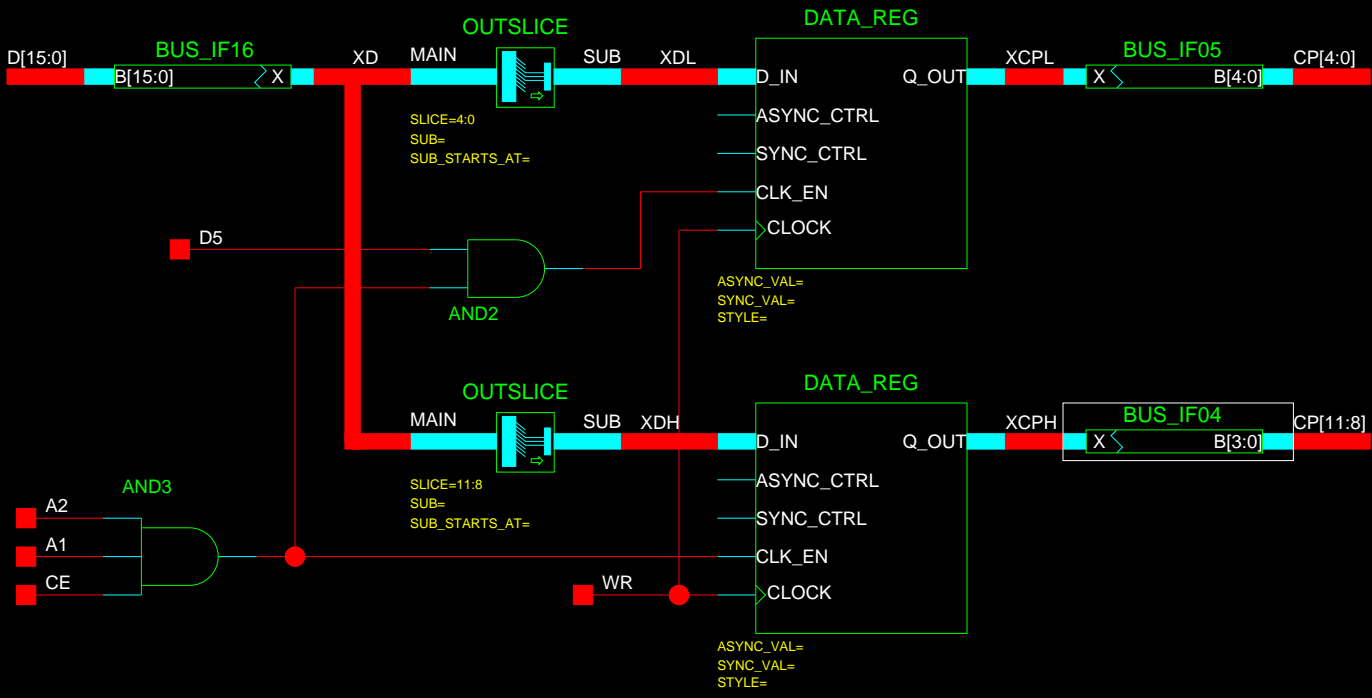
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Channel Status, Service Req.

PREPARED	
CHECKED	
ENGINEER	NTA

size	Code ID No.	Drawing No.	rev
AI		A34-7005	A
1-19-2006_15:32		sheet 2 of 3	10 of 18

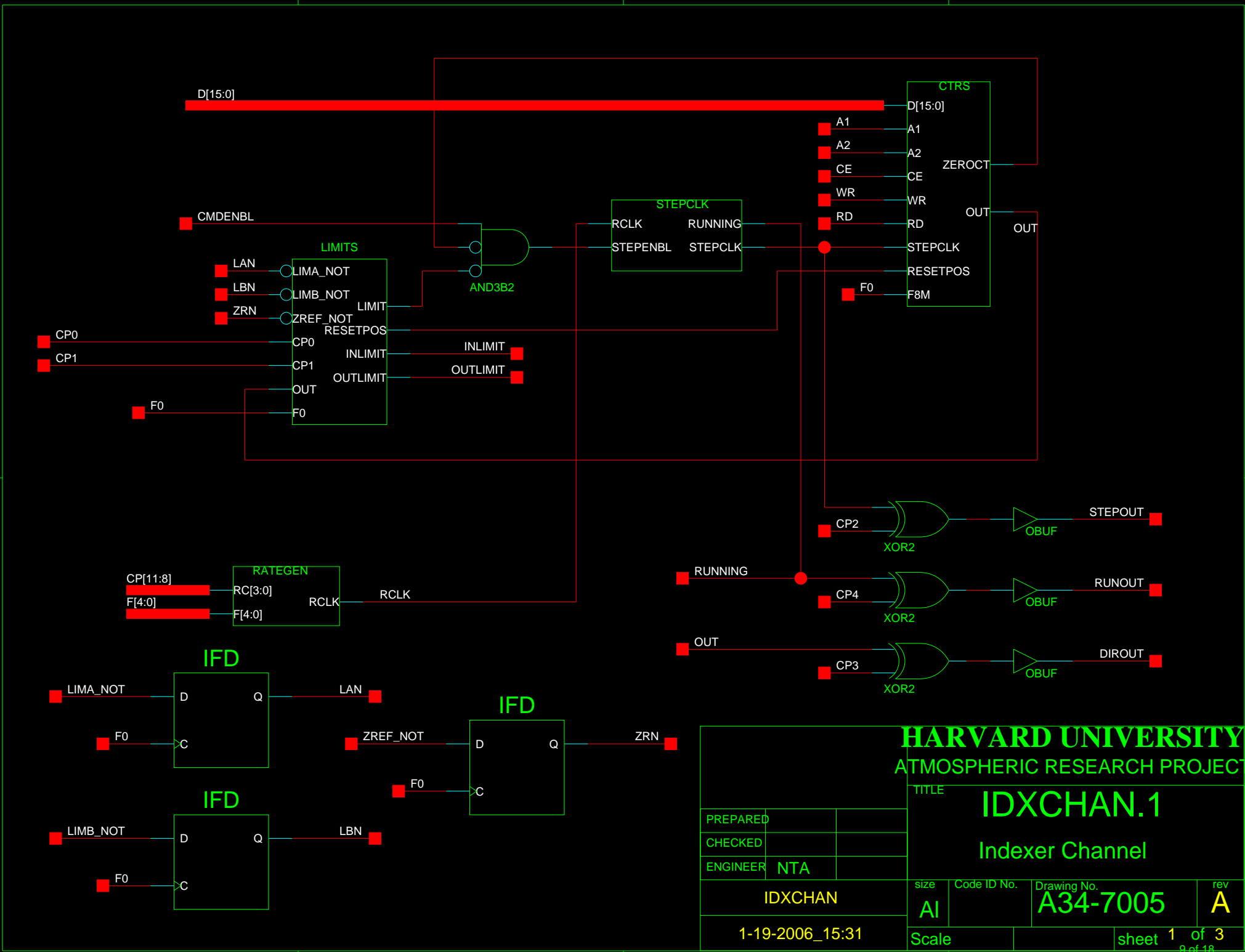
size	Code ID No.	Drawing No.	rev
AI		A34-7005	A
1-19-2006_15:32		sheet 2 of 3	10 of 18



HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

PREPARED	
CHECKED	
ENGINEER	NTA
IDXCHAN	
1-19-2006_15:33	

TITLE			
IDXCHAN.3			
Channel Configuration Port			
size	Code ID No.	Drawing No.	rev
A		A34-7005	A
Scale		sheet 3 of 3	
11 of 18			



HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

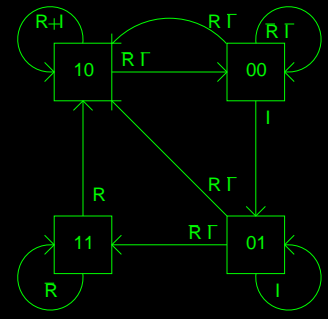
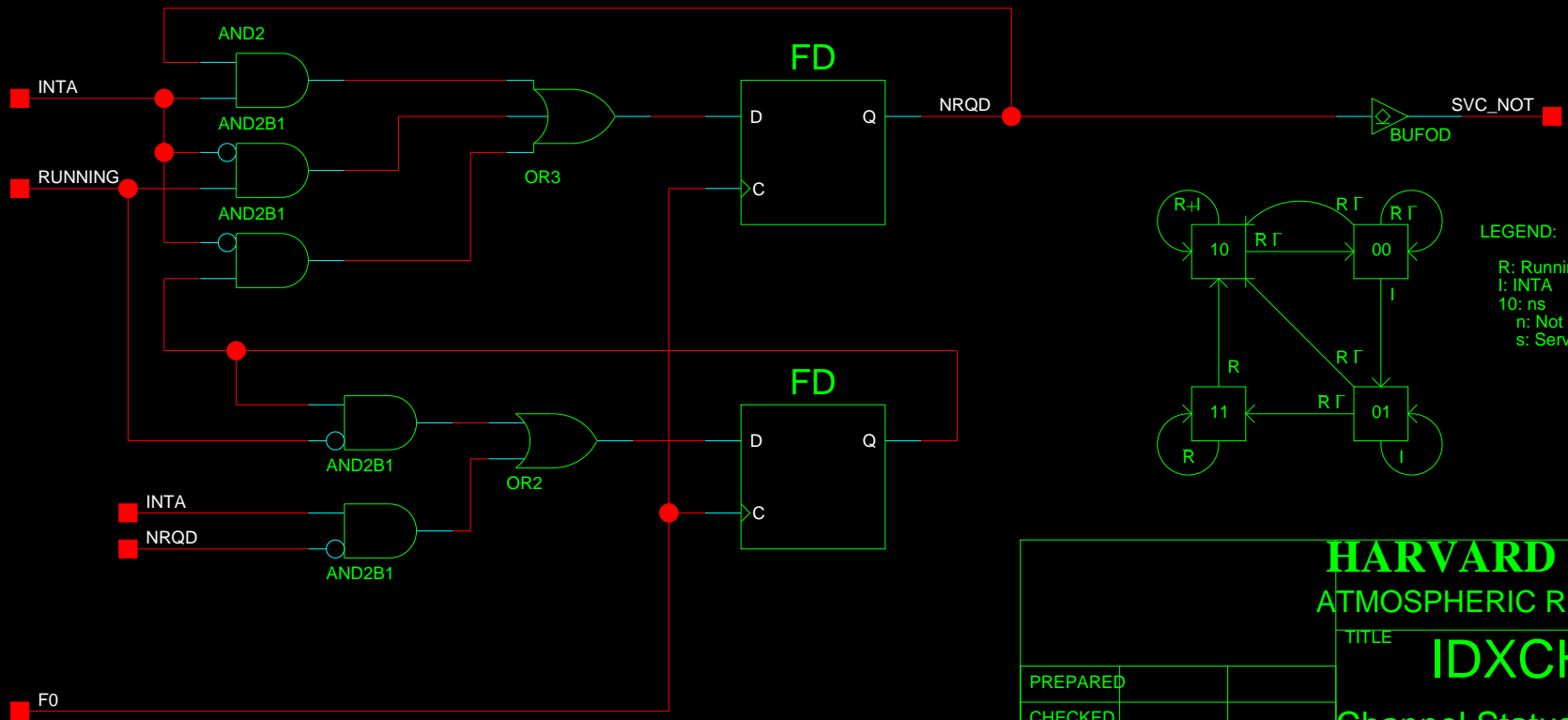
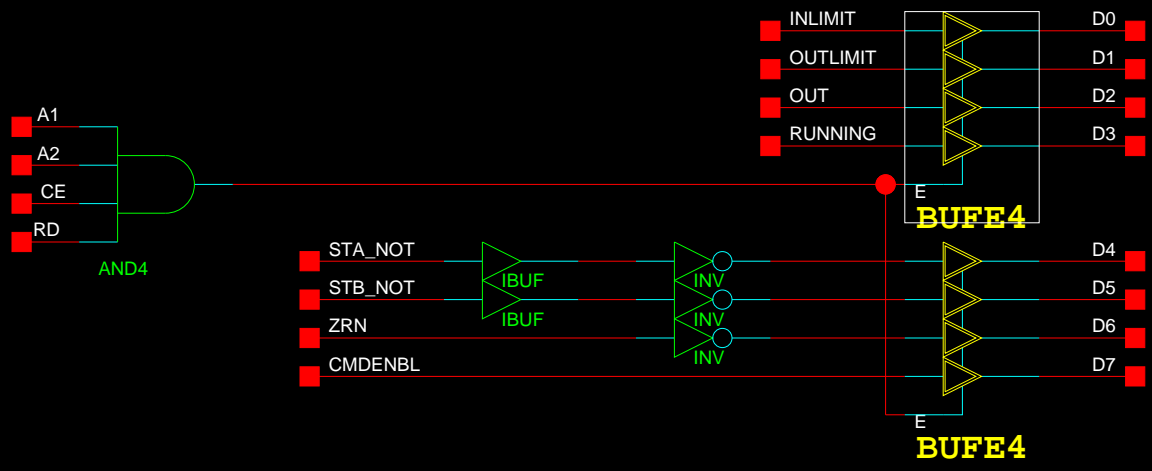
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IDXCHAN.1

Indexer Channel

PREPARED	
CHECKED	
ENGINEER	NTA

size	Code ID No.	Drawing No.	rev
AI		A34-7005	A
1-19-2006_15:31		sheet 1 of 3	9 of 18

size	Code ID No.	Drawing No.	rev
AI		A34-7005	A
1-19-2006_15:31		sheet 1 of 3	9 of 18



LEGEND:
 R: Running
 I: INTA
 10: ns
 n: Not Requested (NRQD)
 s: Serviced

HARVARD UNIVERSITY
 ATMOSPHERIC RESEARCH PROJECT

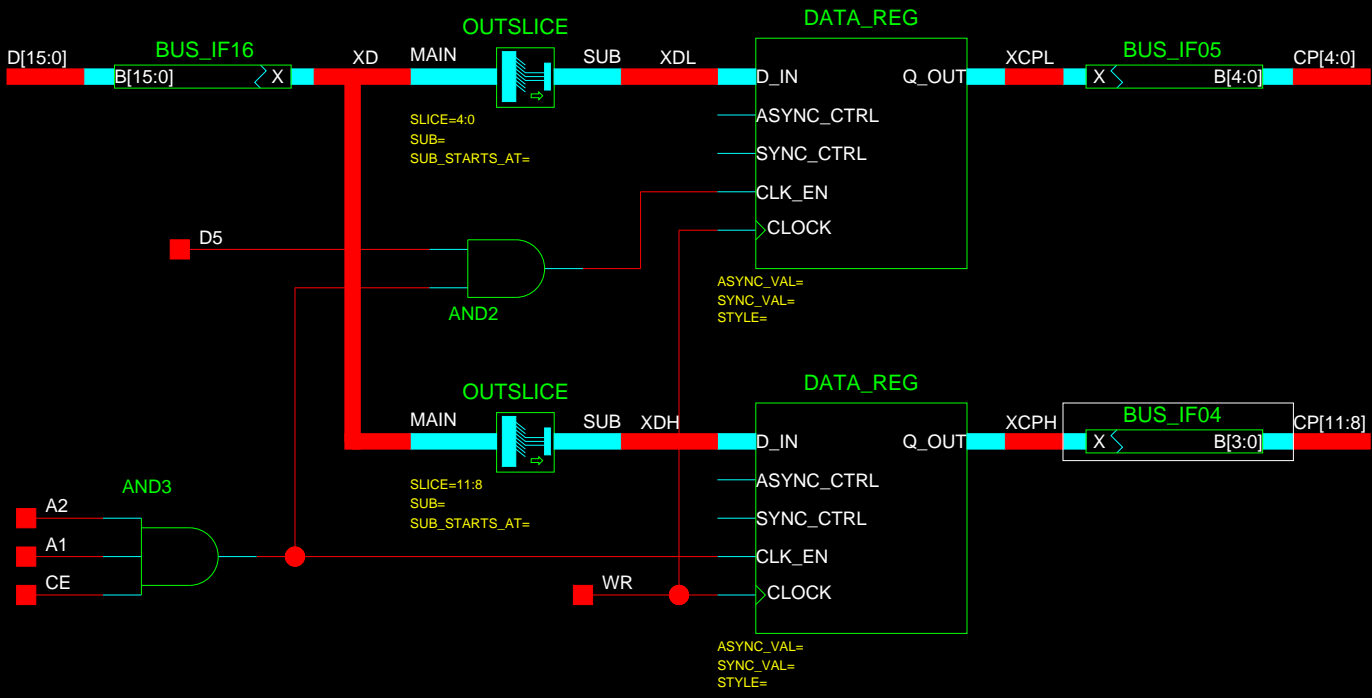
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Channel Status, Service Req.

PREPARED	
CHECKED	
ENGINEER	NTA

size	Code ID No.	Drawing No.	rev
AI		A34-7005	A
1-19-2006_15:32		sheet 2 of 3	10 of 18

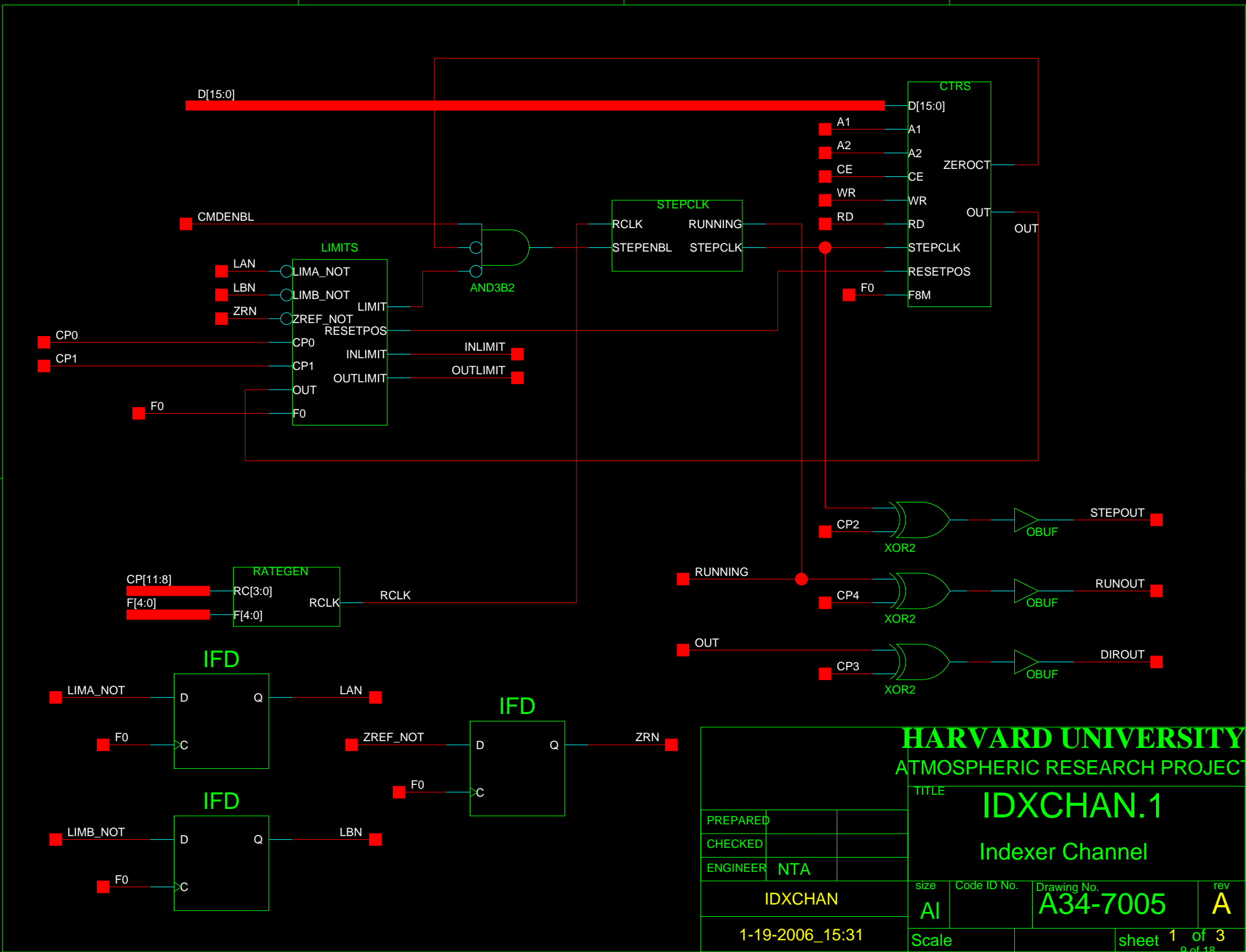
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AI		A34-7005	A
1-19-2006_15:32		sheet 2 of 3	10 of 18



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ATMOSPHERIC RESEARCH PROJECT

PREPARED	
CHECKED	
ENGINEER	NTA
IDXCHAN	
1-19-2006_15:33	

TITLE			
IDXCHAN.3			
Channel Configuration Port			
size	Code ID No.	Drawing No.	rev
A		A34-7005	A
Scale		sheet 3 of 3	
11 of 18			



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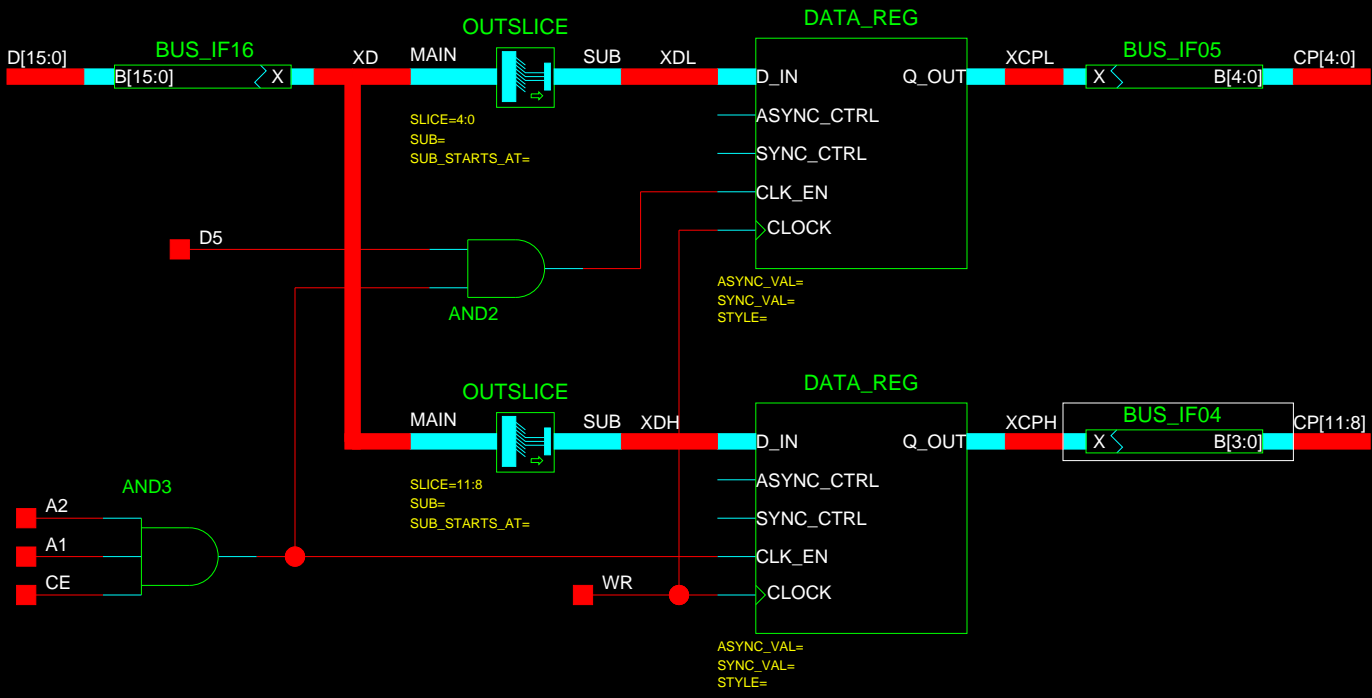
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Indexer Channel

PREPARED	
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size	Code ID No.	Drawing No.	rev
AI		A34-7005	A
1-19-2006_15:31		sheet 1 of 3	9 of 18

size	Code ID No.	Drawing No.	rev
AI		A34-7005	A
1-19-2006_15:31		sheet 1 of 3	9 of 18



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ATMOSPHERIC RESEARCH PROJECT

PREPARED	
CHECKED	
ENGINEER	NTA
IDXCHAN	
1-19-2006_15:33	

TITLE			
IDXCHAN.3			
Channel Configuration Port			
size	Code ID No.	Drawing No.	rev
A		A34-7005	A
Scale		sheet 3 of 3	
11 of 18			



Title: XBLOX Family BUS_IF06

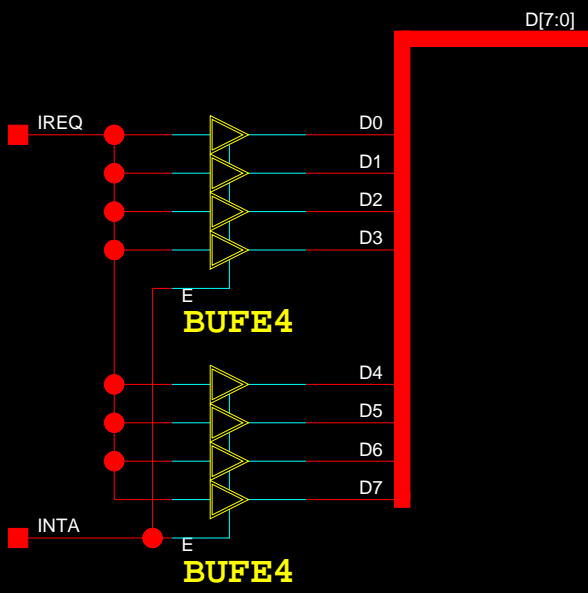
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Date: 7/21/93

Ver: 1

Sheet Size: A

Rev: A



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ATMOSPHERIC RESEARCH PROJECT

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 Interrupt Acknowledge

PREPARED		
CHECKED		
ENGINEER	NTA	

IREQBIT
 1-19-2006_15:40

size A	Code ID No.	Drawing No. A34-7005	rev A
Scale		sheet 1 of 1	8 of 18

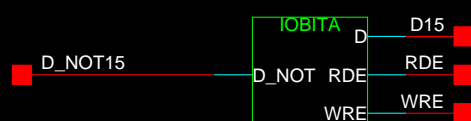
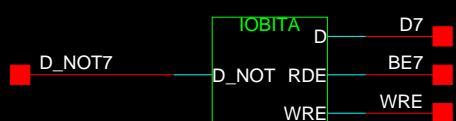
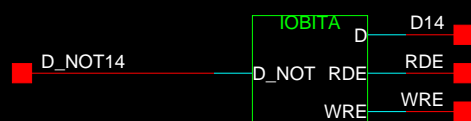
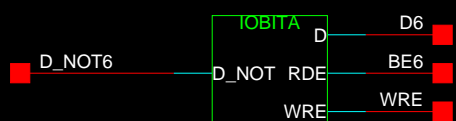
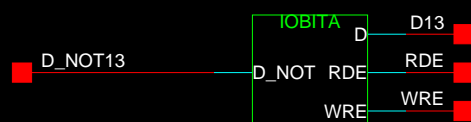
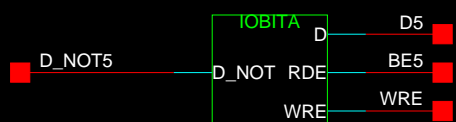
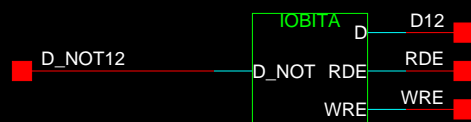
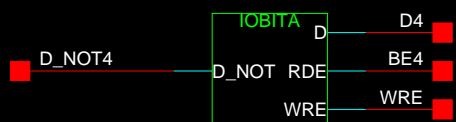
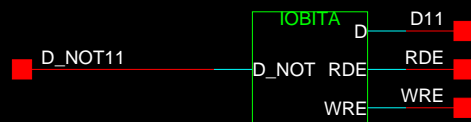
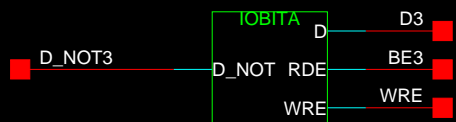
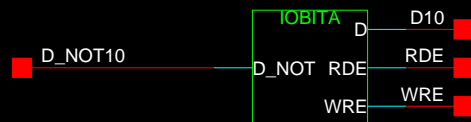
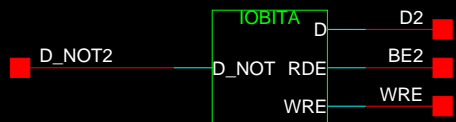
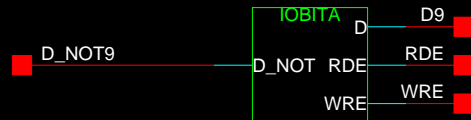
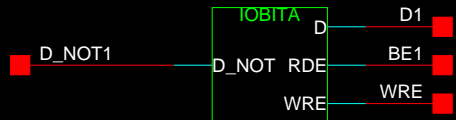
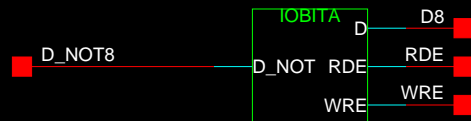
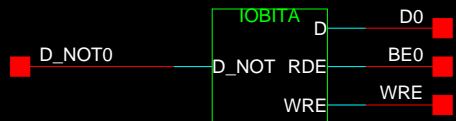
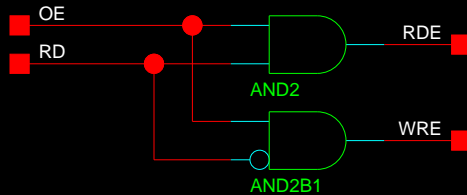
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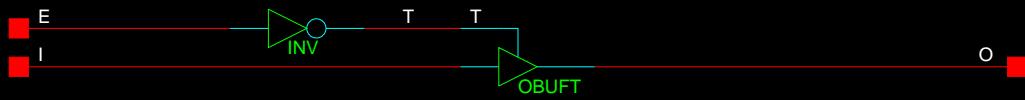


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TITLE
SDBIF.1
Subbus Data Bus Interface

PREPARED	
CHECKED	
ENGINEER	NTA

SDBIF	size A	Code ID No.	Drawing No. A34-7005	rev A
1-19-2006_15:17	Scale		sheet 16 of 181	



Title: XC4000 Family OBUFE Macro

Comments: 3-State Output Buffer with Active High Enable

Date: 22nd February 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XBLOX Family BUS_IF06

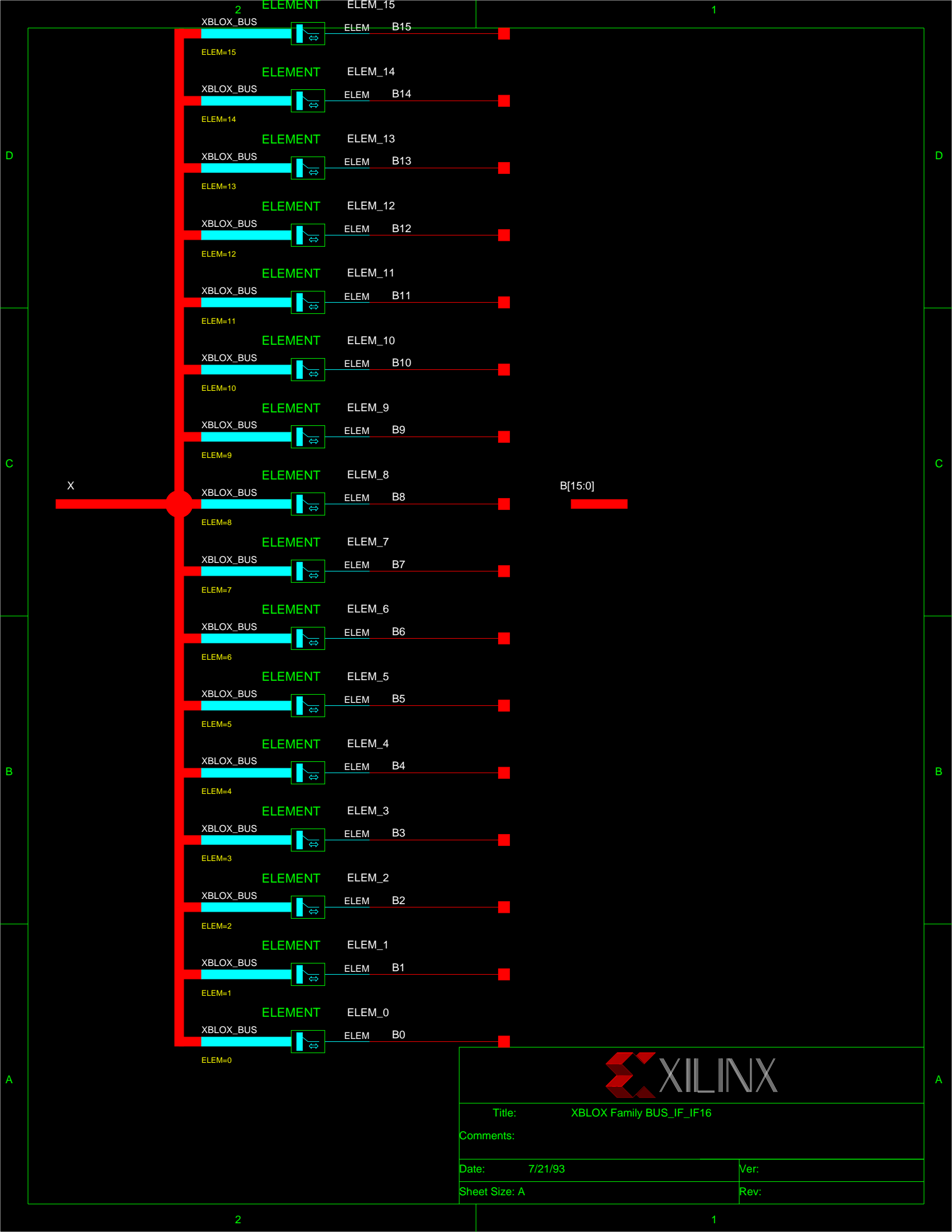
Comments:

Date: 7/21/93

Ver: 1

Sheet Size: A

Rev: A



Title: XBLOX Family BUS_IF_IF16	
Comments:	
Date: 7/21/93	Ver:
Sheet Size: A	Rev:



Title: XBLOX Family BUS_IF08

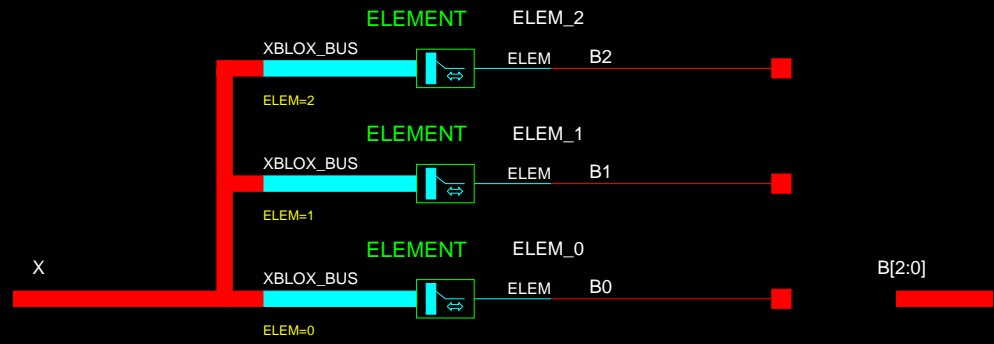
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Date: 7/21/93

Ver: 1

Sheet Size: A

Rev: A



Title: XBLOX Family BUS_IF03

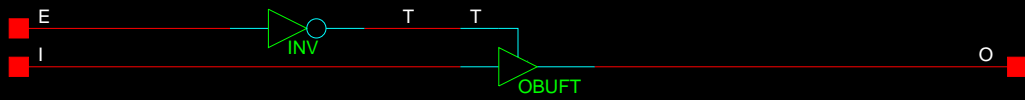
Comments:

Date: 7/21/93

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family OBUFE Macro

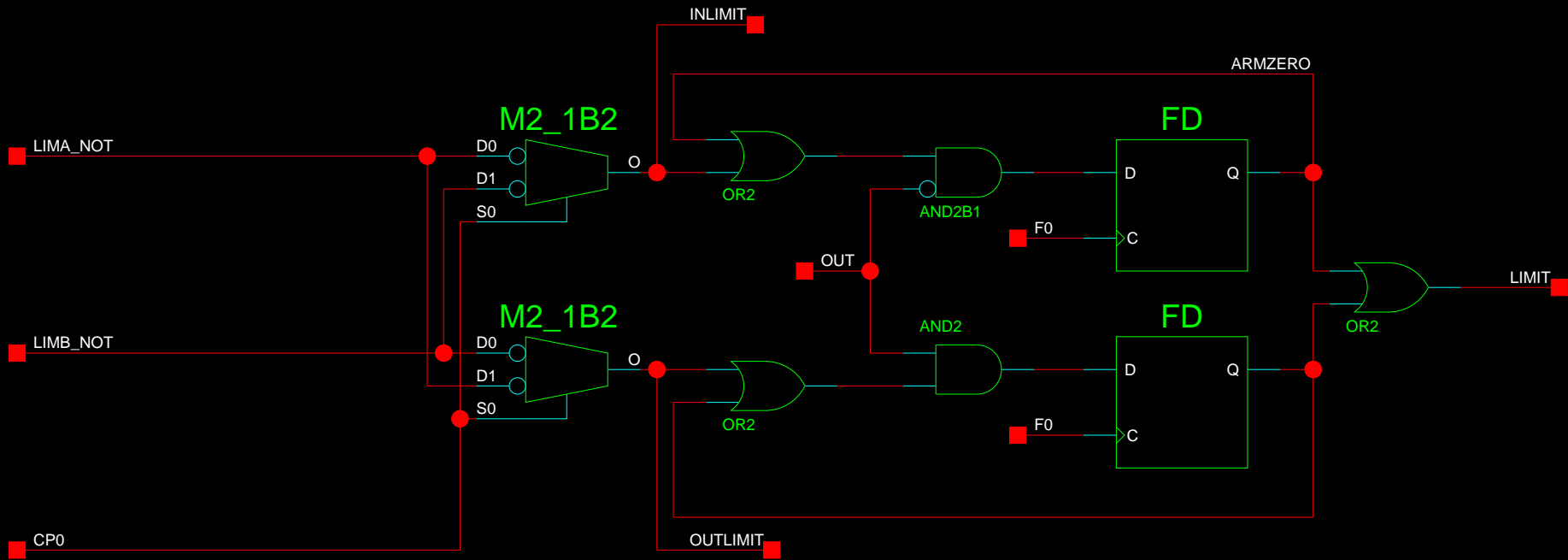
Comments: 3-State Output Buffer with Active High Enable

Date: 22nd February 1993

Ver: 1

Sheet Size: A

Rev: A



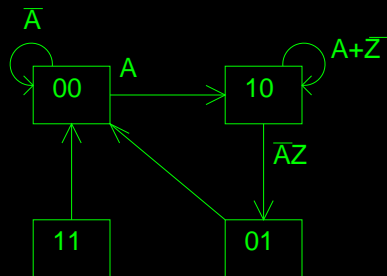
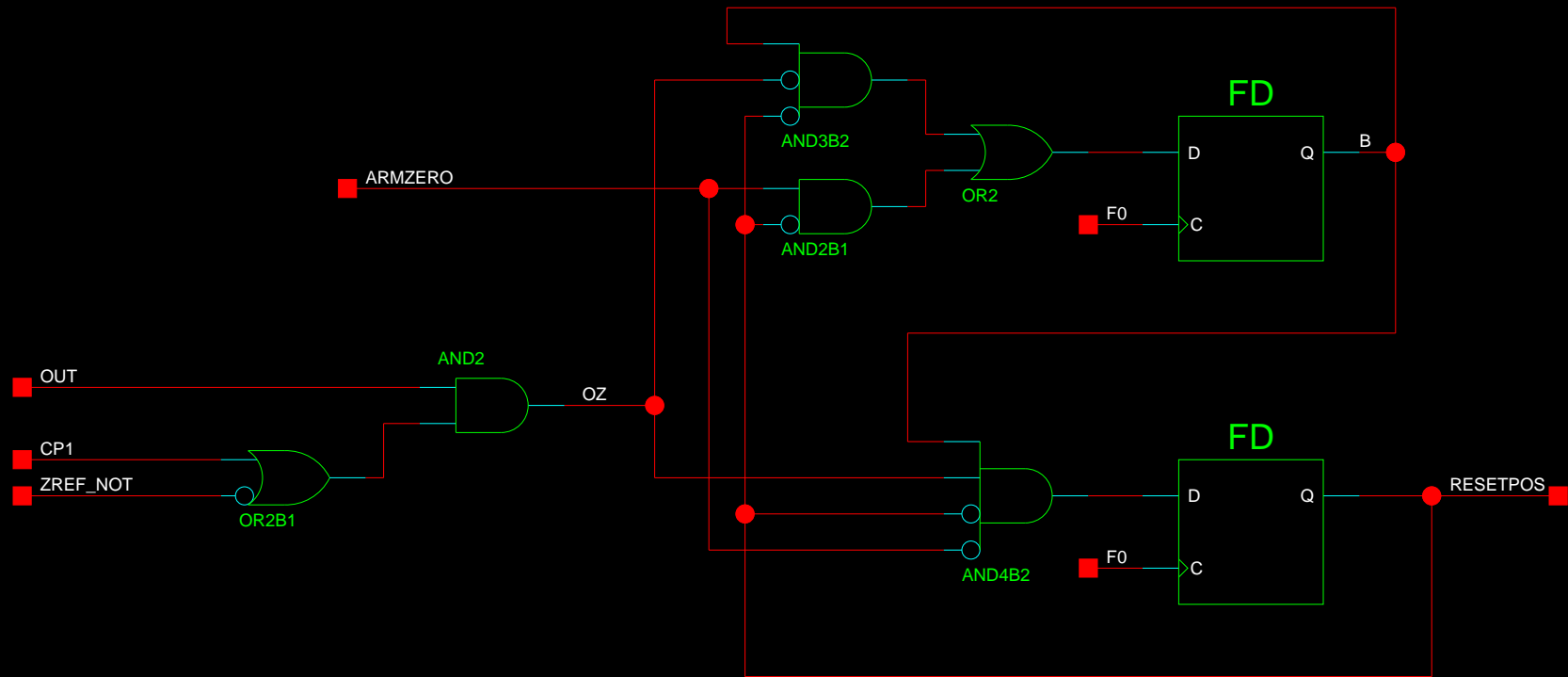
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ATMOSPHERIC RESEARCH PROJECT

TITLE
LIMITS.1

Indexer Limit Switches

PREPARED		
CHECKED		
ENGINEER	NTA	

LIMITS	size	Code ID No.	Drawing No.	rev
	AI		A34-7005	A
1-19-2006_15:35	Scale		sheet	1 of 12



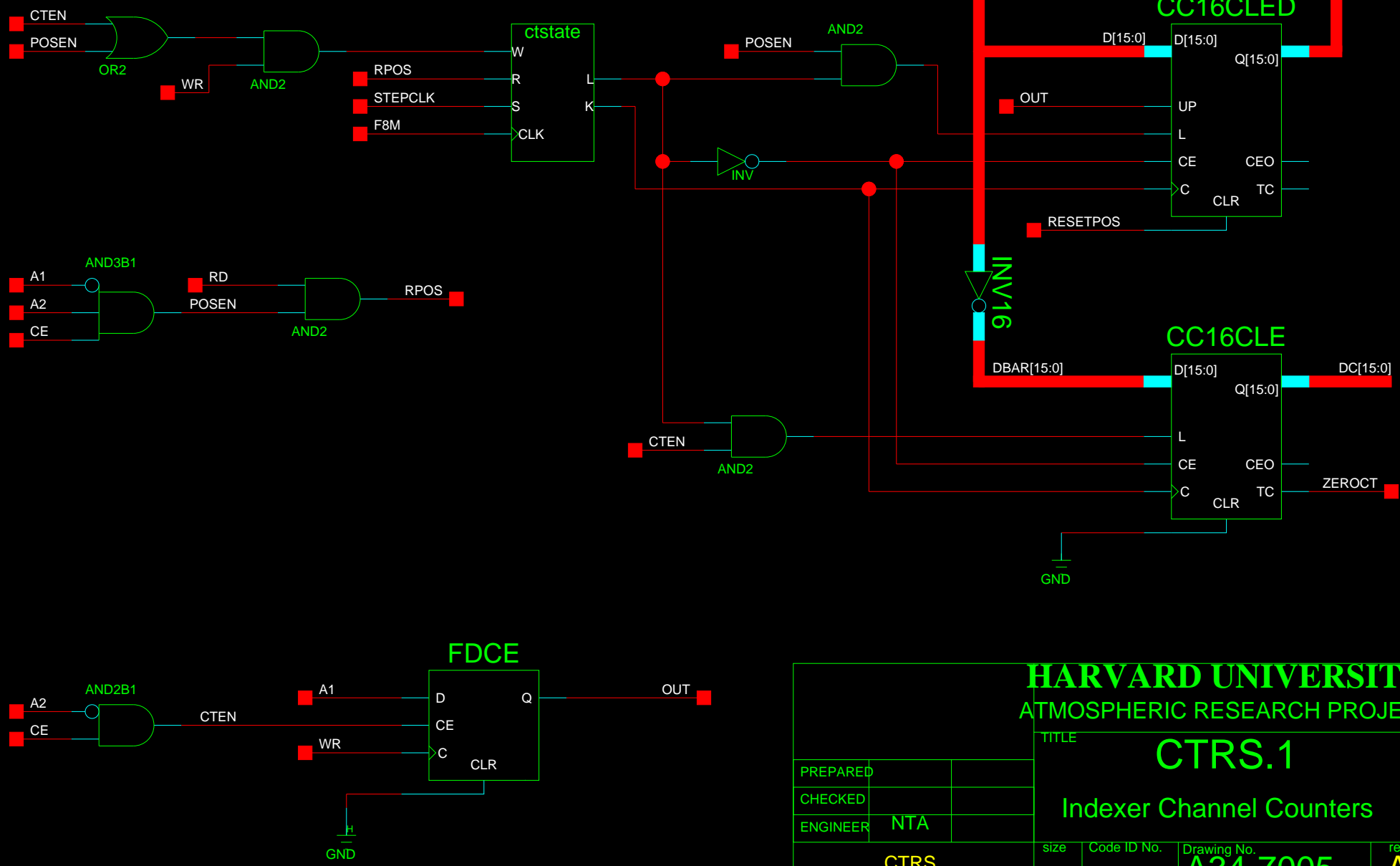
A: ARMZERO Input
 Z: OZ Qualified Zeroref
 00
 — RESETPOS Output
 — B "Zero Armed" State

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 ATMOSPHERIC RESEARCH PROJECT

TITLE
LIMITS.2
 Indexer Limit Switches

PREPARED
 CHECKED
 ENGINEER NTA

LIMITS	size	Code ID No.	Drawing No.	rev
	AI		A34-7005	A
1-19-2006_15:36	Scale	sheet 2 of 2		13 of 18



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CTRS.1

Indexer Channel Counters

PREPARED	
CHECKED	
ENGINEER	NTA

CTRS	size	Code ID No.	Drawing No.	rev
1-19-2006_15:37	AI		A34-7005	A
	Scale		sheet 1 of 1	15 of 18

A

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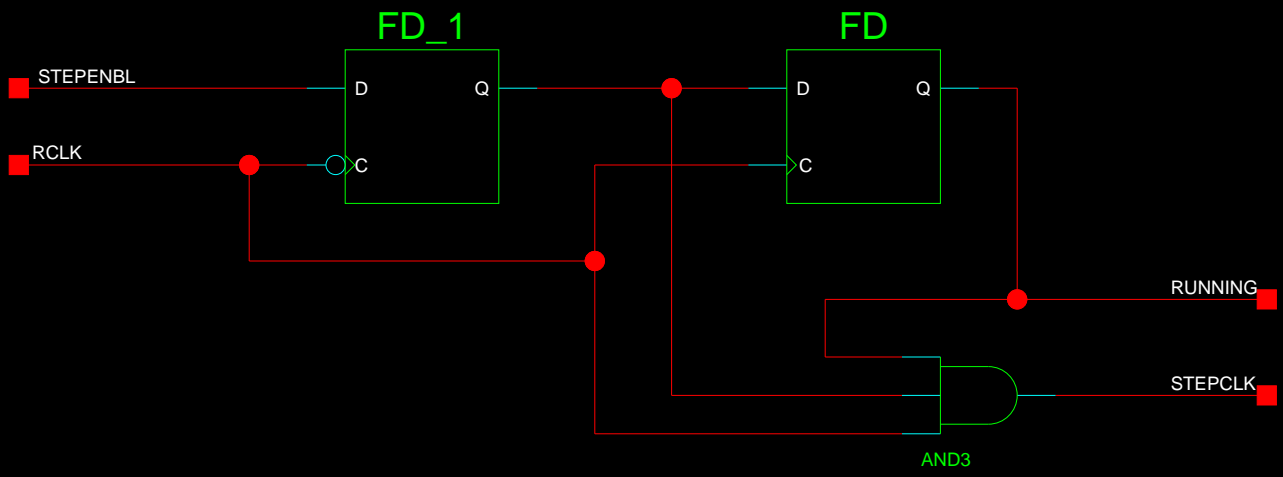
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5



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TITLE **STEPCLK.1**

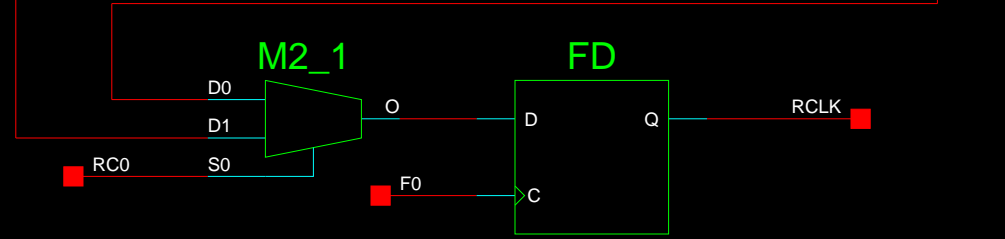
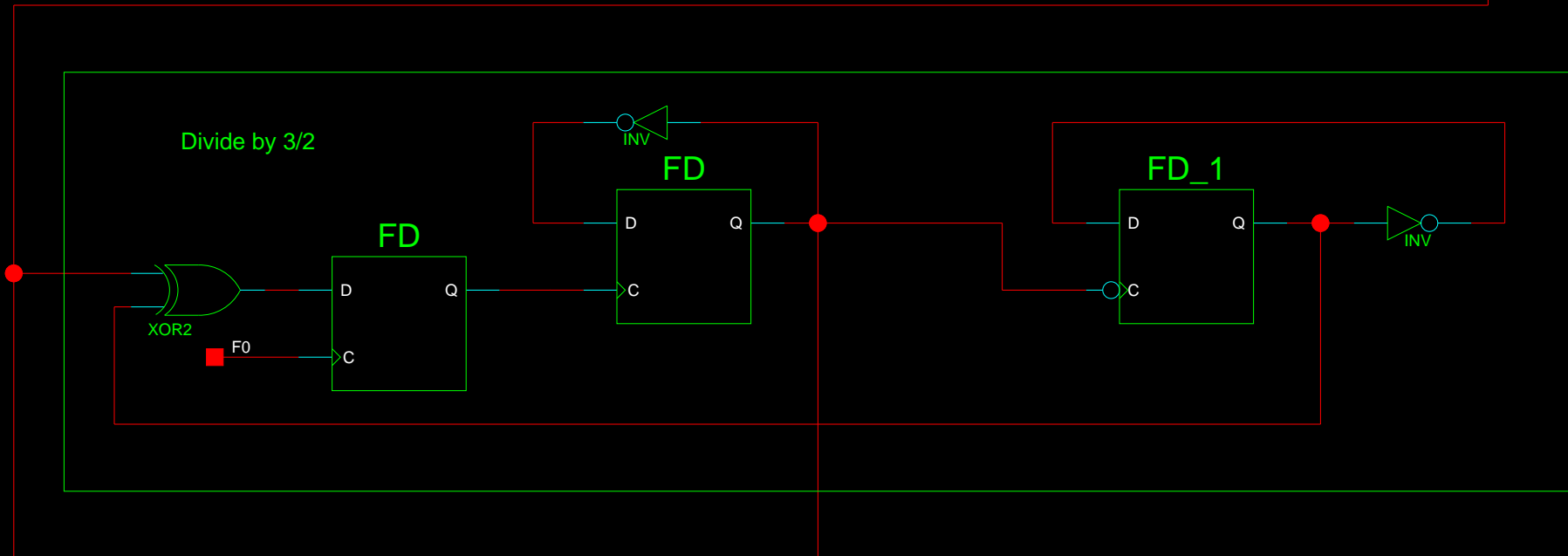
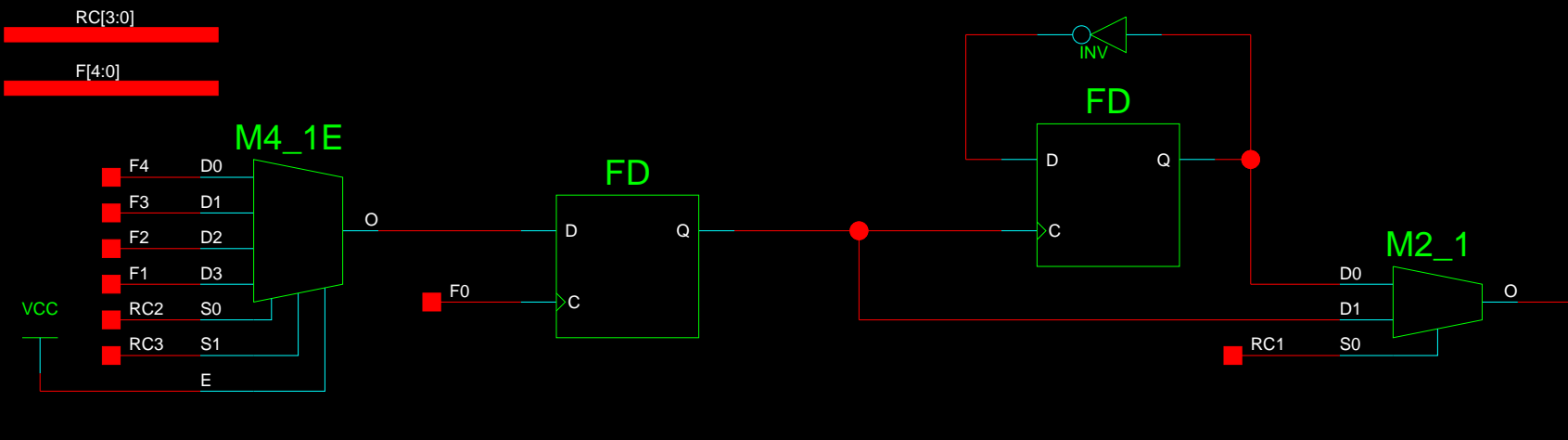
Indexer Channel Step Gen

PREPARED		
CHECKED		
ENGINEER	NTA	
STEPCLK		
1-19-2006_15:35		

size	Code ID No.	Drawing No.	rev
A		A34-7005	A
Scale		sheet	1 of 1

A

B



HARVARD UNIVERSITY
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TITLE **RATEGEN.1**
Channel Rate Selection

PREPARED			
CHECKED			
ENGINEER	NTA		
RATEGEN		size AI	Code ID No.
1-19-2006_15:37		Scale	Drawing No. A34-7005
			rev A
			sheet 1 of 1 <small>18 of 18</small>

4

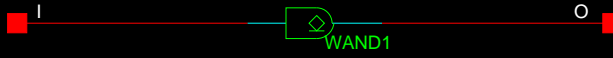
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B

B



A

A



Title: XC4000 Family BUFOD Macro

Comments: Open-Drain Buffer

Date: 6th April 1993

Ver: 1

Sheet Size: A

Rev: A

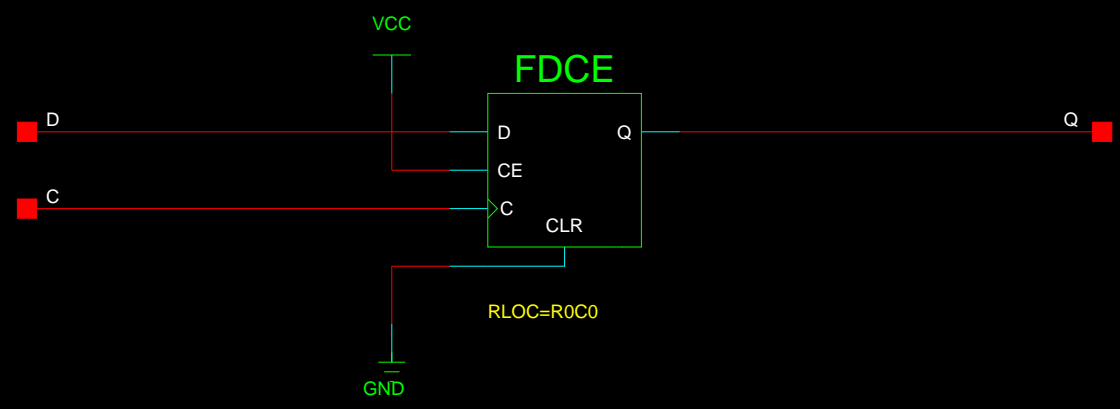
drawn by KS
Copyright (c) 1993, Xilinx Inc.

4

3

2

1



Title: XC4000 Family FD Macro

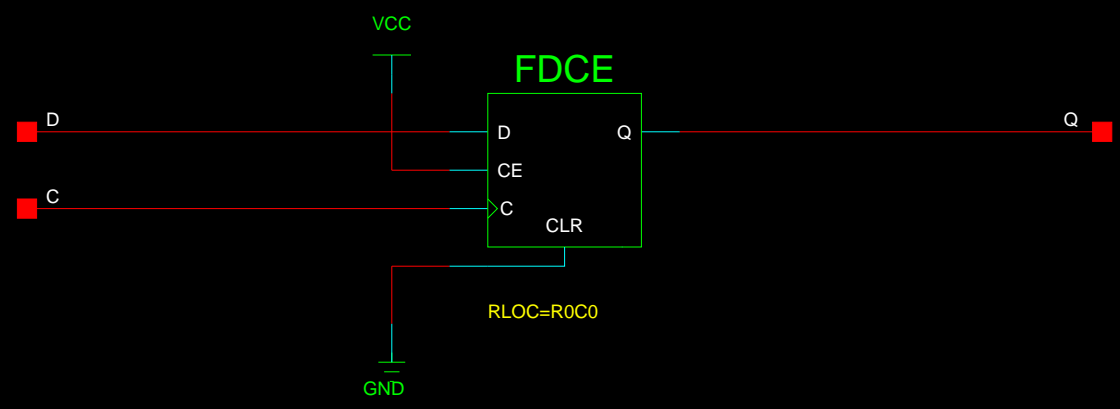
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Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

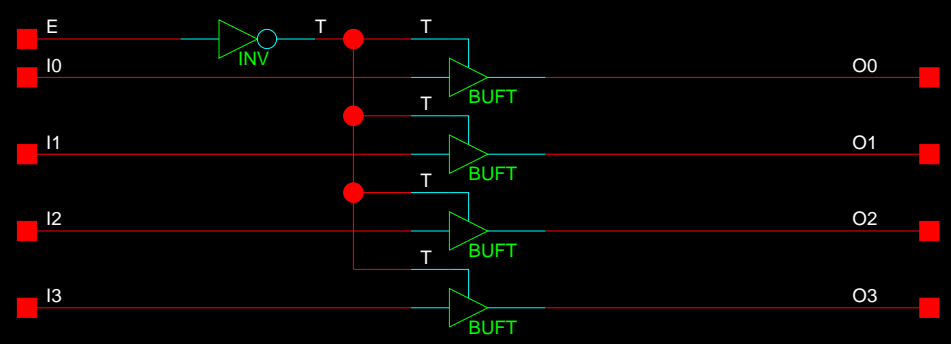
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family BUFE4 Macro

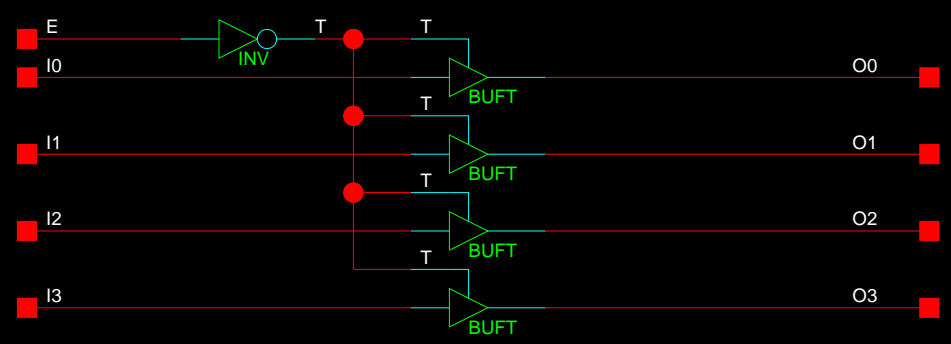
Comments: 4-Bit 3-State Buffer
w/ an Active High Enable

Date: 23rd February 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family BUFE4 Macro

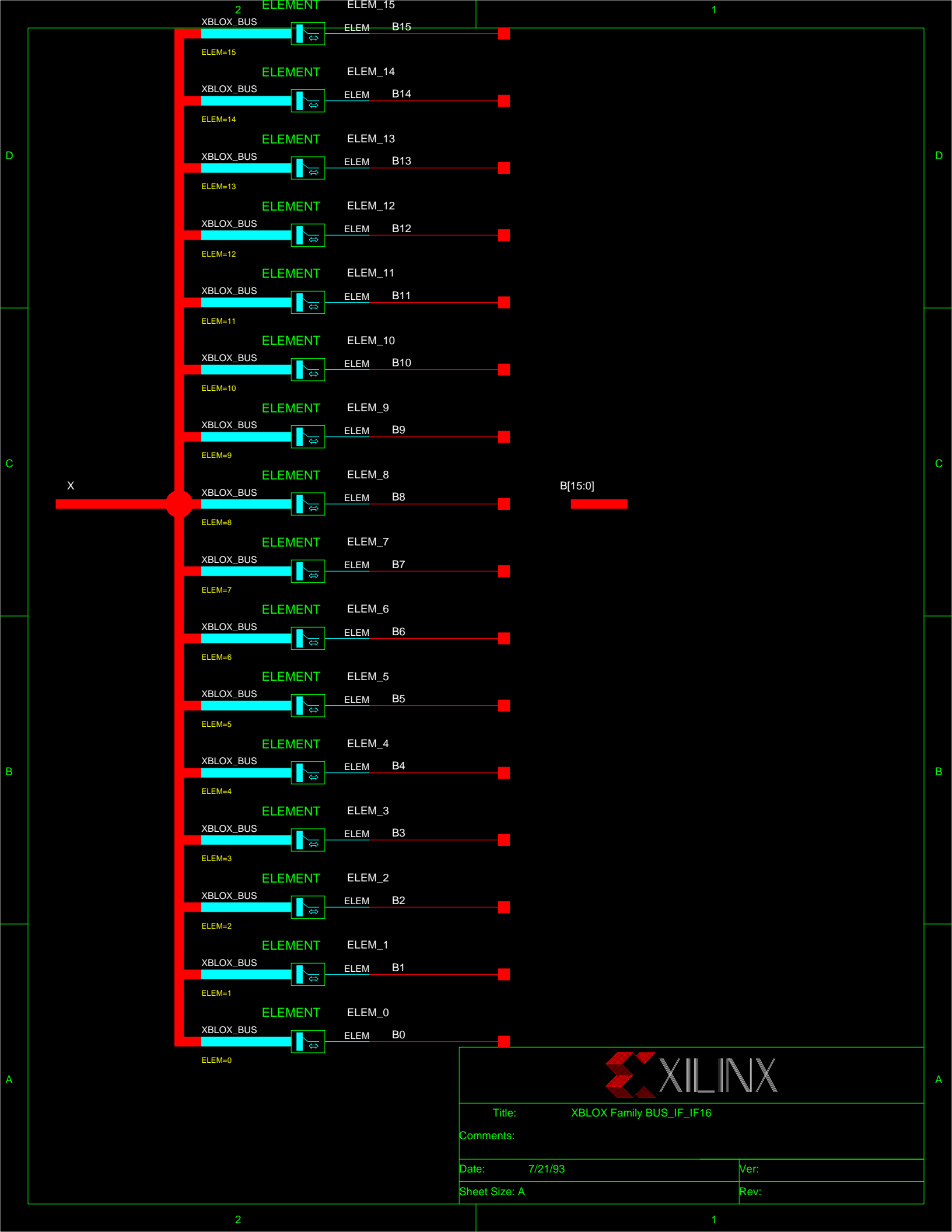
Comments: 4-Bit 3-State Buffer
w/ an Active High Enable

Date: 23rd February 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XBLOX Family BUS_IF_IF16	
Comments:	
Date: 7/21/93	Ver:
Sheet Size: A	Rev:



Title: XBLOX Family BUS_IF05

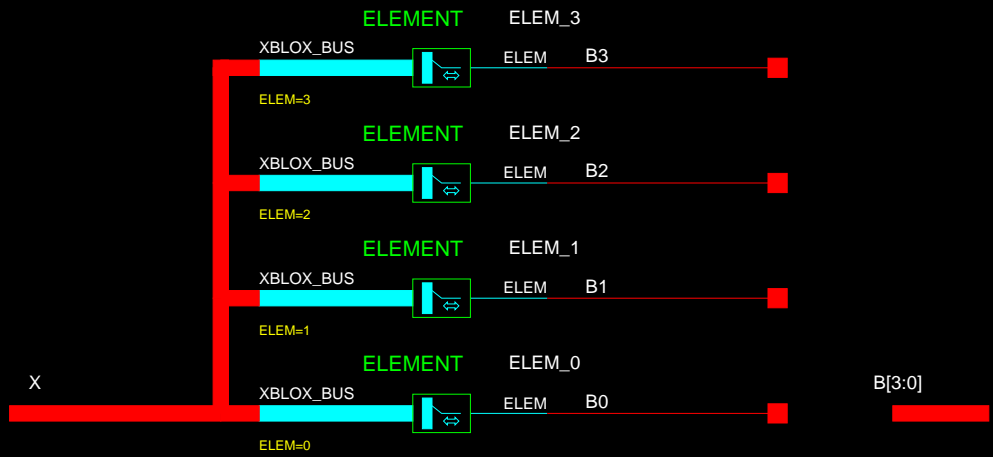
Comments:

Date: 7/21/93

Ver: 1

Sheet Size: A

Rev: A



Title: XBLOX Family BUS_IF04

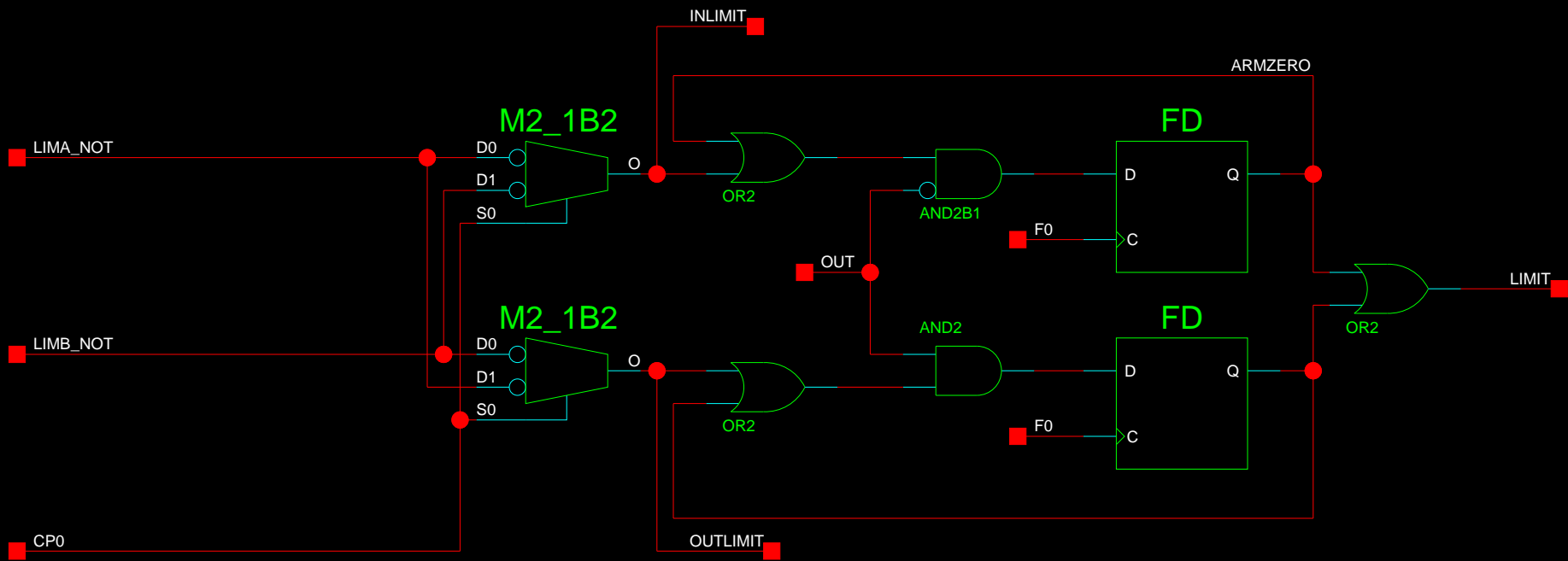
Comments:

Date: 7/21/93

Ver: 1

Sheet Size: A

Rev: A



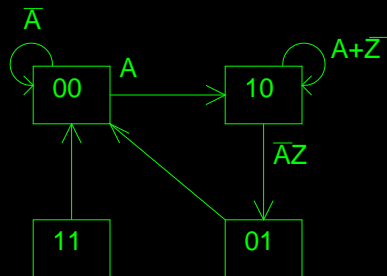
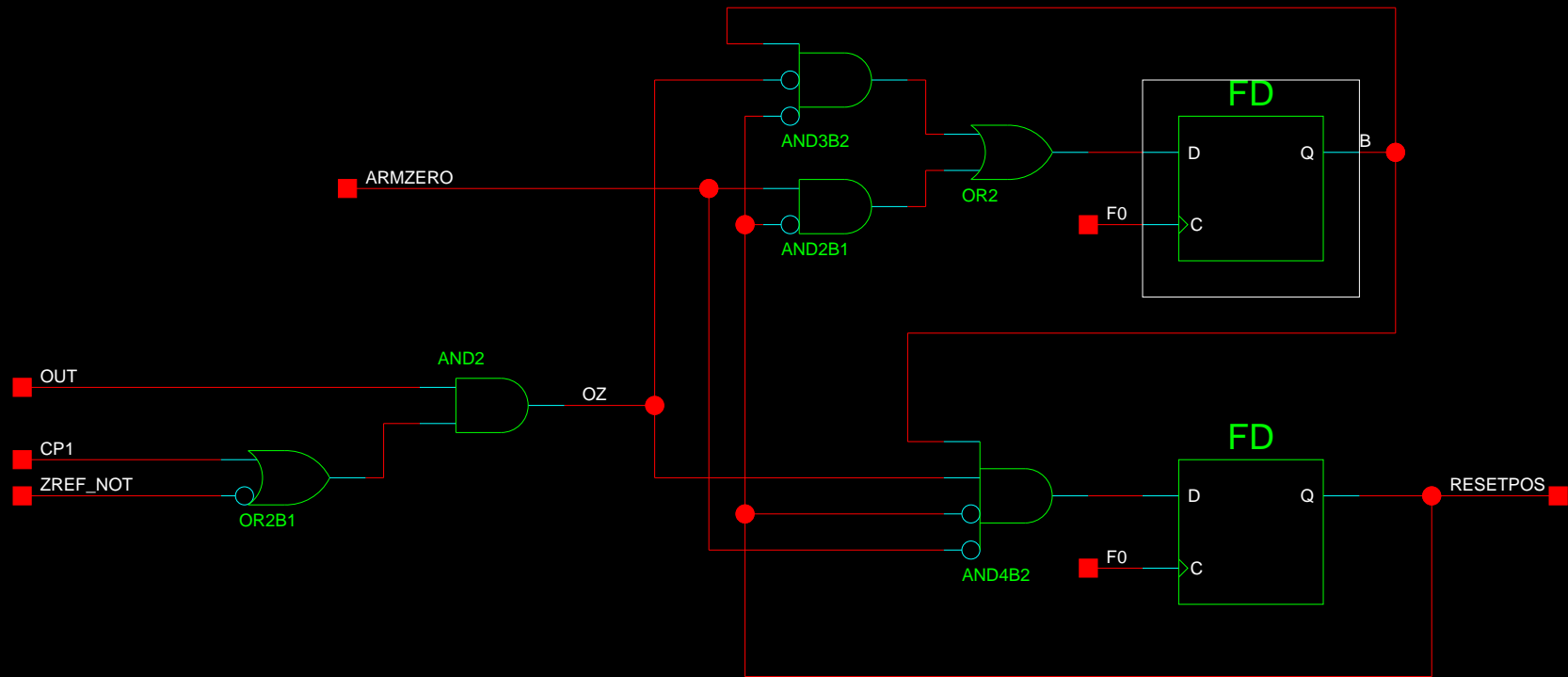
HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE
LIMITS.1

Indexer Limit Switches

PREPARED		
CHECKED		
ENGINEER	NTA	

LIMITS	size	Code ID No.	Drawing No.	rev
	AI		A34-7005	A
1-19-2006_15:35	Scale		sheet	1 of 12



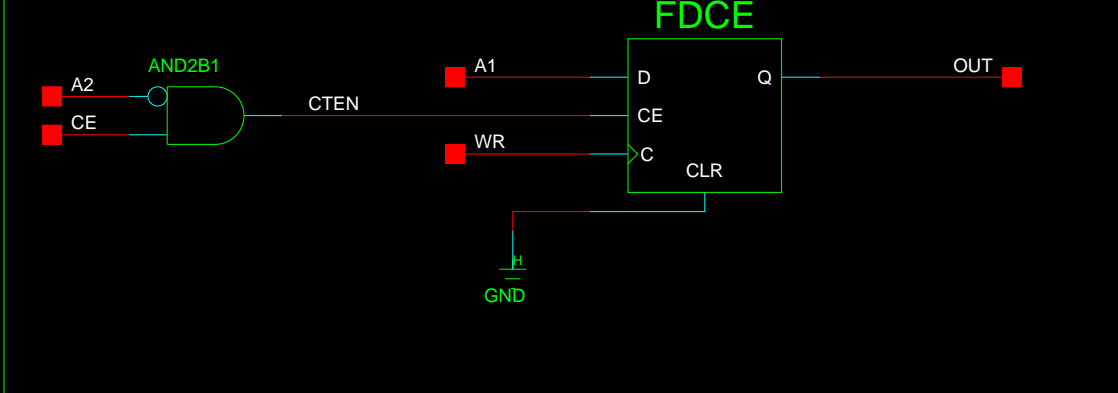
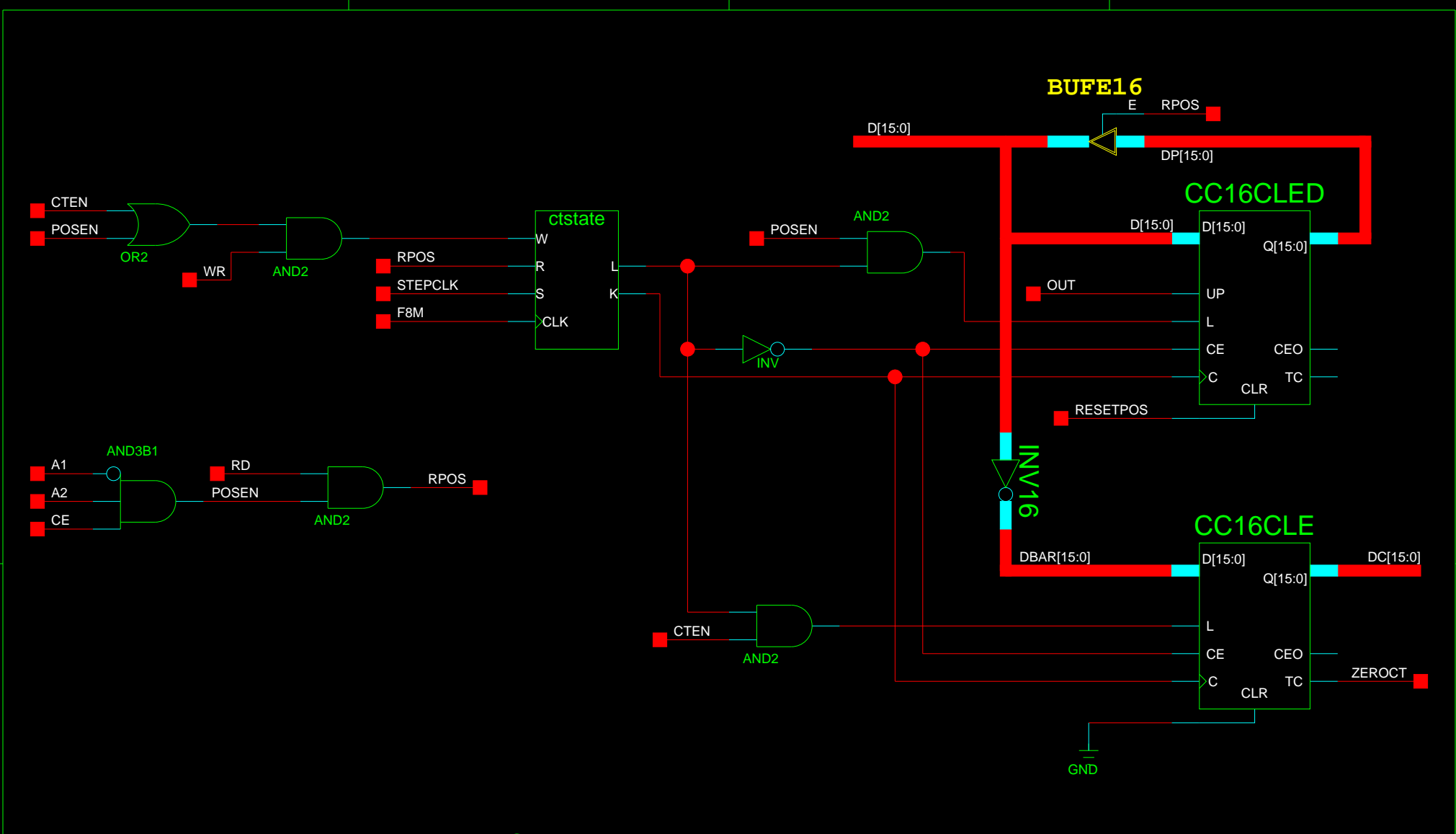
A: ARMZERO Input
 Z: OZ Qualified Zeroref
 00
 — RESETPOS Output
 — B "Zero Armed" State

HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE
LIMITS.2
 Indexer Limit Switches

PREPARED
 CHECKED
 ENGINEER **NTA**

LIMITS	size	Code ID No.	Drawing No.	rev
	AI		A34-7005	A
1-19-2006_15:36	Scale	sheet 2 of 2		13 of 18



HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

CTRS.1
Indexer Channel Counters

PREPARED		TITLE	
CHECKED		CTRS.1	
ENGINEER	NTA	Indexer Channel Counters	
CTRS		size	Code ID No.
		AI	A34-7005
1-19-2006_15:37		Drawing No.	A
		Scale	sheet 1 of 1
		15 of 18	

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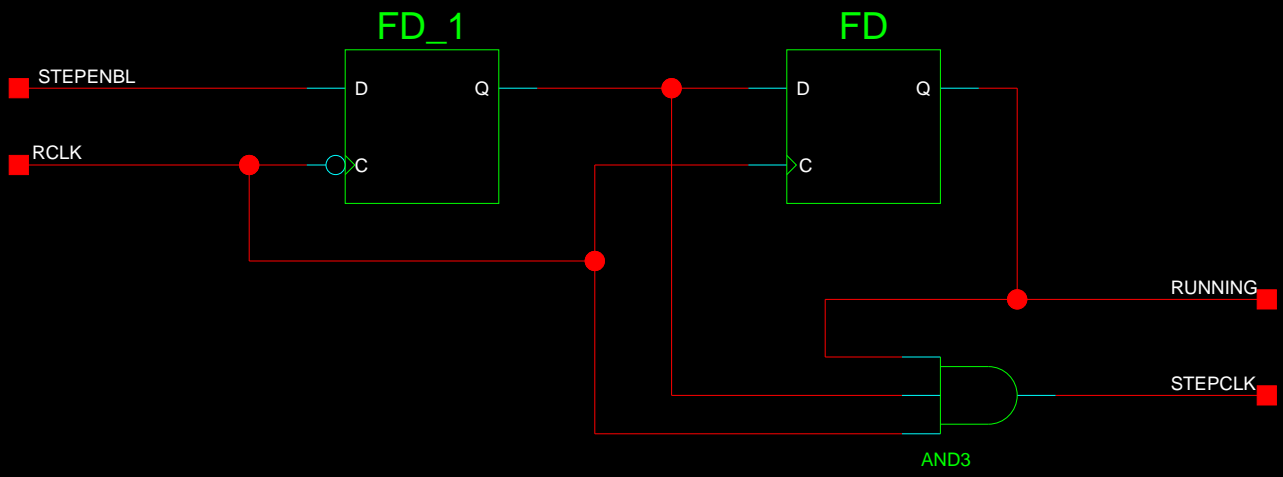
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HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE **STEPCLK.1**

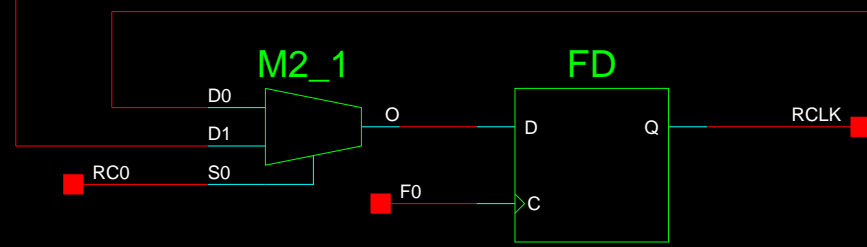
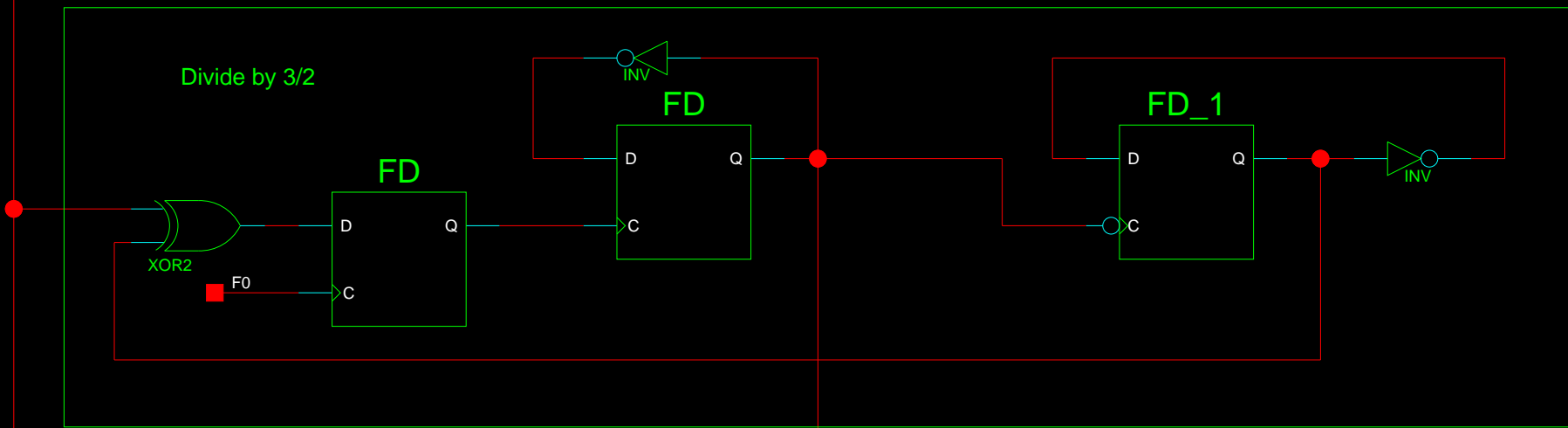
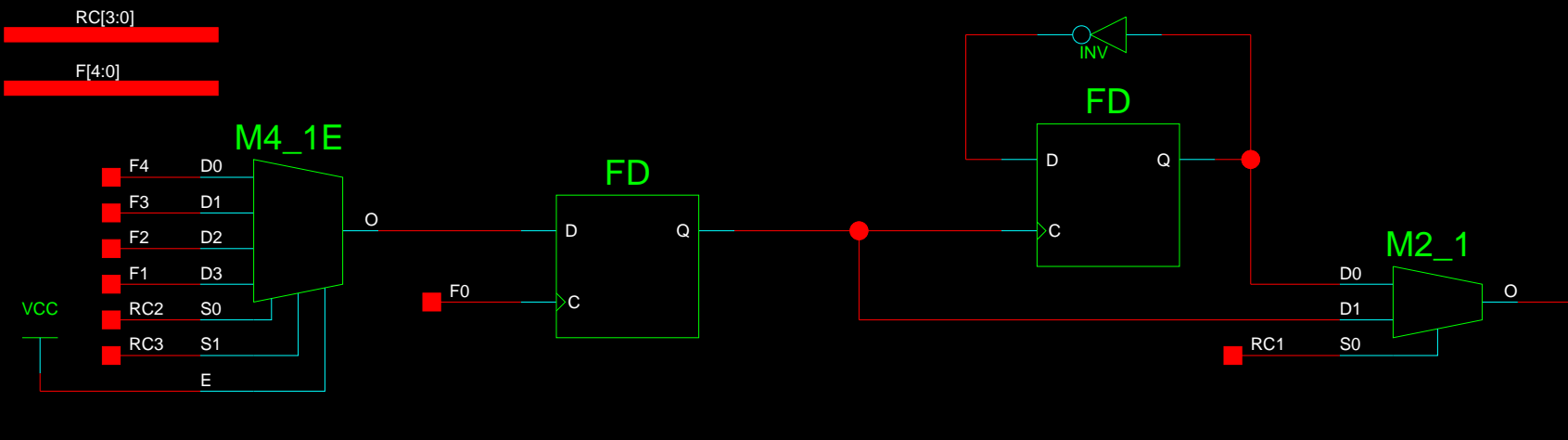
Indexer Channel Step Gen

PREPARED		
CHECKED		
ENGINEER	NTA	
STEPCLK		
1-19-2006_15:35		

size	Code ID No.	Drawing No.	rev
A		A34-7005	A
Scale		sheet	1 of 1

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HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE **RATEGEN.1**
Channel Rate Selection

PREPARED			
CHECKED			
ENGINEER	NTA		
RATEGEN		size AI	Code ID No.
1-19-2006_15:37		Scale	Drawing No. A34-7005
			rev A
sheet			1 of 1 18 of 18

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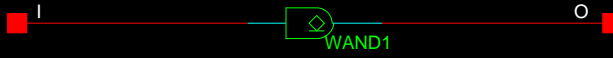
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A



Title: XC4000 Family BUFOD Macro

Comments: Open-Drain Buffer

Date: 6th April 1993

Ver: 1

Sheet Size: A

Rev: A

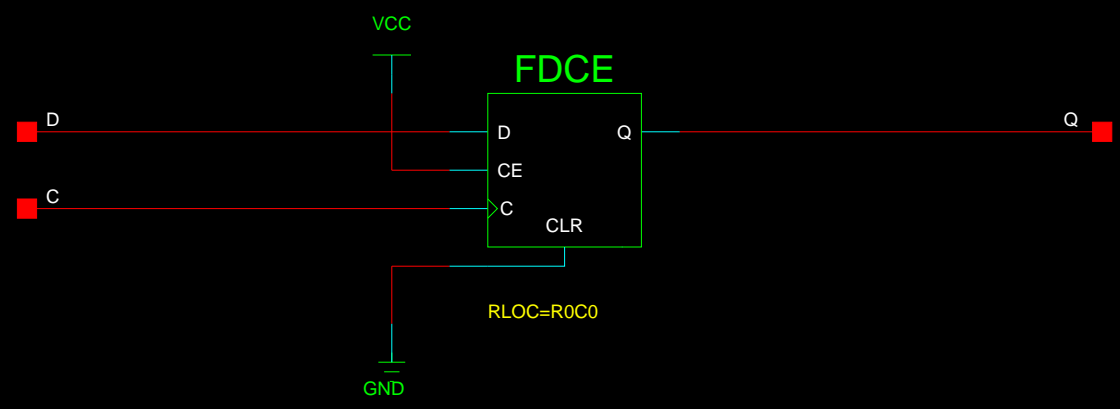
drawn by KS
Copyright (c) 1993, Xilinx Inc.

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Title: XC4000 Family FD Macro

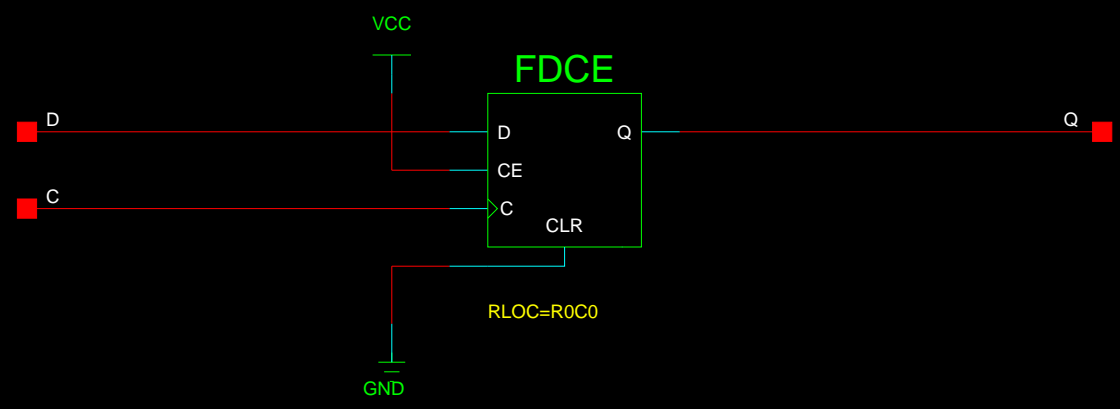
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Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

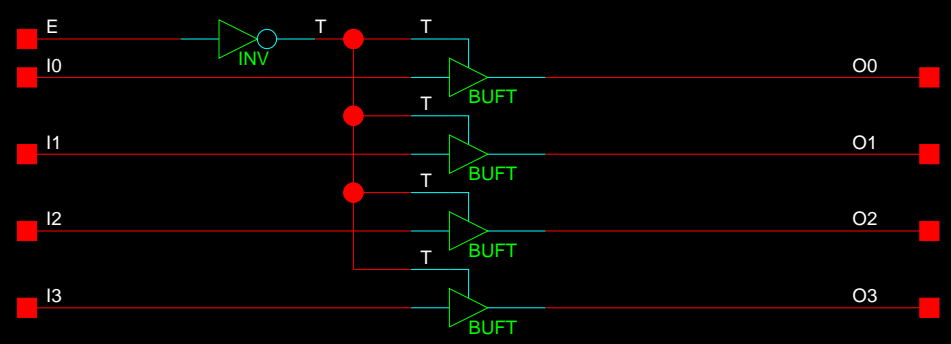
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family BUFE4 Macro

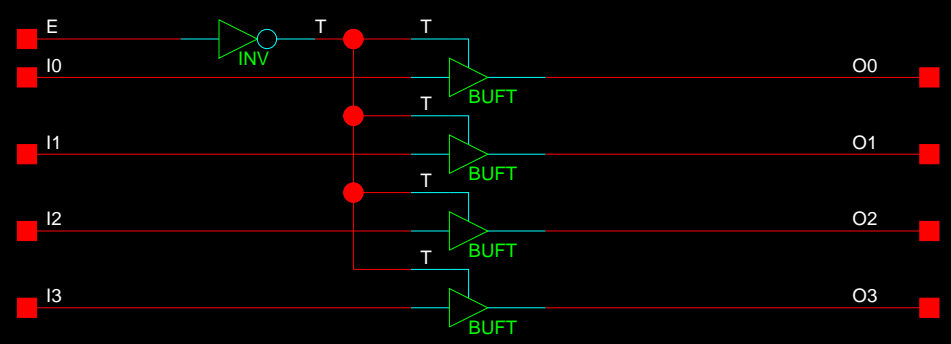
Comments: 4-Bit 3-State Buffer
w/ an Active High Enable

Date: 23rd February 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family BUFE4 Macro

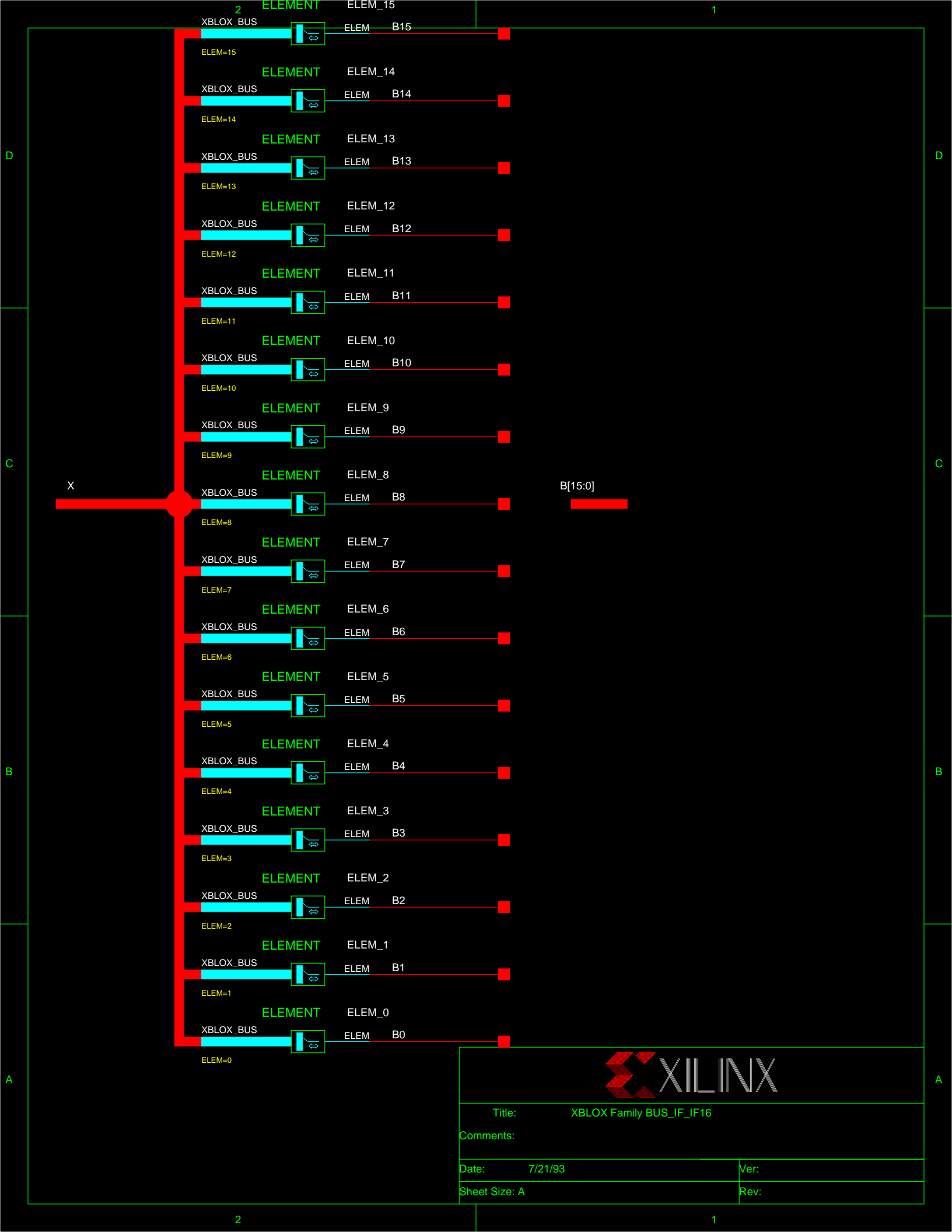
Comments: 4-Bit 3-State Buffer
w/ an Active High Enable

Date: 23rd February 1993

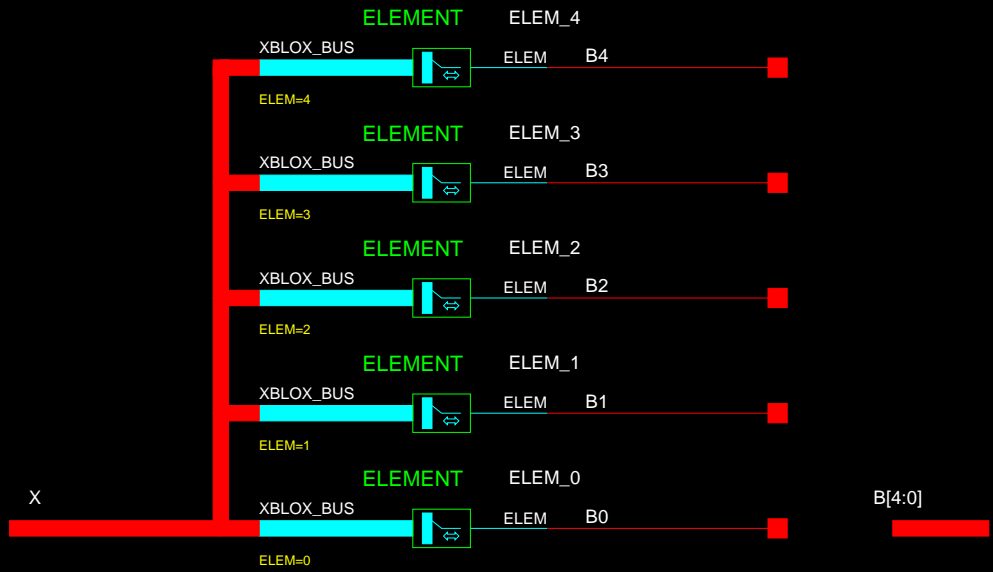
Ver: 1

Sheet Size: A

Rev: A



Title: XBLOX Family BUS_IF_IF16	
Comments:	
Date: 7/21/93	Ver:
Sheet Size: A	Rev:



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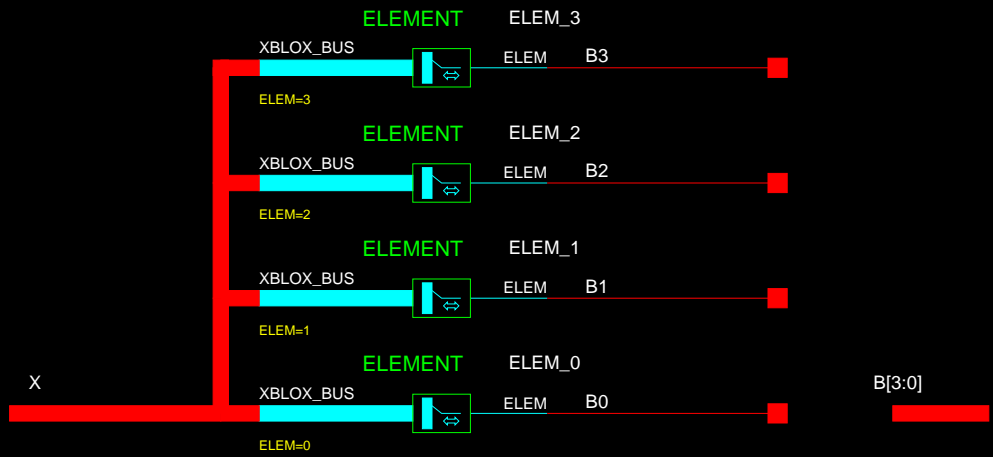
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Date: 7/21/93

Ver: 1

Sheet Size: A

Rev: A



Title: XBLOX Family BUS_IF04

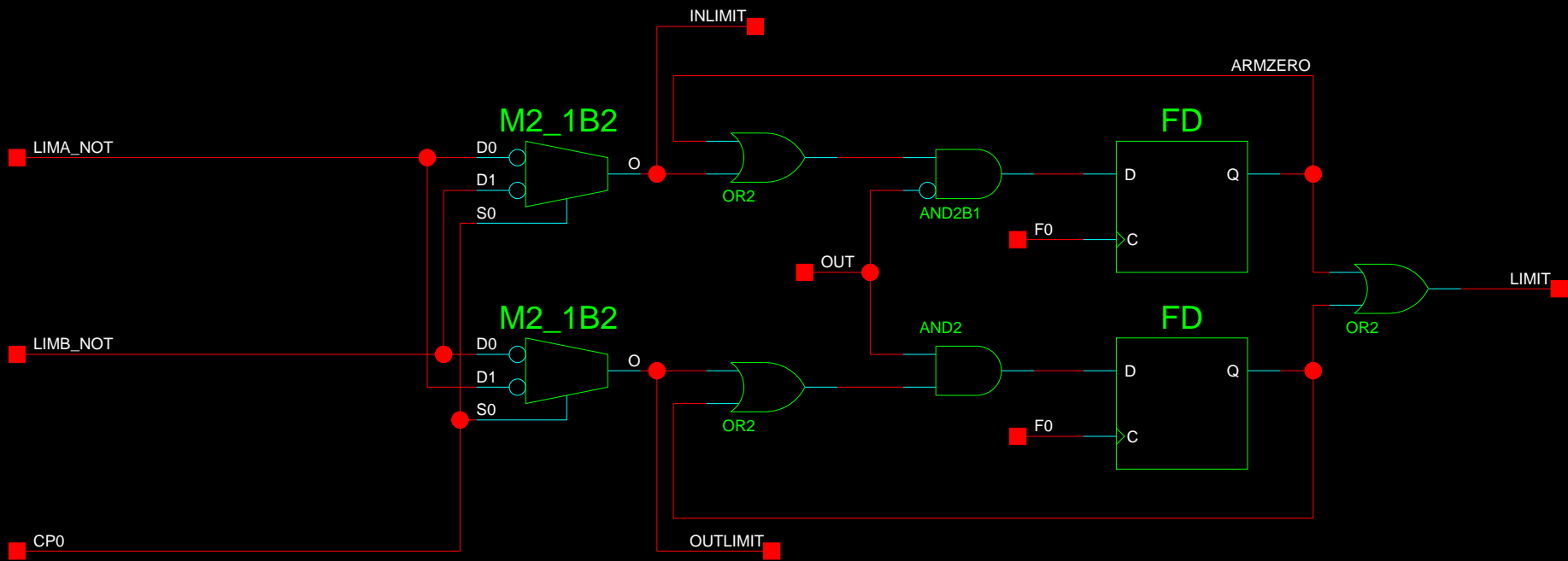
Comments:

Date: 7/21/93

Ver: 1

Sheet Size: A

Rev: A



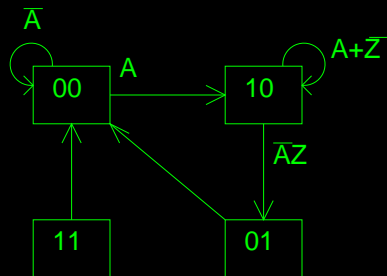
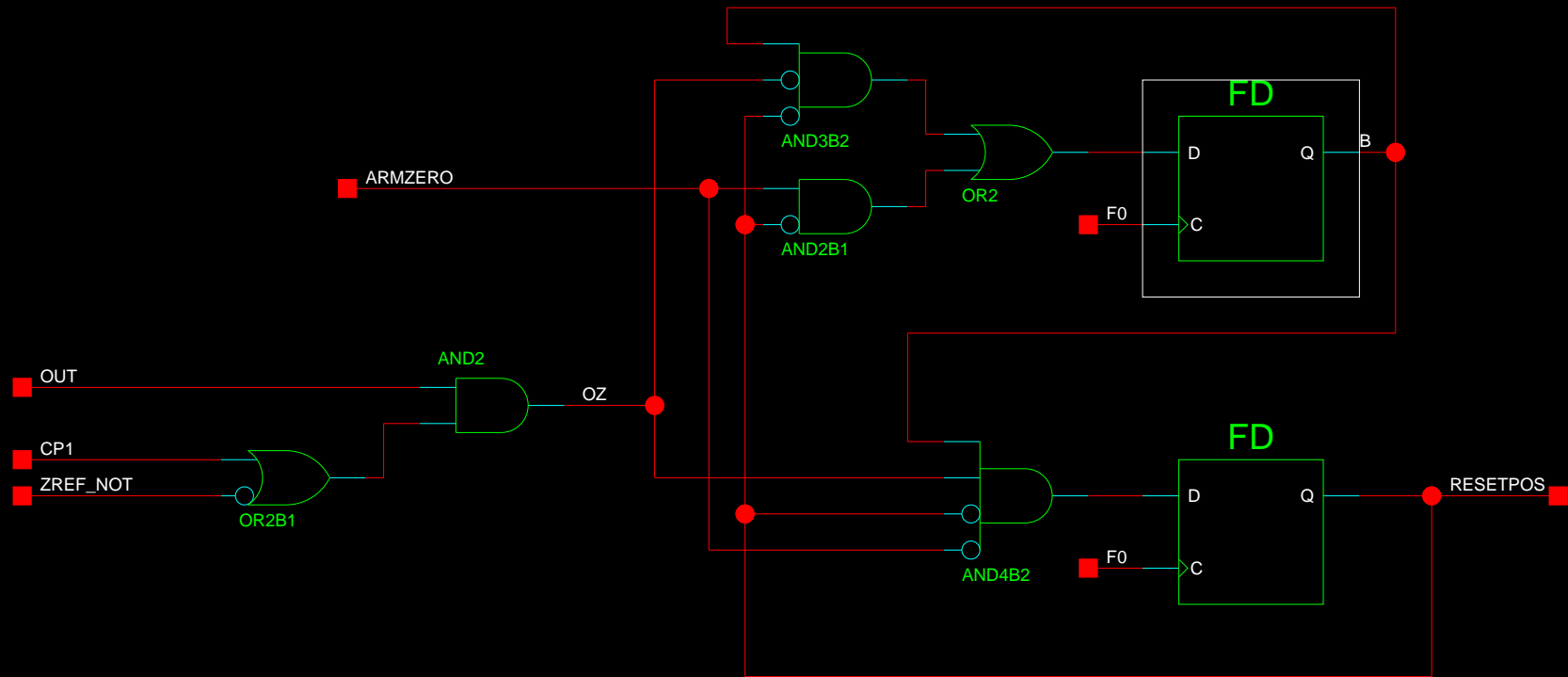
HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE
LIMITS.1

Indexer Limit Switches

PREPARED		
CHECKED		
ENGINEER	NTA	

LIMITS	size	Code ID No.	Drawing No.	rev
	AI		A34-7005	A
1-19-2006_15:35	Scale		sheet	1 of 12



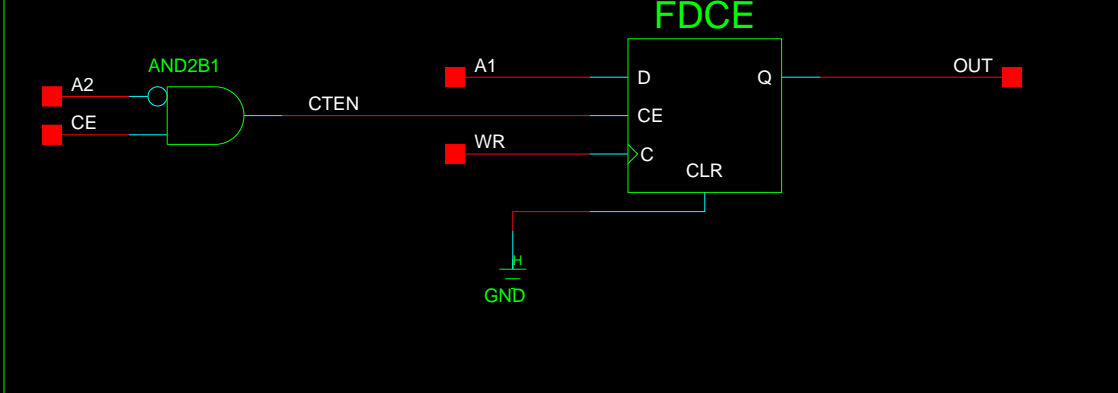
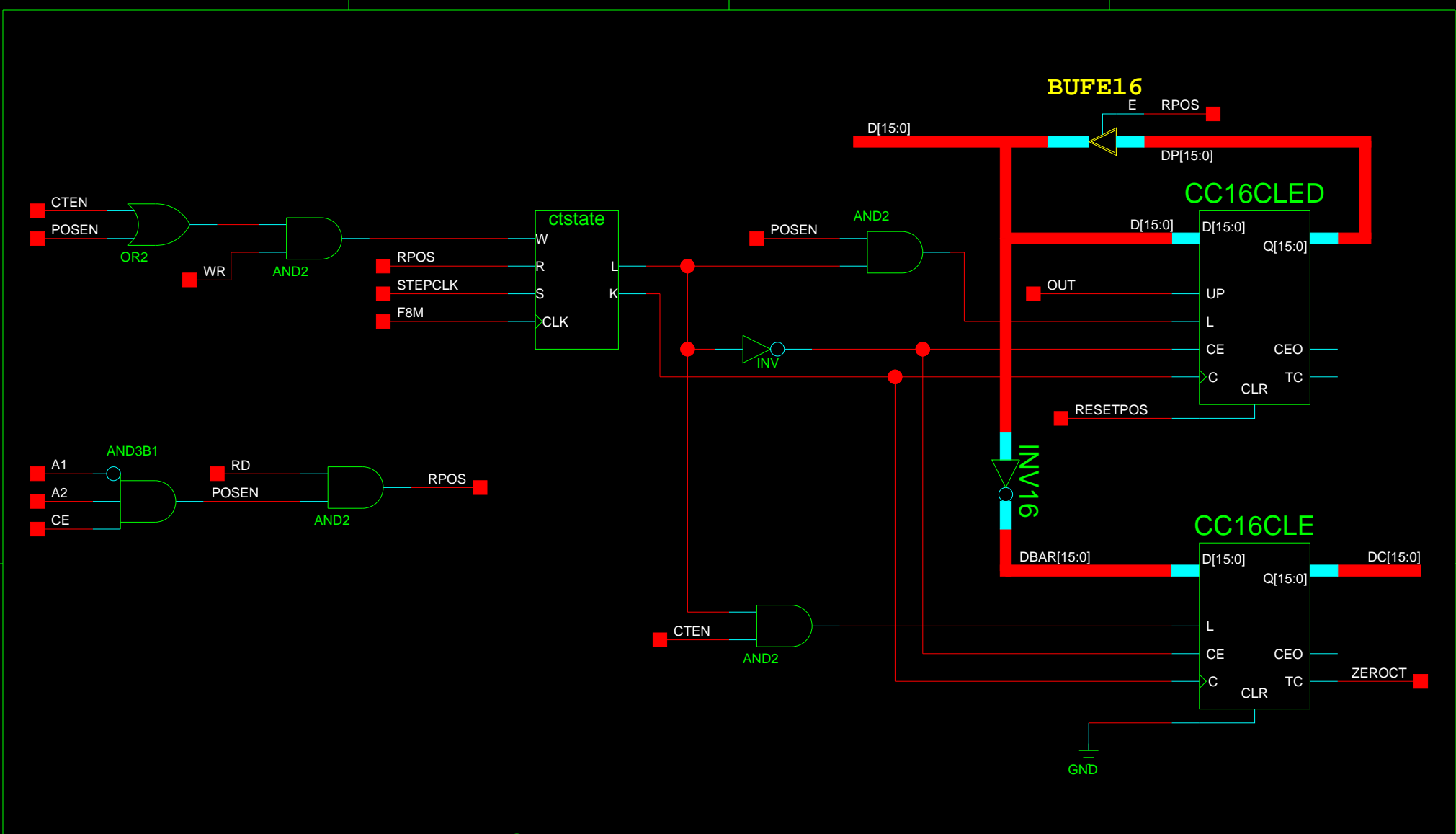
A: ARMZERO Input
 Z: OZ Qualified Zeroref
 00
 — RESETPOS Output
 — B "Zero Armed" State

HARVARD UNIVERSITY
 ATMOSPHERIC RESEARCH PROJECT

TITLE
LIMITS.2
 Indexer Limit Switches

PREPARED
 CHECKED
 ENGINEER NTA

LIMITS	size	Code ID No.	Drawing No.	rev
	AI		A34-7005	A
1-19-2006_15:36	Scale	sheet 2 of 2		13 of 18



HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

CTRS.1
Indexer Channel Counters

PREPARED		TITLE	
CHECKED		CTRS.1	
ENGINEER	NTA	Indexer Channel Counters	
CTRS		size	Code ID No.
		AI	A34-7005
1-19-2006_15:37		Drawing No.	A
		sheet 1 of 1	
		15 of 18	

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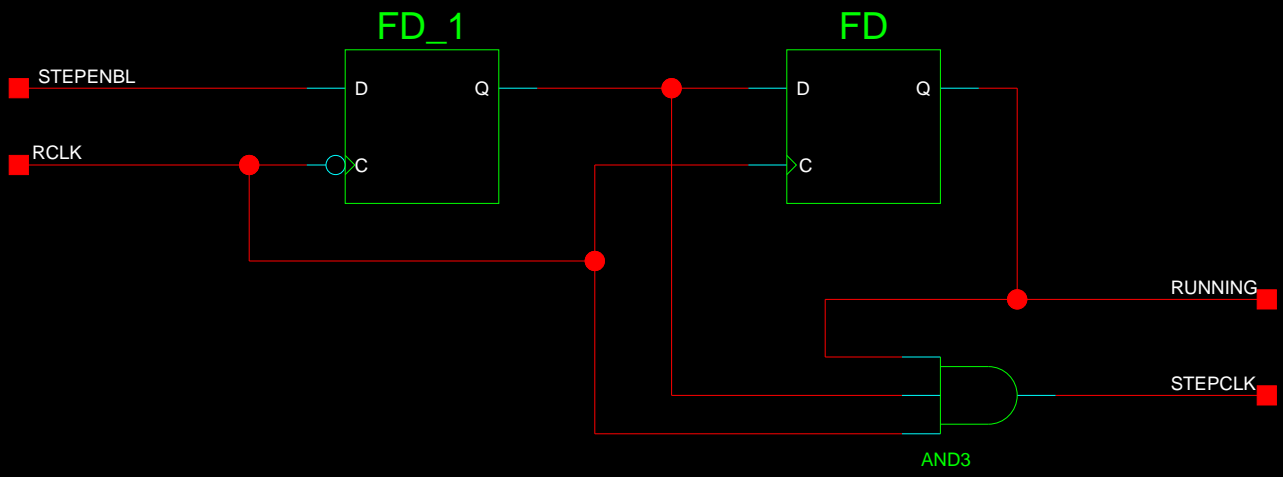
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HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE **STEPCLK.1**

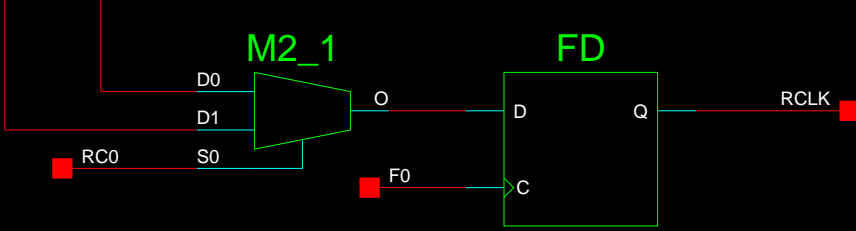
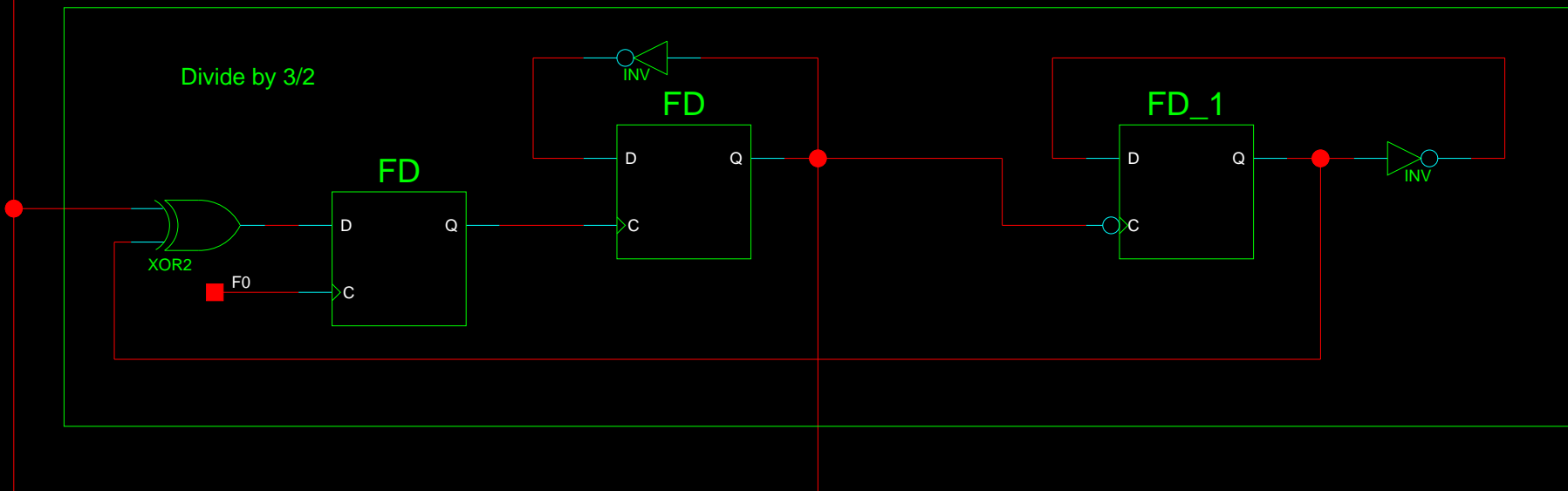
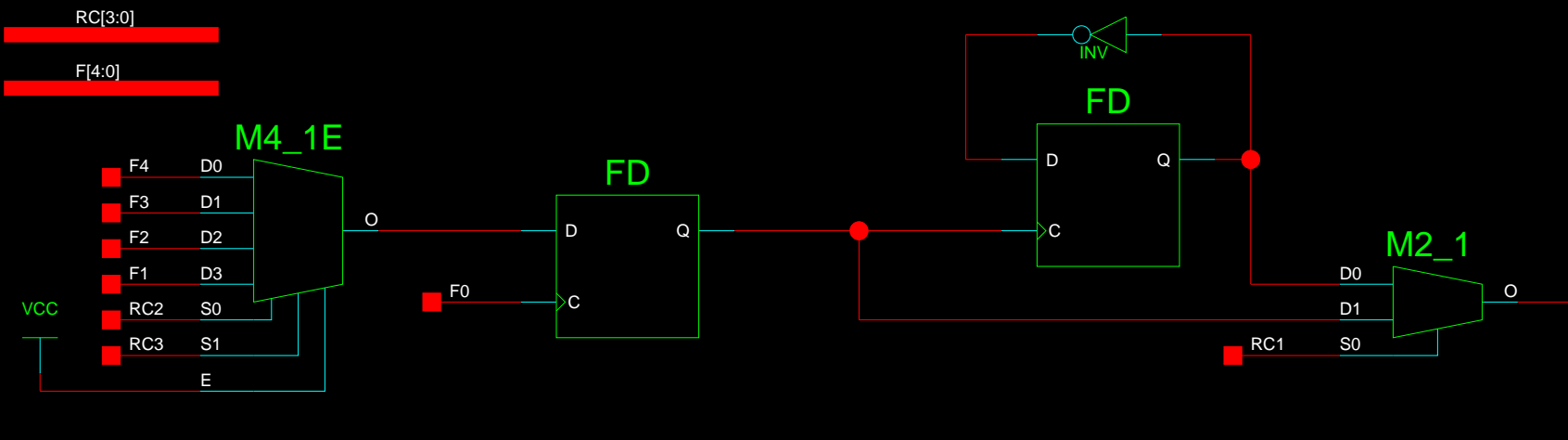
Indexer Channel Step Gen

PREPARED		
CHECKED		
ENGINEER	NTA	
STEPCLK		
1-19-2006_15:35		

size	Code ID No.	Drawing No.	rev
A		A34-7005	A
Scale		sheet	1 of 1

A

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HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE **RATEGEN.1**
Channel Rate Selection

PREPARED			
CHECKED			
ENGINEER	NTA		
RATEGEN		size AI	Code ID No.
1-19-2006_15:37		Scale	Drawing No. A34-7005
			rev A
			sheet 1 of 1 <small>18 of 18</small>

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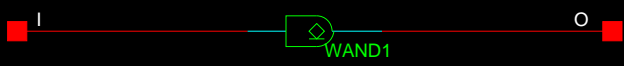
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B

B



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Title: XC4000 Family BUFOD Macro

Comments: Open-Drain Buffer

Date: 6th April 1993

Ver: 1

Sheet Size: A

Rev: A

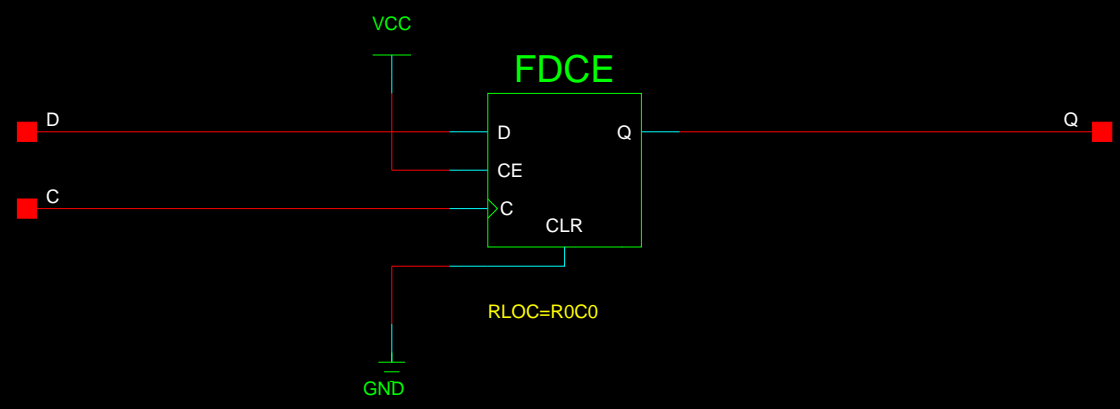
drawn by KS
Copyright (c) 1993, Xilinx Inc.

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Title: XC4000 Family FD Macro

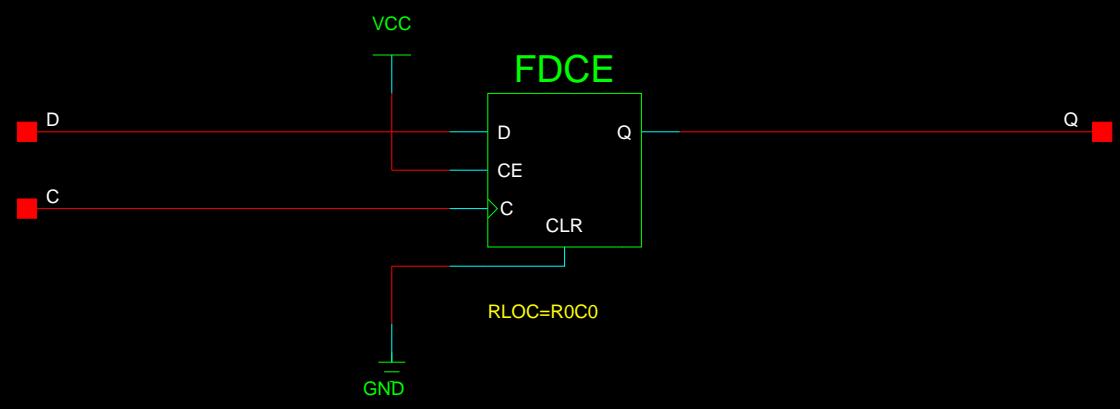
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Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

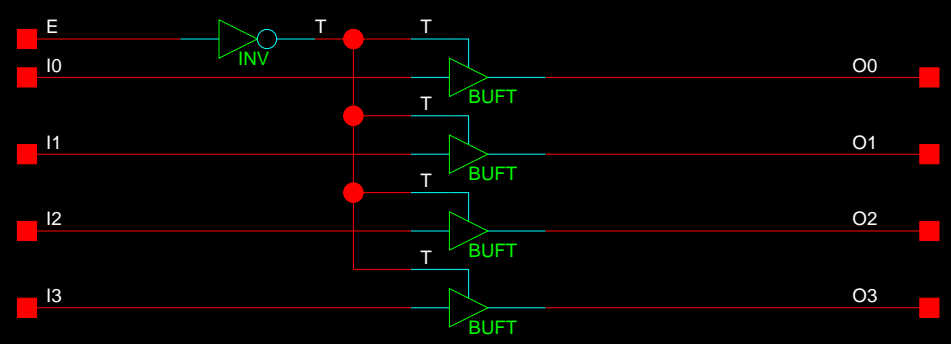
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Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family BUFE4 Macro

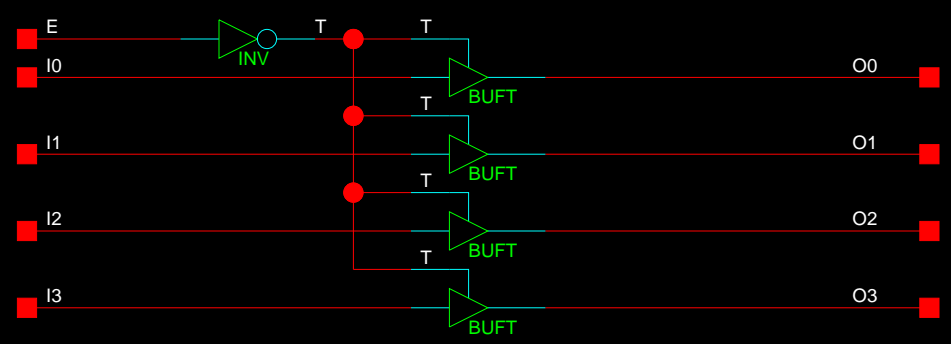
Comments: 4-Bit 3-State Buffer
w/ an Active High Enable

Date: 23rd February 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family BUFE4 Macro

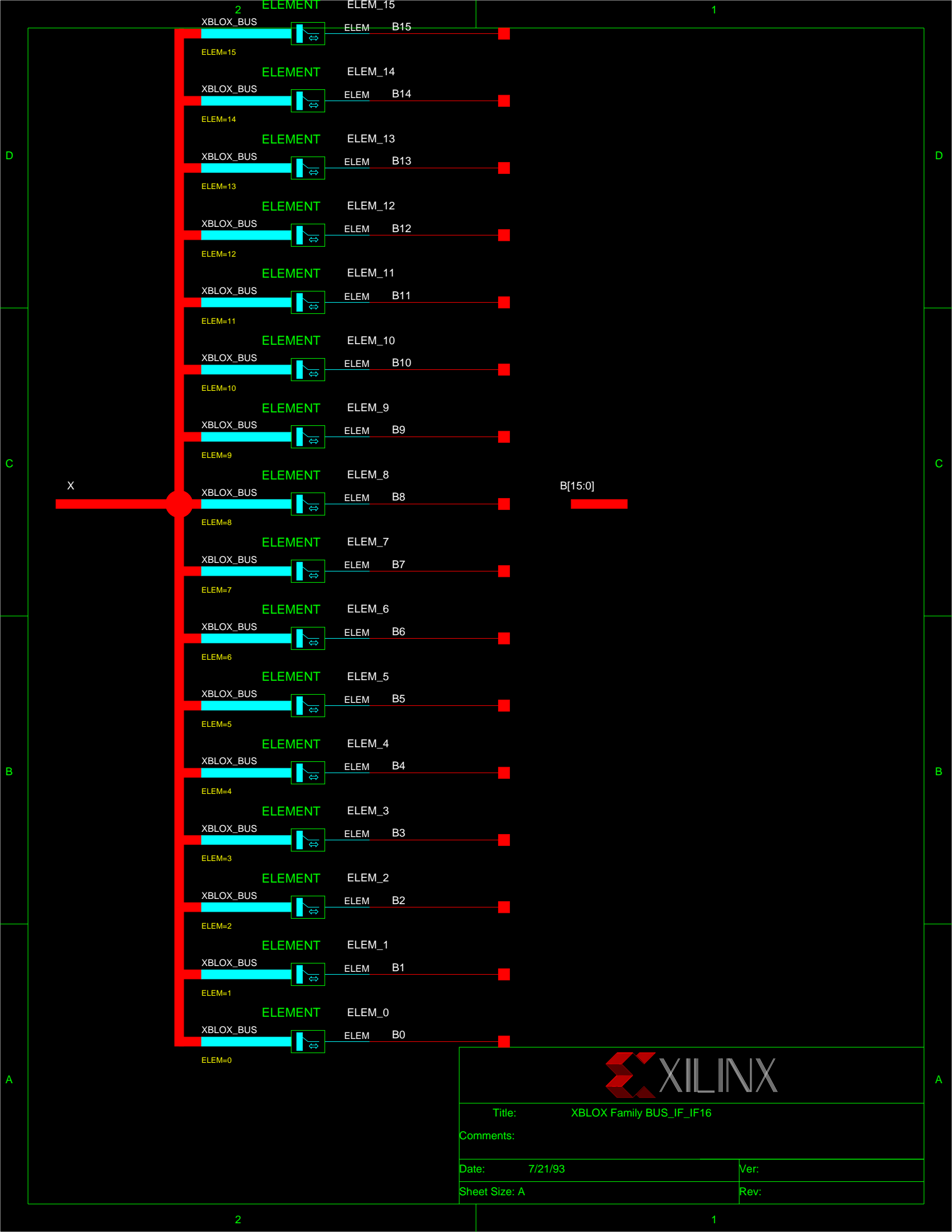
Comments: 4-Bit 3-State Buffer
w/ an Active High Enable

Date: 23rd February 1993

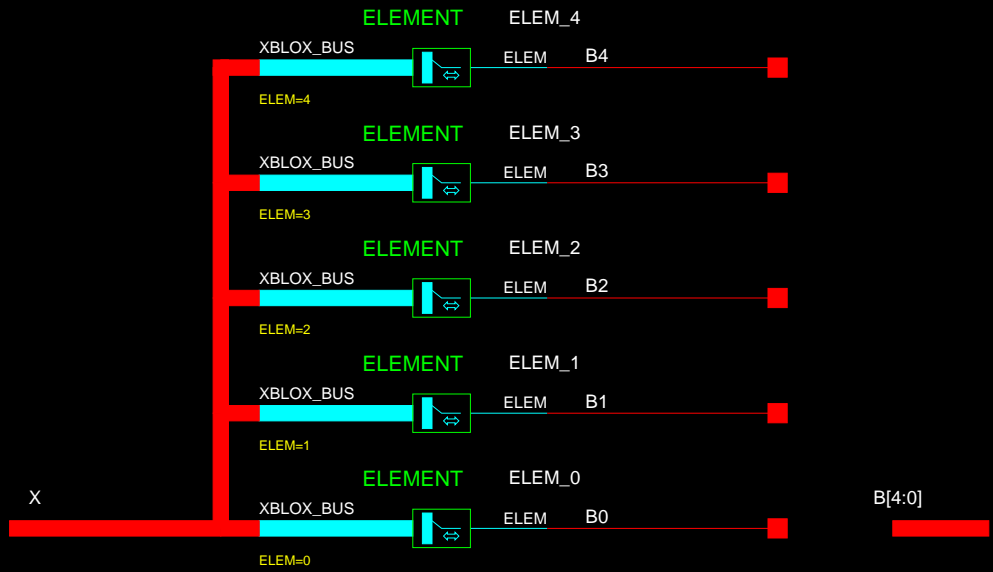
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Sheet Size: A

Rev: A



Title: XBLOX Family BUS_IF_IF16	
Comments:	
Date: 7/21/93	Ver:
Sheet Size: A	Rev:



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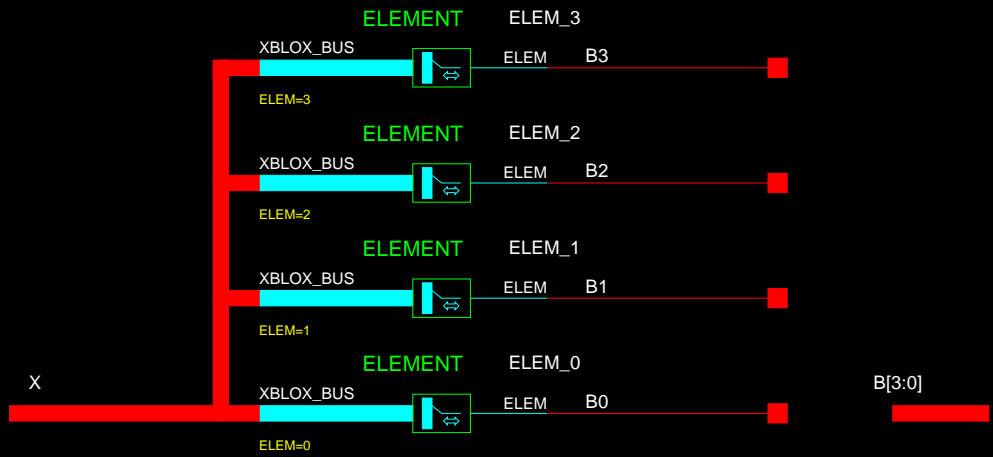
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Date: 7/21/93

Ver: 1

Sheet Size: A

Rev: A



Title: XBLOX Family BUS_IF04

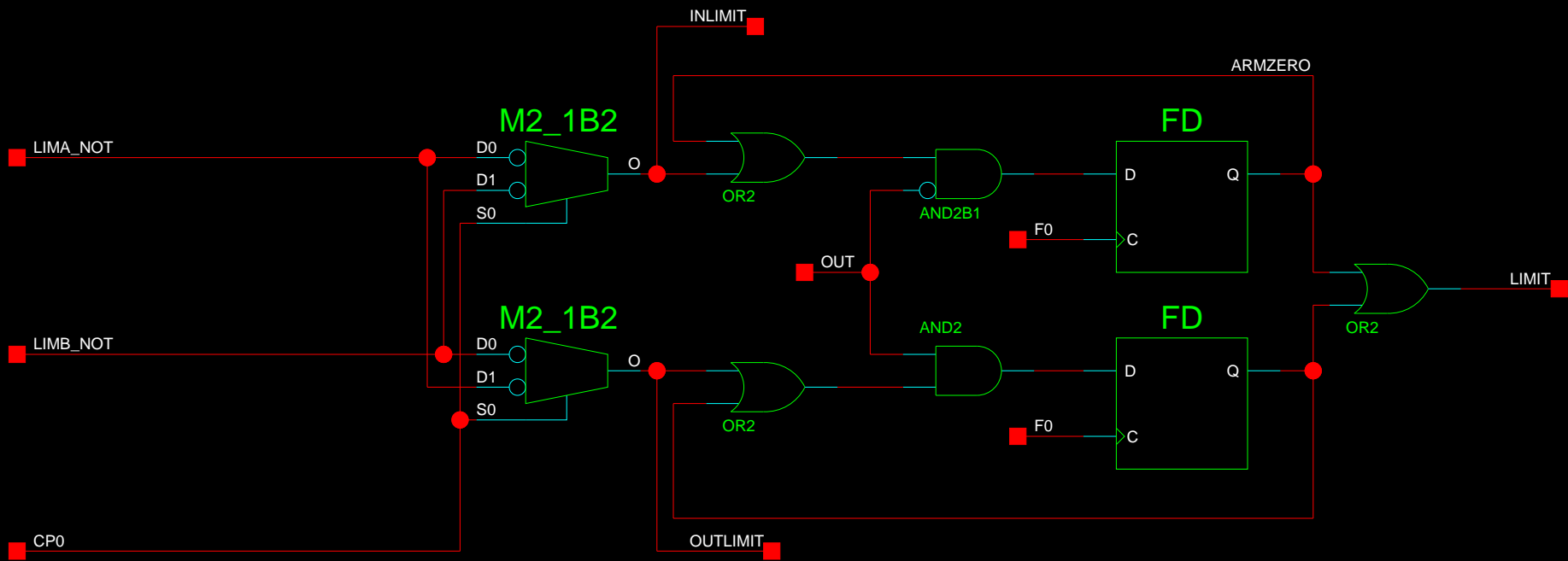
Comments:

Date: 7/21/93

Ver: 1

Sheet Size: A

Rev: A



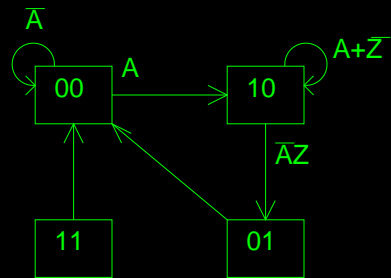
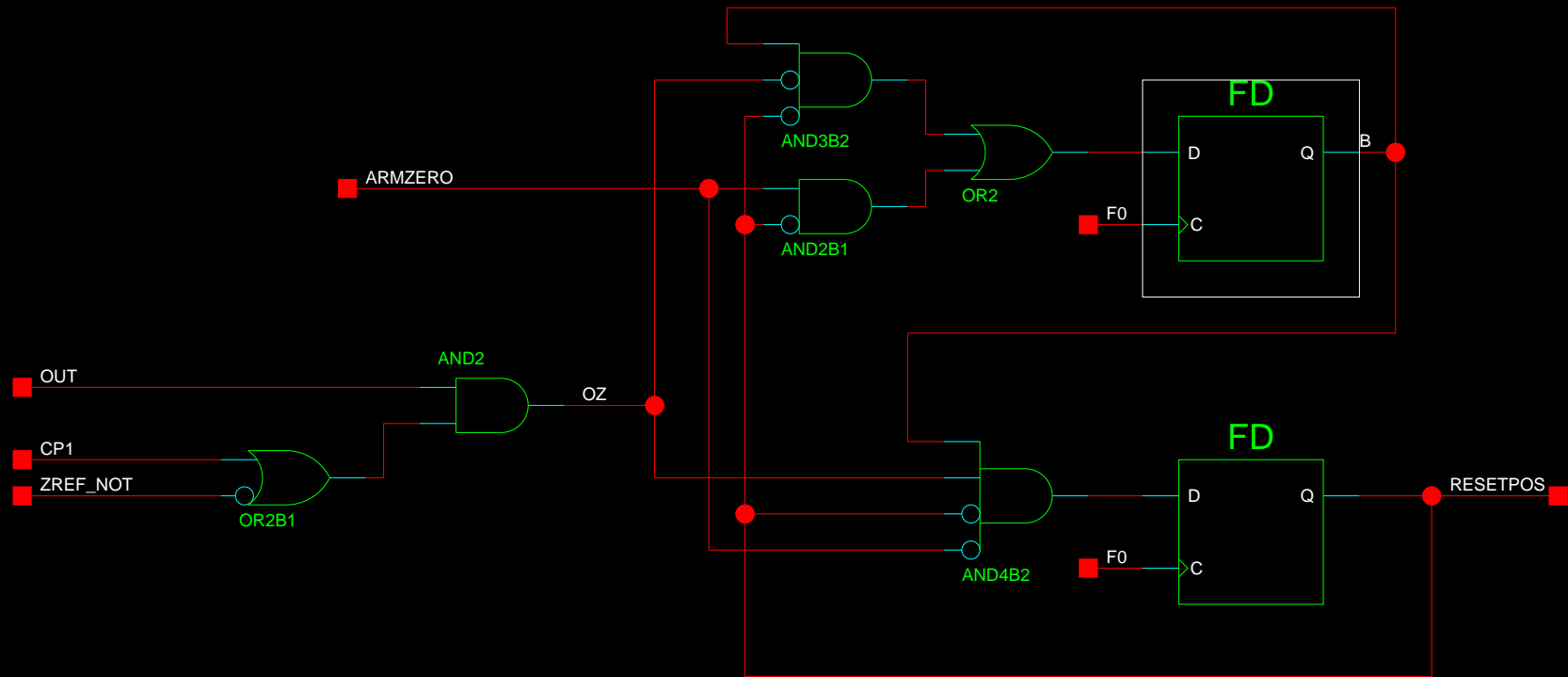
HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE
LIMITS.1

Indexer Limit Switches

PREPARED		
CHECKED		
ENGINEER	NTA	

LIMITS	size	Code ID No.	Drawing No.	rev
	AI		A34-7005	A
1-19-2006_15:35	Scale		sheet	1 of 12



A: ARMZERO Input
 Z: OZ Qualified Zeroref
 00
 — RESETPOS Output
 — B "Zero Armed" State

HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE
LIMITS.2
 Indexer Limit Switches

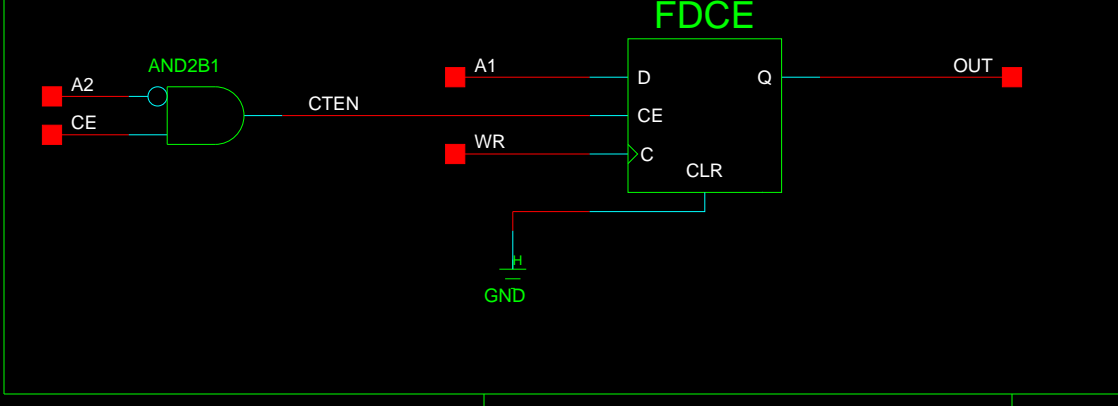
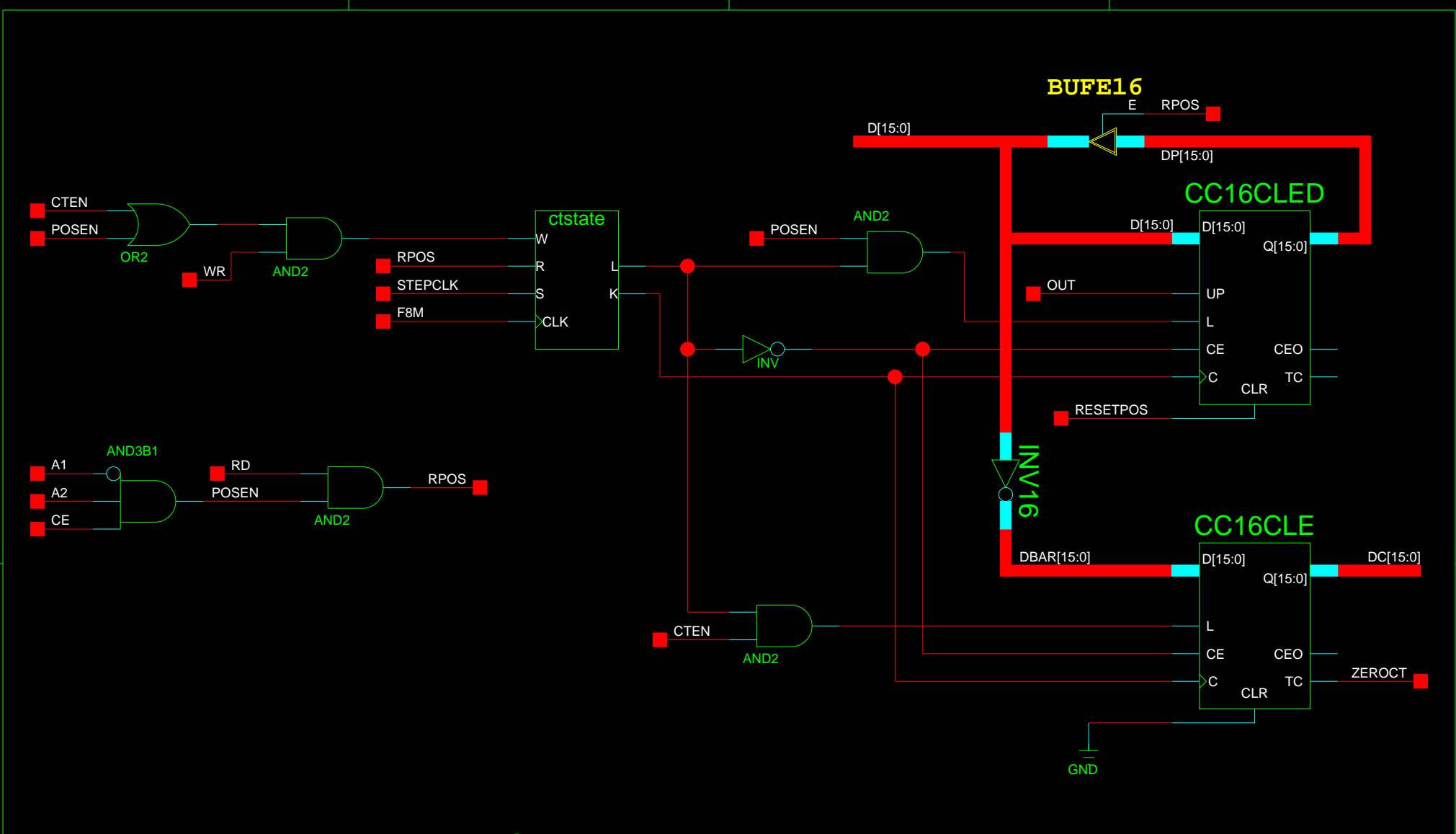
PREPARED
 CHECKED
 ENGINEER **NTA**

LIMITS

size	Code ID No.	Drawing No.	rev
AI		A34-7005	A

1-19-2006_15:36

Scale sheet **2** of **2**
13 of 18



HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

CTRS.1
Indexer Channel Counters

PREPARED		TITLE	
CHECKED		CTRS.1	
ENGINEER	NTA	Indexer Channel Counters	
CTRS		size	Code ID No.
		AI	A34-7005
1-19-2006_15:37		Drawing No.	A
		Scale	sheet 1 of 1
		15 of 18	

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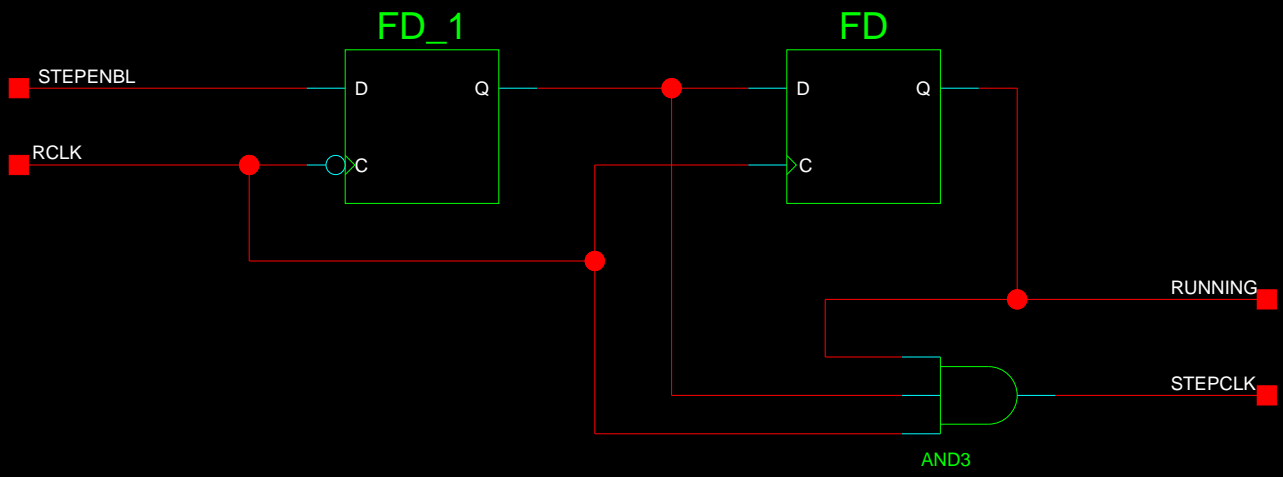
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HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE **STEPCLK.1**

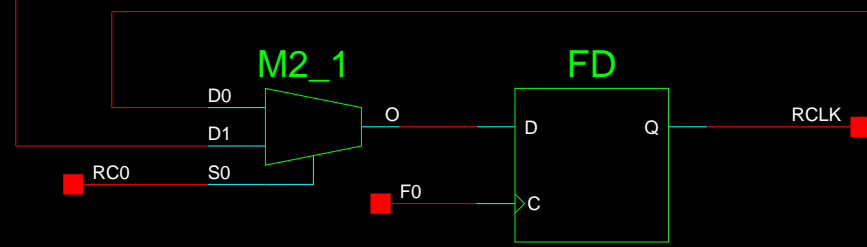
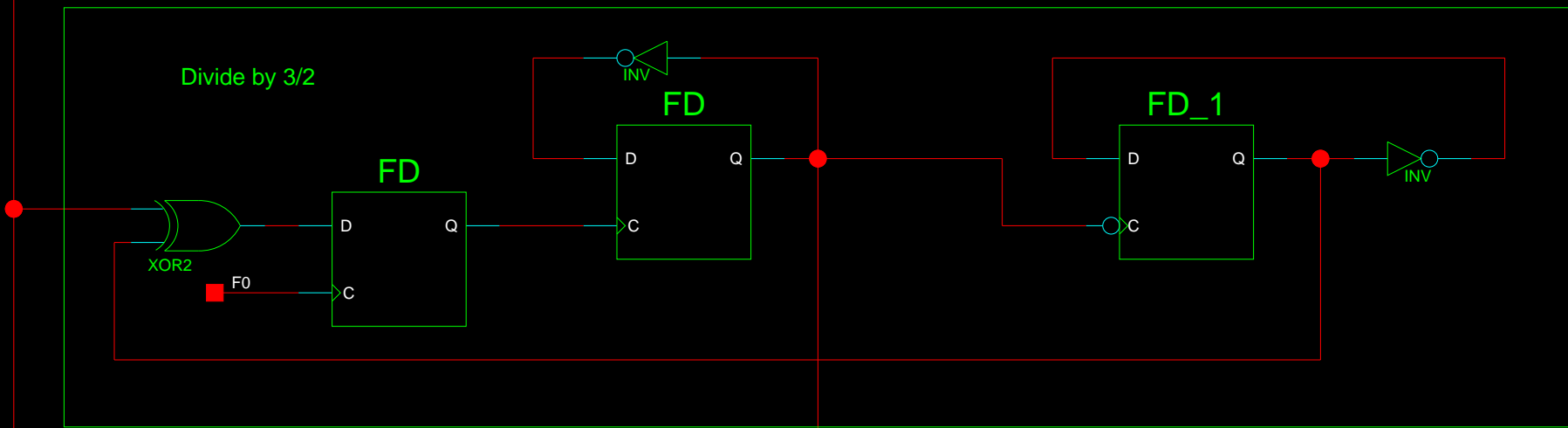
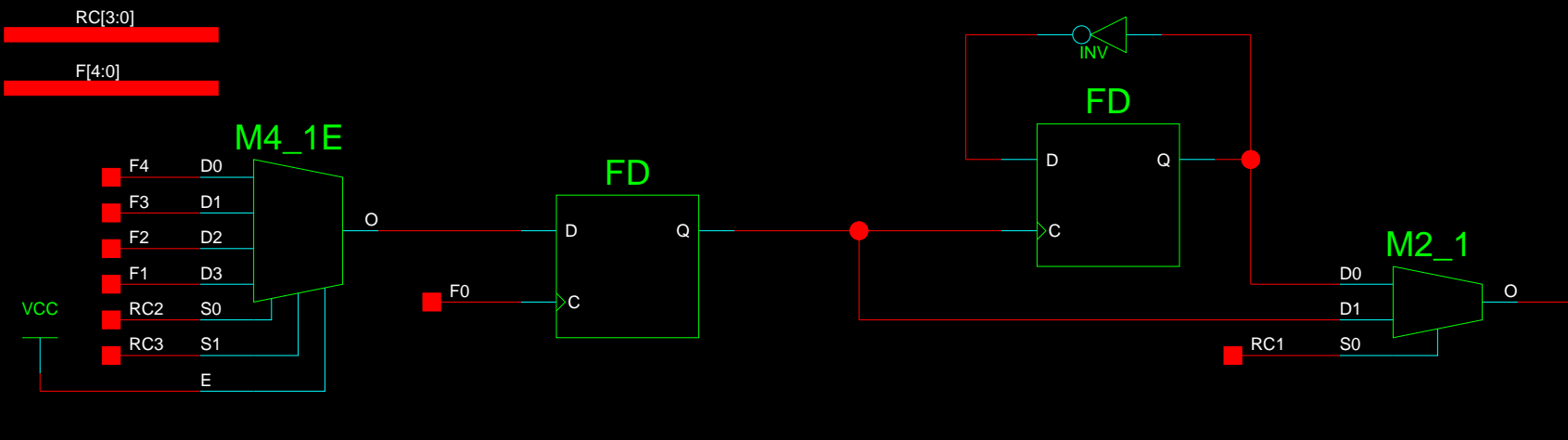
Indexer Channel Step Gen

PREPARED		
CHECKED		
ENGINEER	NTA	
STEPCLK		
1-19-2006_15:35		

size	Code ID No.	Drawing No.	rev
A		A34-7005	A
Scale		sheet	1 of 1

A

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HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE **RATEGEN.1**
Channel Rate Selection

PREPARED			
CHECKED			
ENGINEER	NTA		
RATEGEN		size AI	Code ID No.
1-19-2006_15:37		Scale	Drawing No. A34-7005
			rev A
sheet			1 of 1 18 of 18

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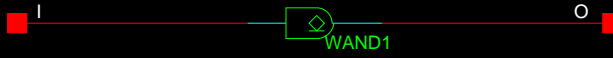
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A

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Title: XC4000 Family BUFOD Macro

Comments: Open-Drain Buffer

Date: 6th April 1993

Ver: 1

Sheet Size: A

Rev: A

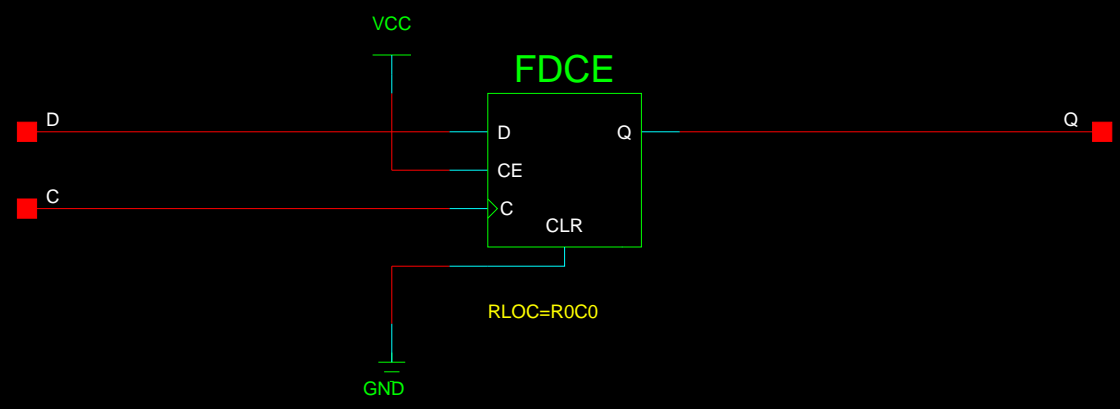
drawn by KS
Copyright (c) 1993, Xilinx Inc.

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1



Title: XC4000 Family FD Macro

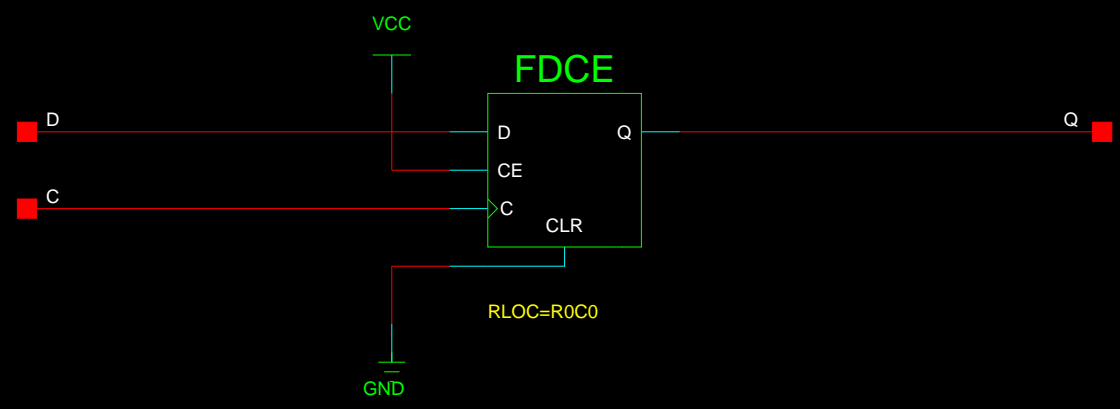
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Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

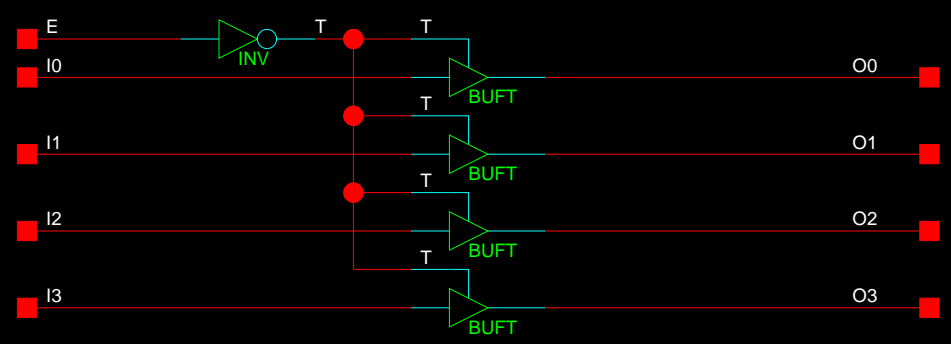
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Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



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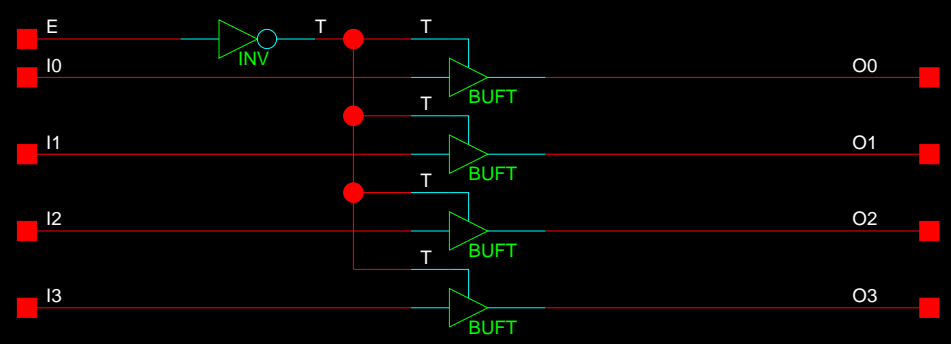
Comments: 4-Bit 3-State Buffer
w/ an Active High Enable

Date: 23rd February 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family BUFE4 Macro

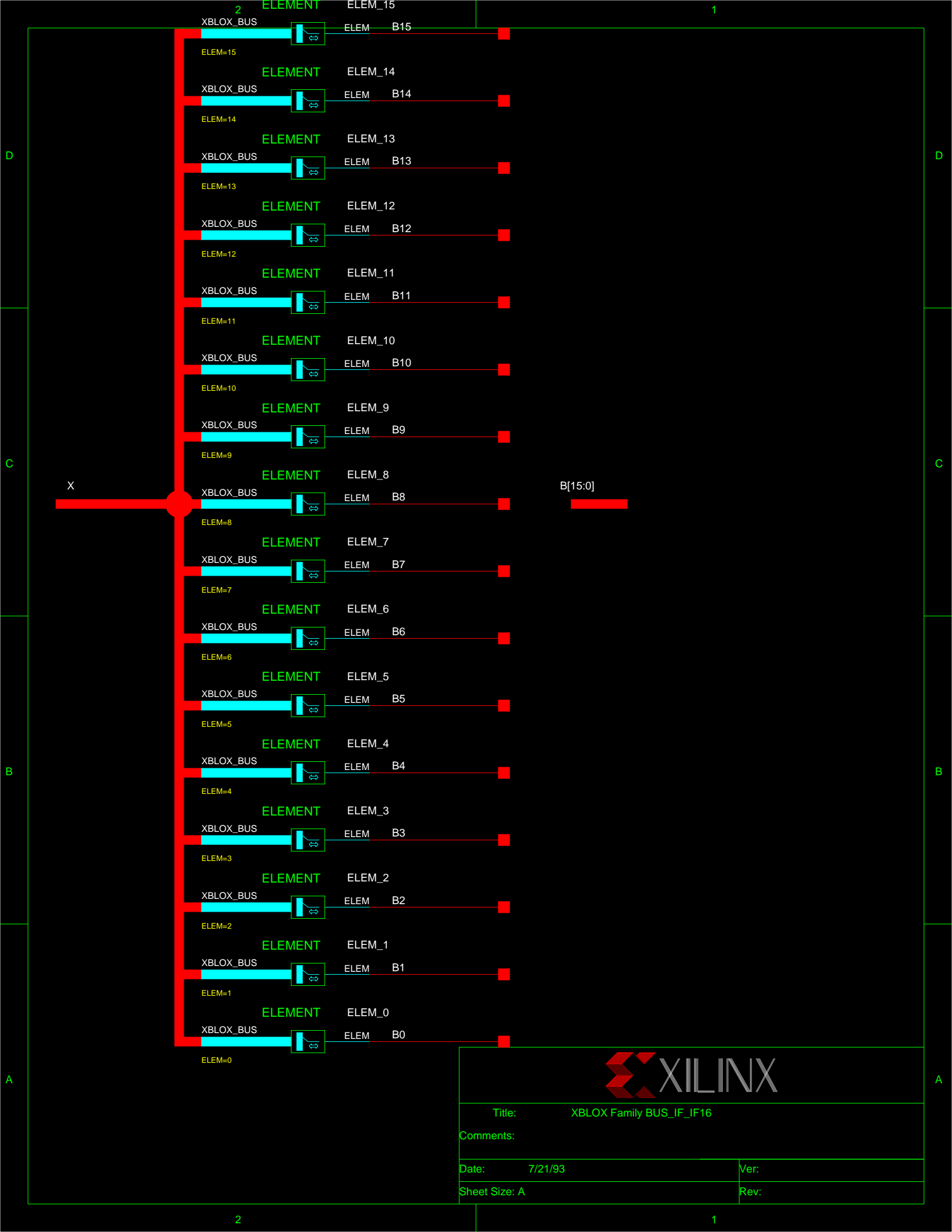
Comments: 4-Bit 3-State Buffer
w/ an Active High Enable

Date: 23rd February 1993

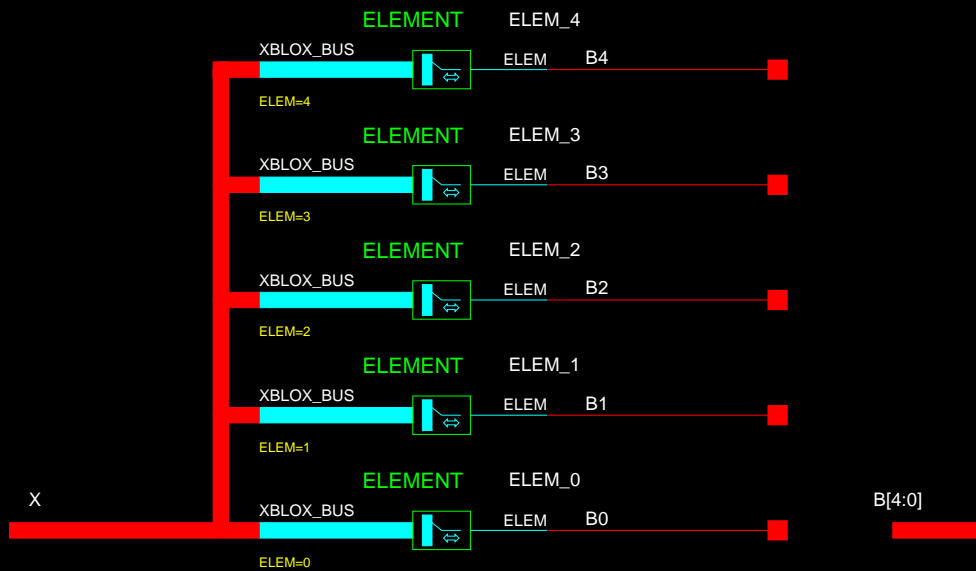
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Sheet Size: A

Rev: A



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Comments:	
Date: 7/21/93	Ver:
Sheet Size: A	Rev:



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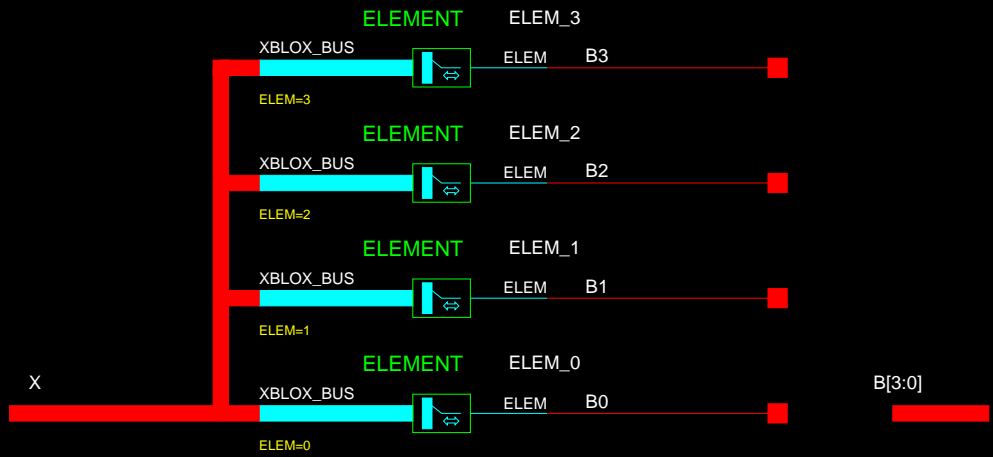
Comments:

Date: 7/21/93

Ver: 1

Sheet Size: A

Rev: A



Title: XBLOX Family BUS_IF04

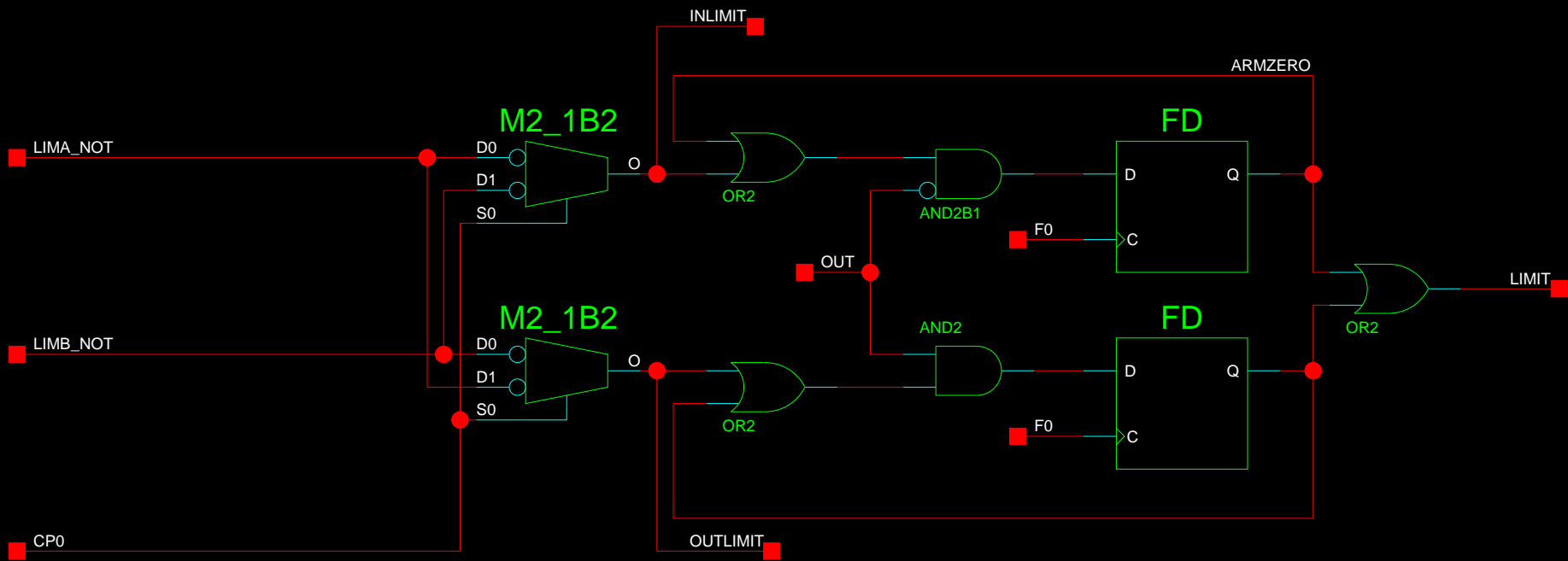
Comments:

Date: 7/21/93

Ver: 1

Sheet Size: A

Rev: A



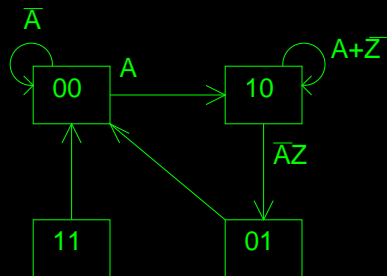
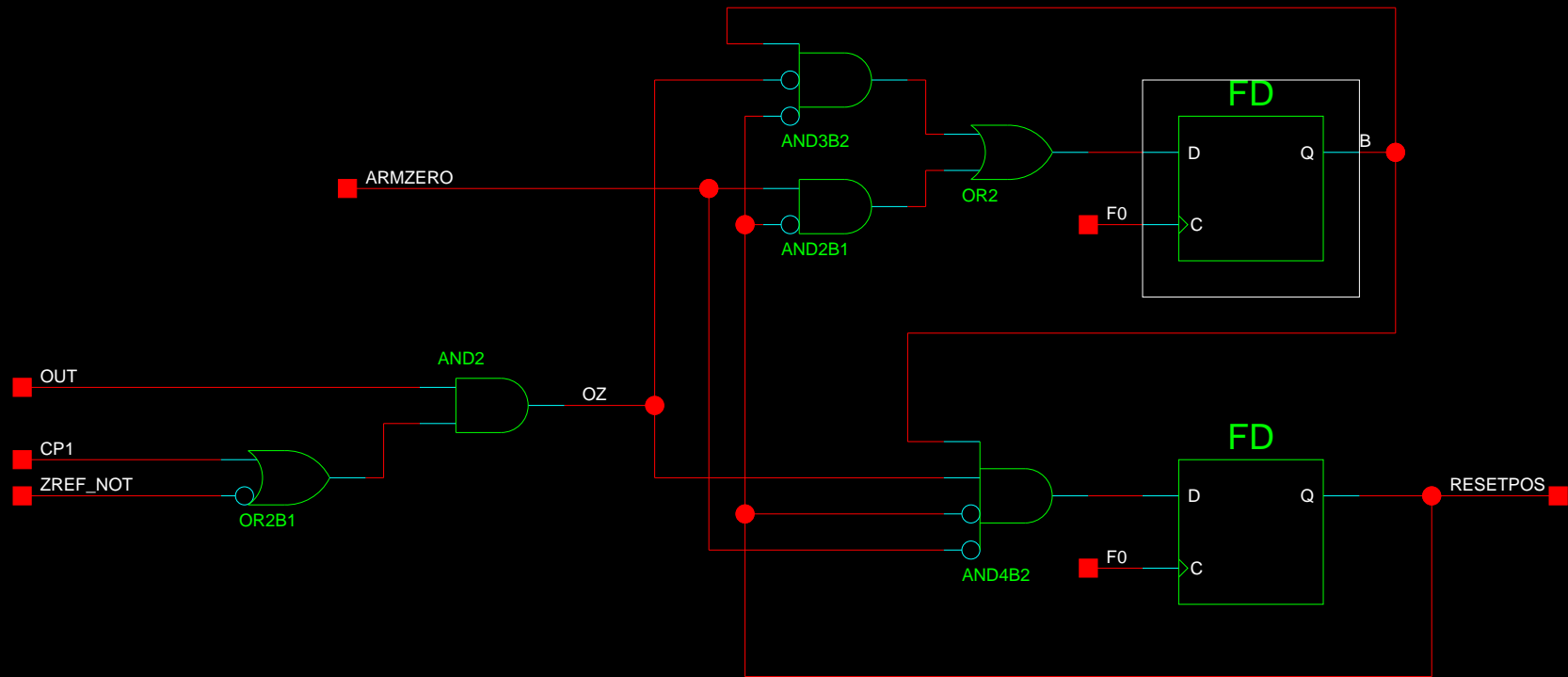
HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE
LIMITS.1

Indexer Limit Switches

PREPARED		
CHECKED		
ENGINEER	NTA	

LIMITS	size	Code ID No.	Drawing No.	rev
	AI		A34-7005	A
1-19-2006_15:35	Scale		sheet	1 of 12



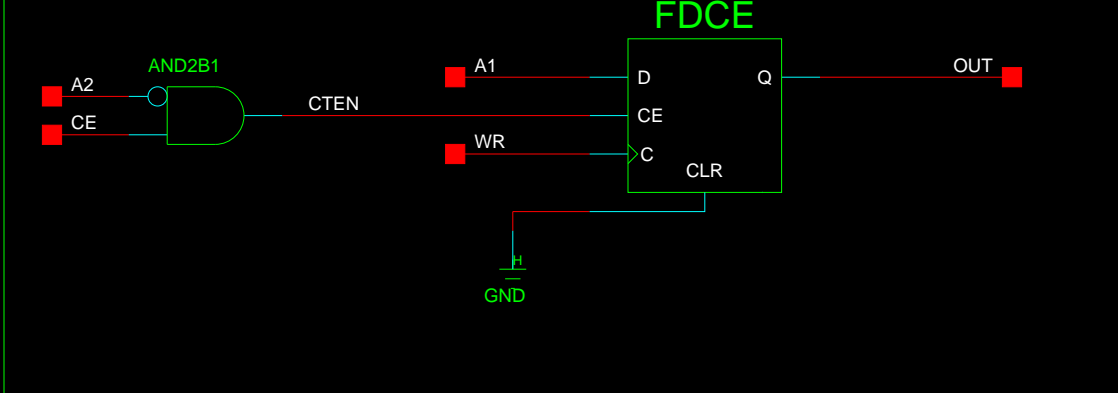
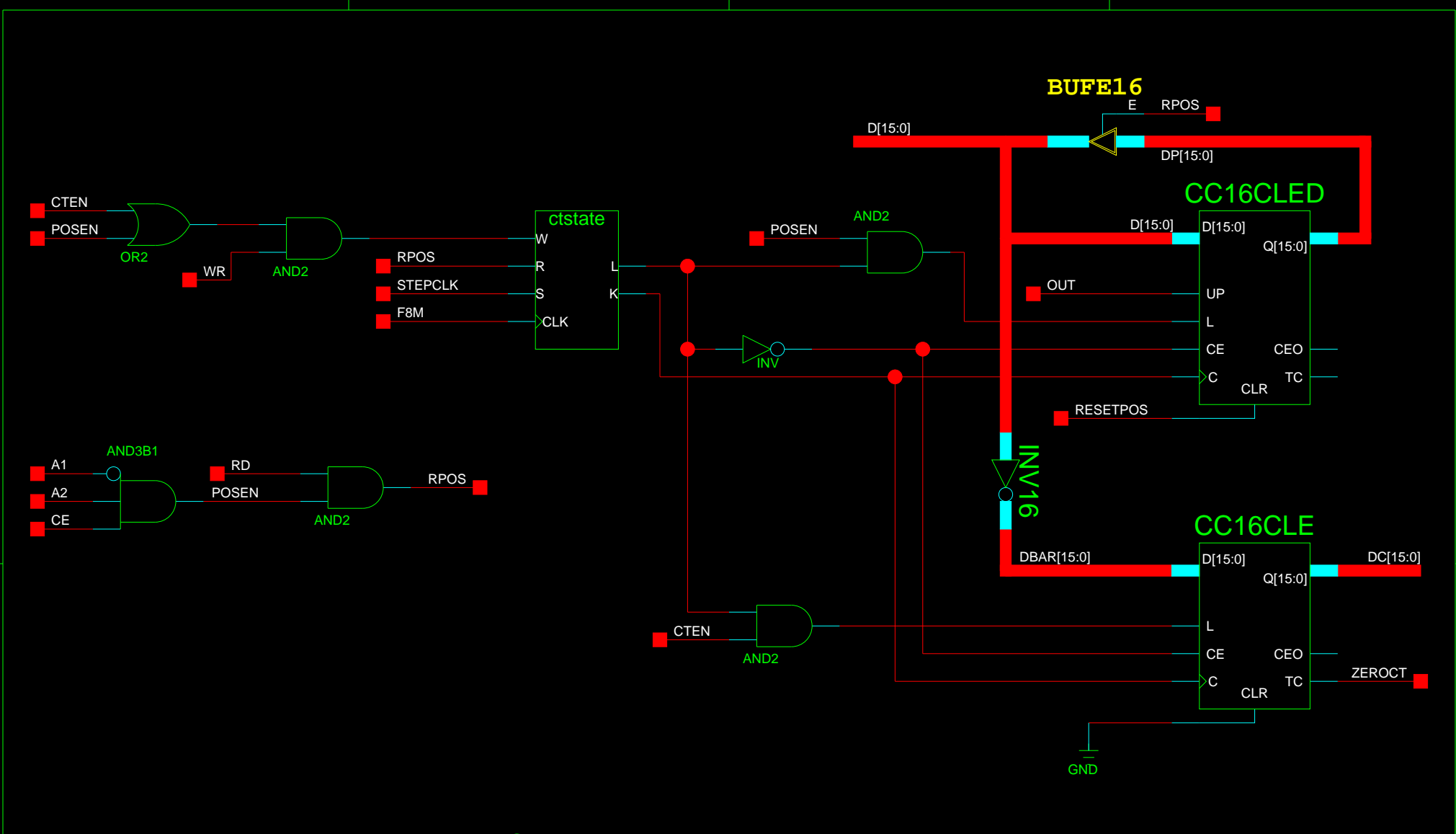
A: ARMZERO Input
 Z: OZ Qualified Zeroref
 00
 — RESETPOS Output
 — B "Zero Armed" State

HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE
LIMITS.2
 Indexer Limit Switches

PREPARED
 CHECKED
 ENGINEER NTA

LIMITS	size	Code ID No.	Drawing No.	rev
	AI		A34-7005	A
1-19-2006_15:36	Scale	sheet 2 of 2		13 of 18



HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

CTRS.1
Indexer Channel Counters

PREPARED		TITLE	
CHECKED		CTRS.1	
ENGINEER	NTA	Indexer Channel Counters	
CTRS		size	Code ID No.
		AI	A34-7005
1-19-2006_15:37		Drawing No.	A
		Scale	sheet 1 of 1
		15 of 18	

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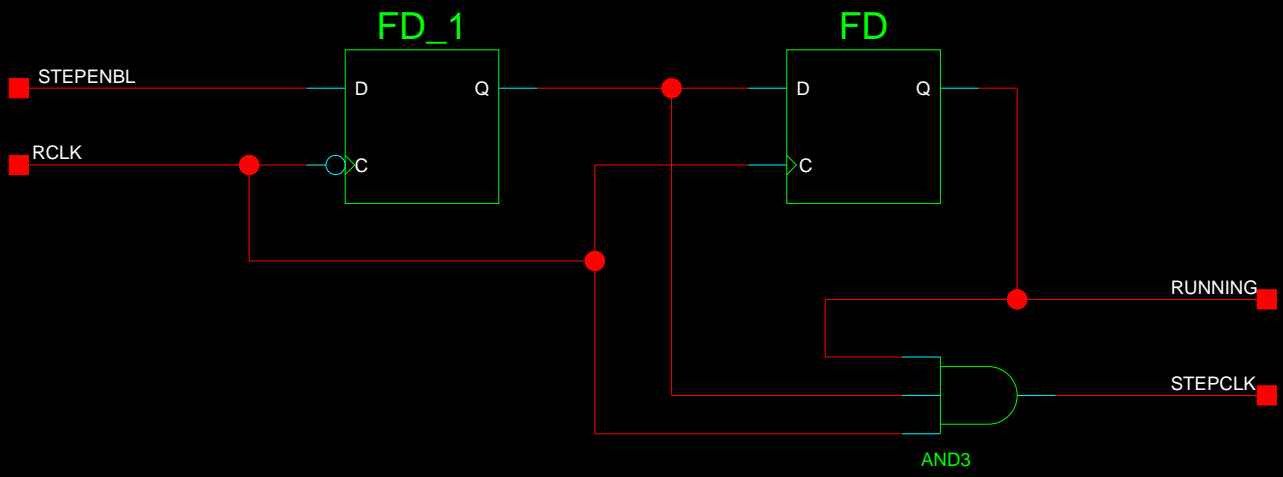
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HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

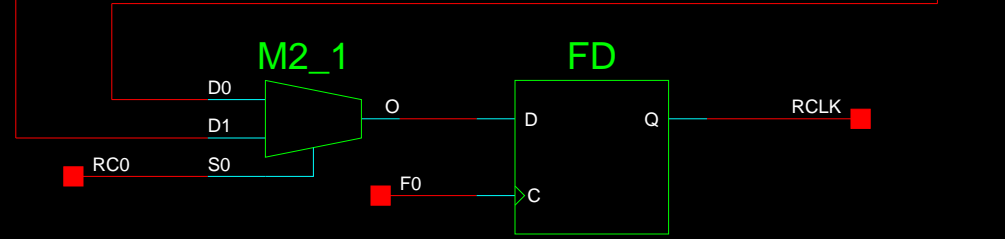
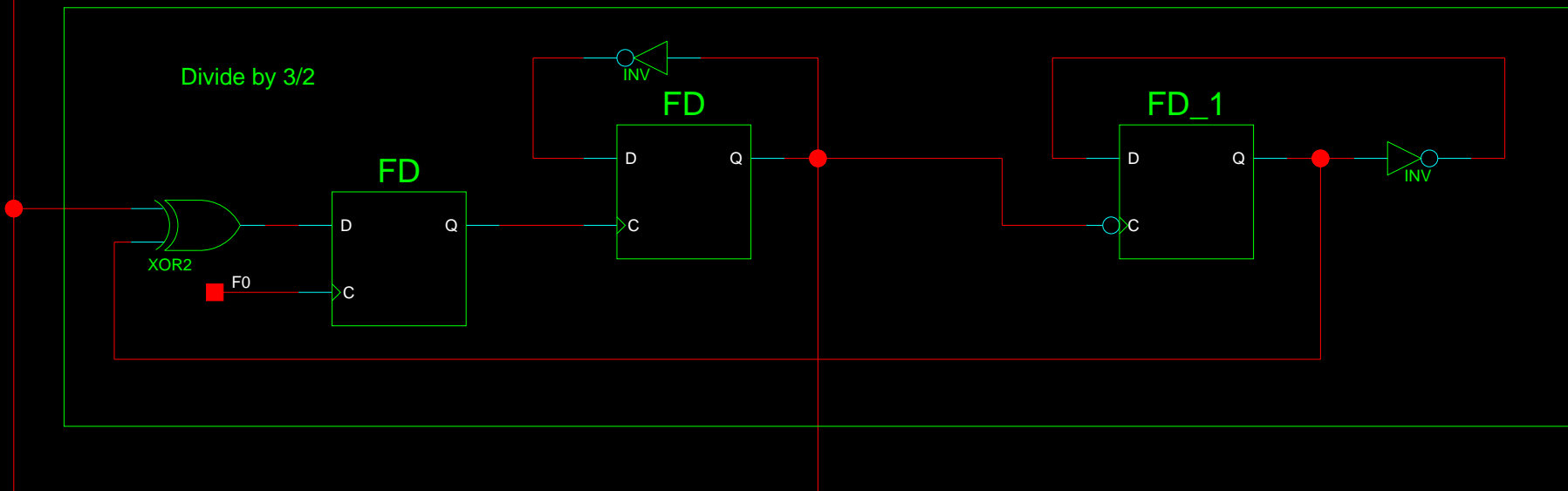
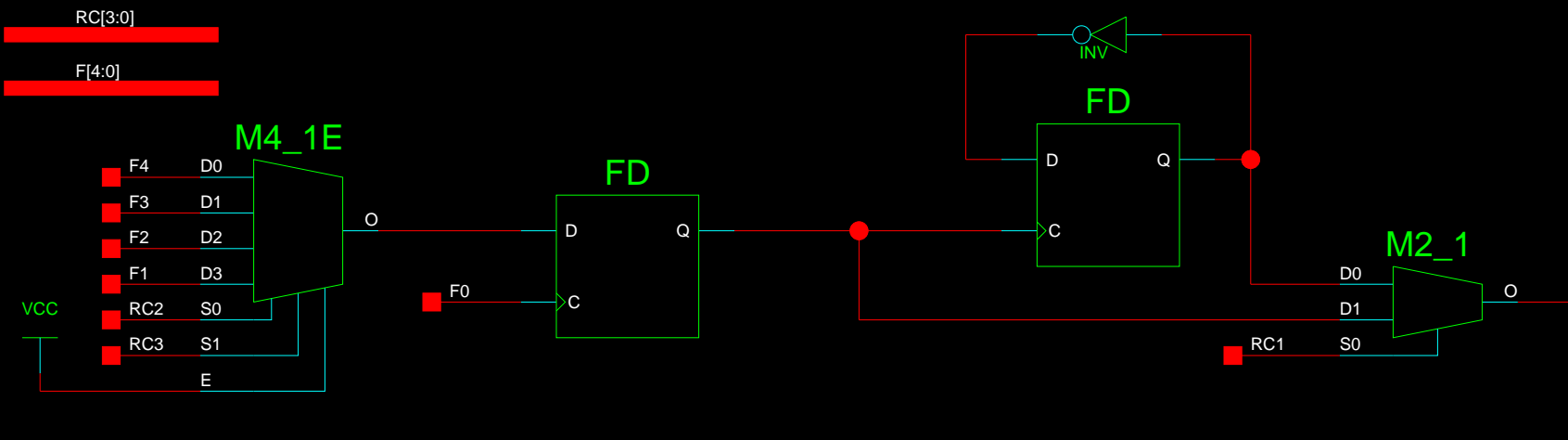
TITLE
STEPCLK.1

Indexer Channel Step Gen

PREPARED			size		Code ID No.	Drawing No.	rev
CHECKED			A			A34-7005	A
ENGINEER	NTA		STEPCLK		Scale	sheet	1 of 1
1-19-2006_15:35						14	11

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HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE **RATEGEN.1**
Channel Rate Selection

PREPARED			
CHECKED			
ENGINEER	NTA		
RATEGEN		size AI	Code ID No.
1-19-2006_15:37		Scale	Drawing No. A34-7005
			rev A
			sheet 1 of 1 <small>18 of 18</small>

4

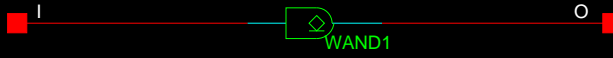
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Title: XC4000 Family BUFOD Macro

Comments: Open-Drain Buffer

Date: 6th April 1993

Ver: 1

Sheet Size: A

Rev: A

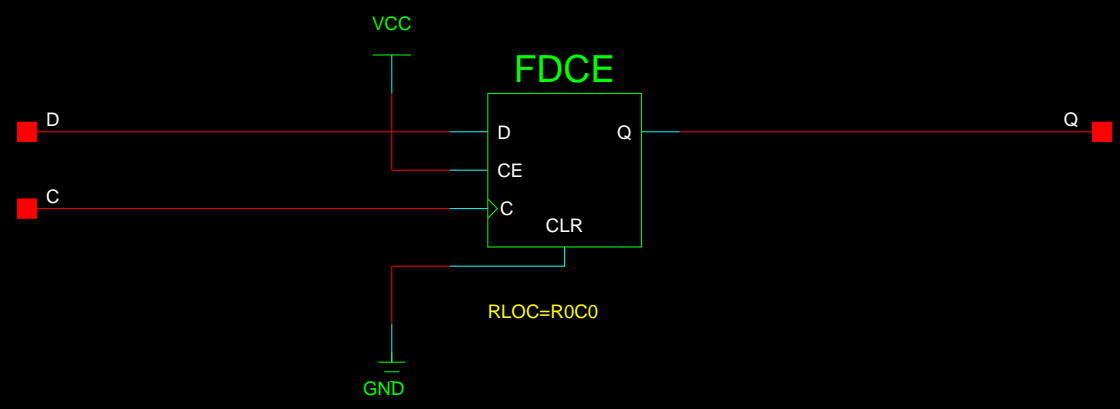
drawn by KS
Copyright (c) 1993, Xilinx Inc.

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Title: XC4000 Family FD Macro

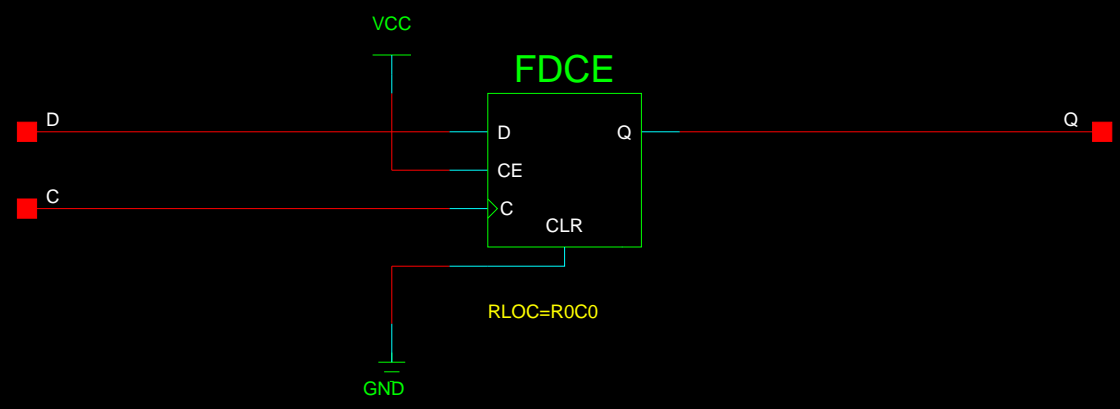
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

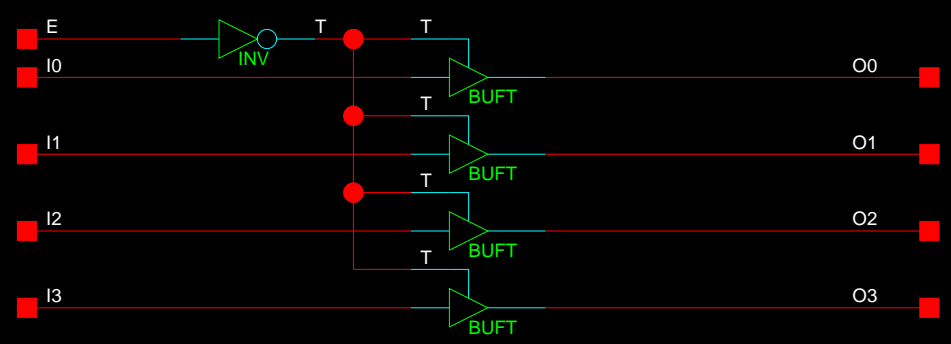
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family BUFE4 Macro

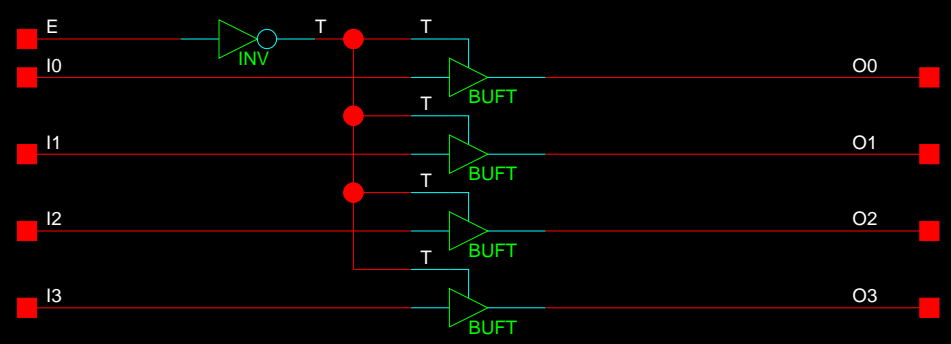
Comments: 4-Bit 3-State Buffer
w/ an Active High Enable

Date: 23rd February 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family BUFE4 Macro

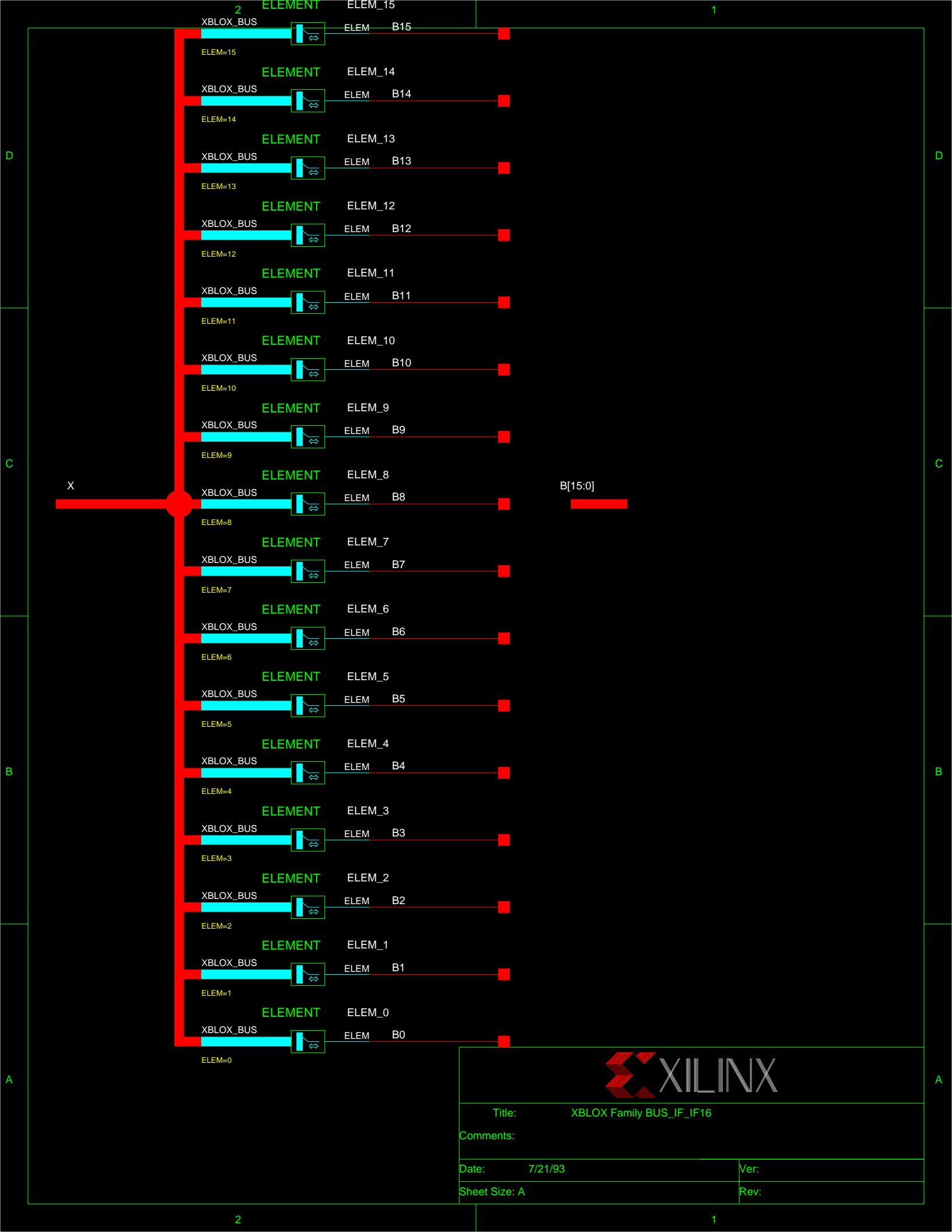
Comments: 4-Bit 3-State Buffer
w/ an Active High Enable

Date: 23rd February 1993

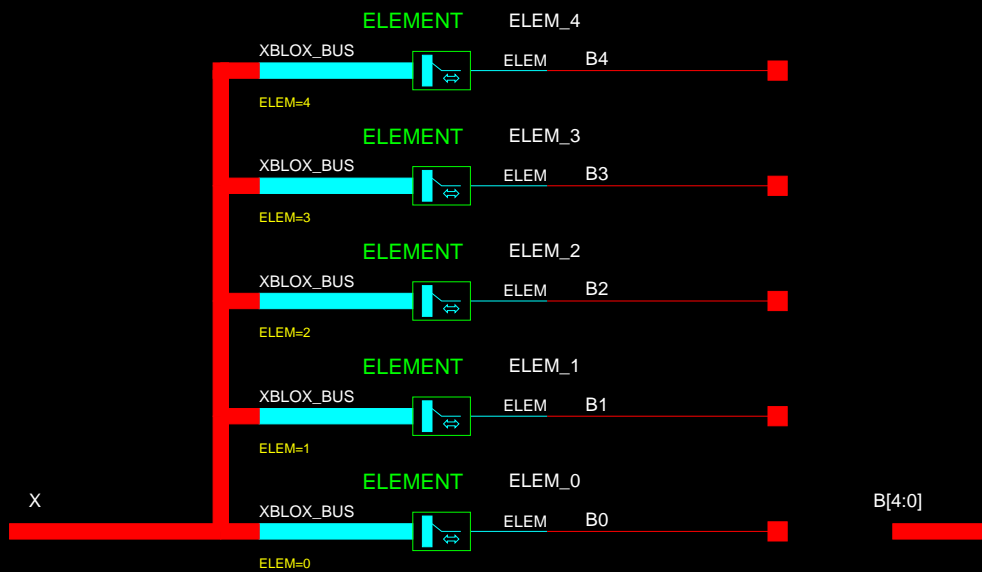
Ver: 1

Sheet Size: A

Rev: A



Title: XBLOX Family BUS_IF_IF16	
Comments:	
Date: 7/21/93	Ver:
Sheet Size: A	Rev:



Title: XBLOX Family BUS_IF05

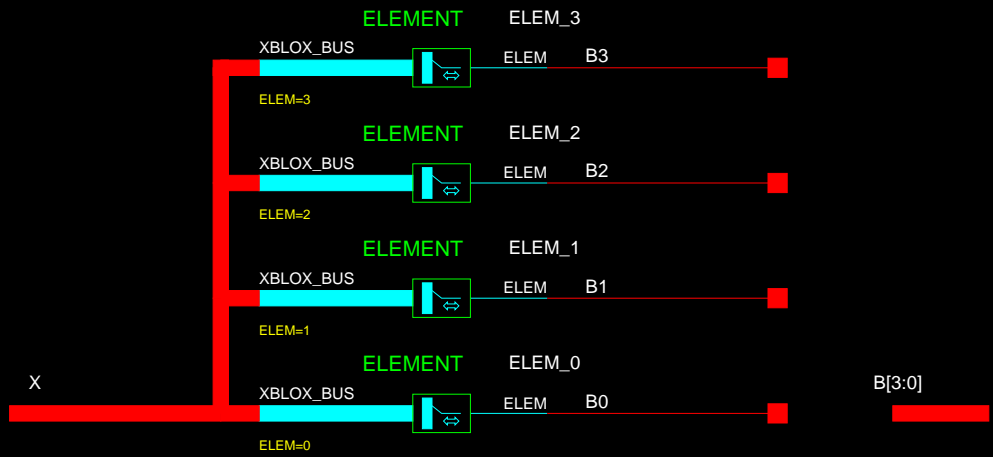
Comments:

Date: 7/21/93

Ver: 1

Sheet Size: A

Rev: A



Title: XBLOX Family BUS_IF04

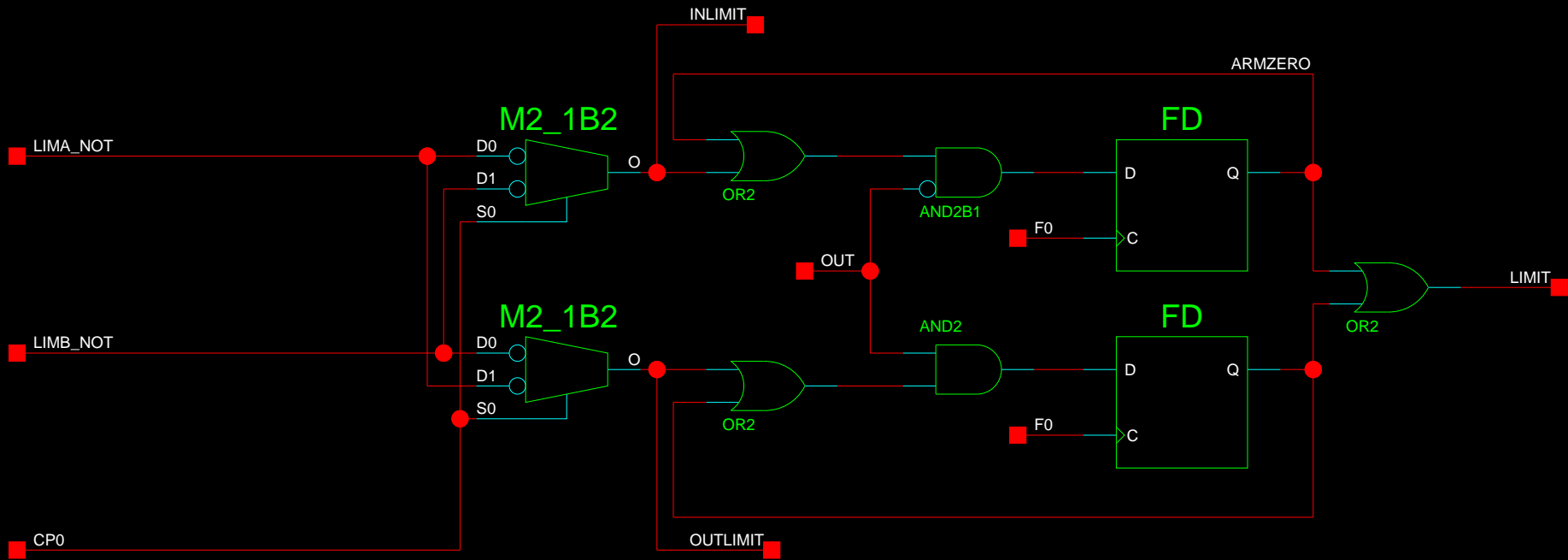
Comments:

Date: 7/21/93

Ver: 1

Sheet Size: A

Rev: A



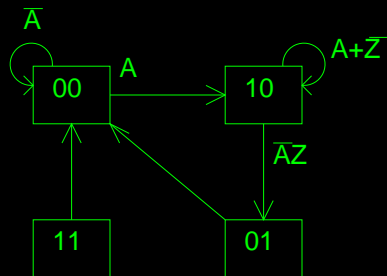
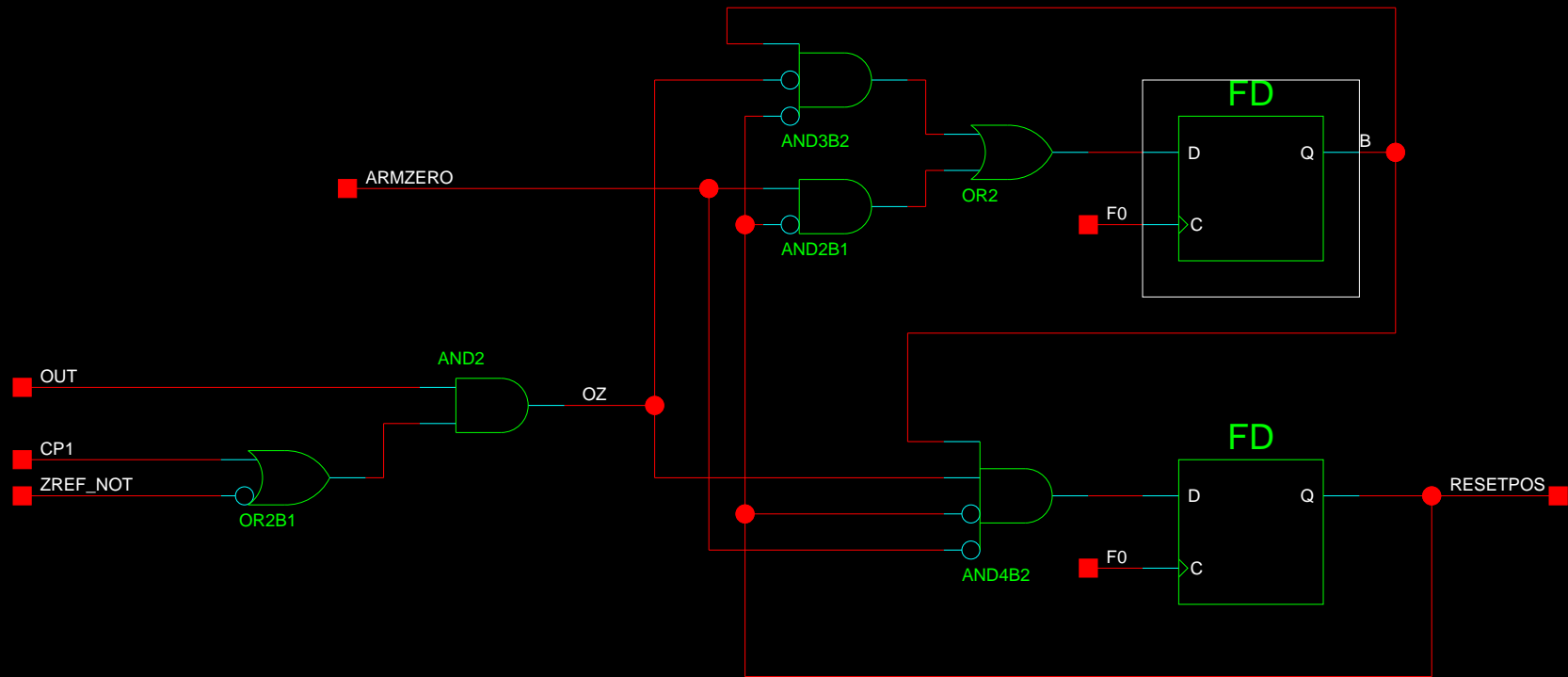
HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE
LIMITS.1

Indexer Limit Switches

PREPARED		
CHECKED		
ENGINEER	NTA	

LIMITS	size	Code ID No.	Drawing No.	rev
	AI		A34-7005	A
1-19-2006_15:35	Scale		sheet	1 of 12



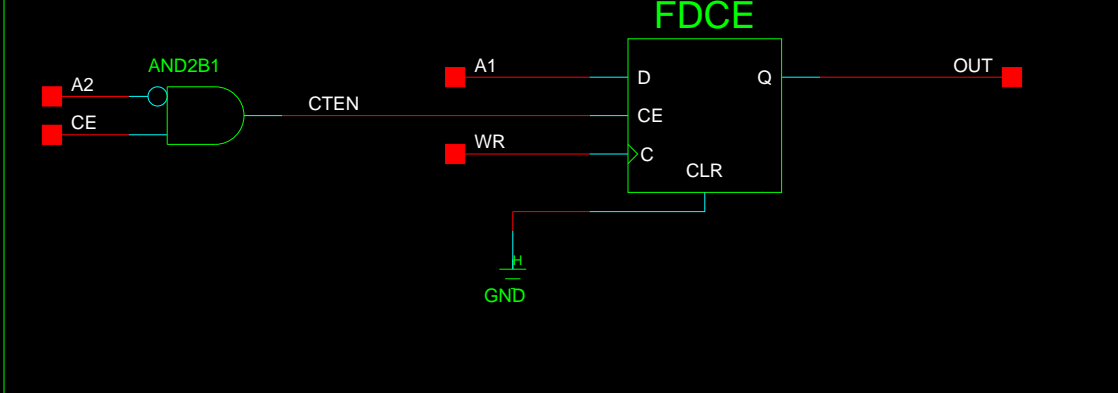
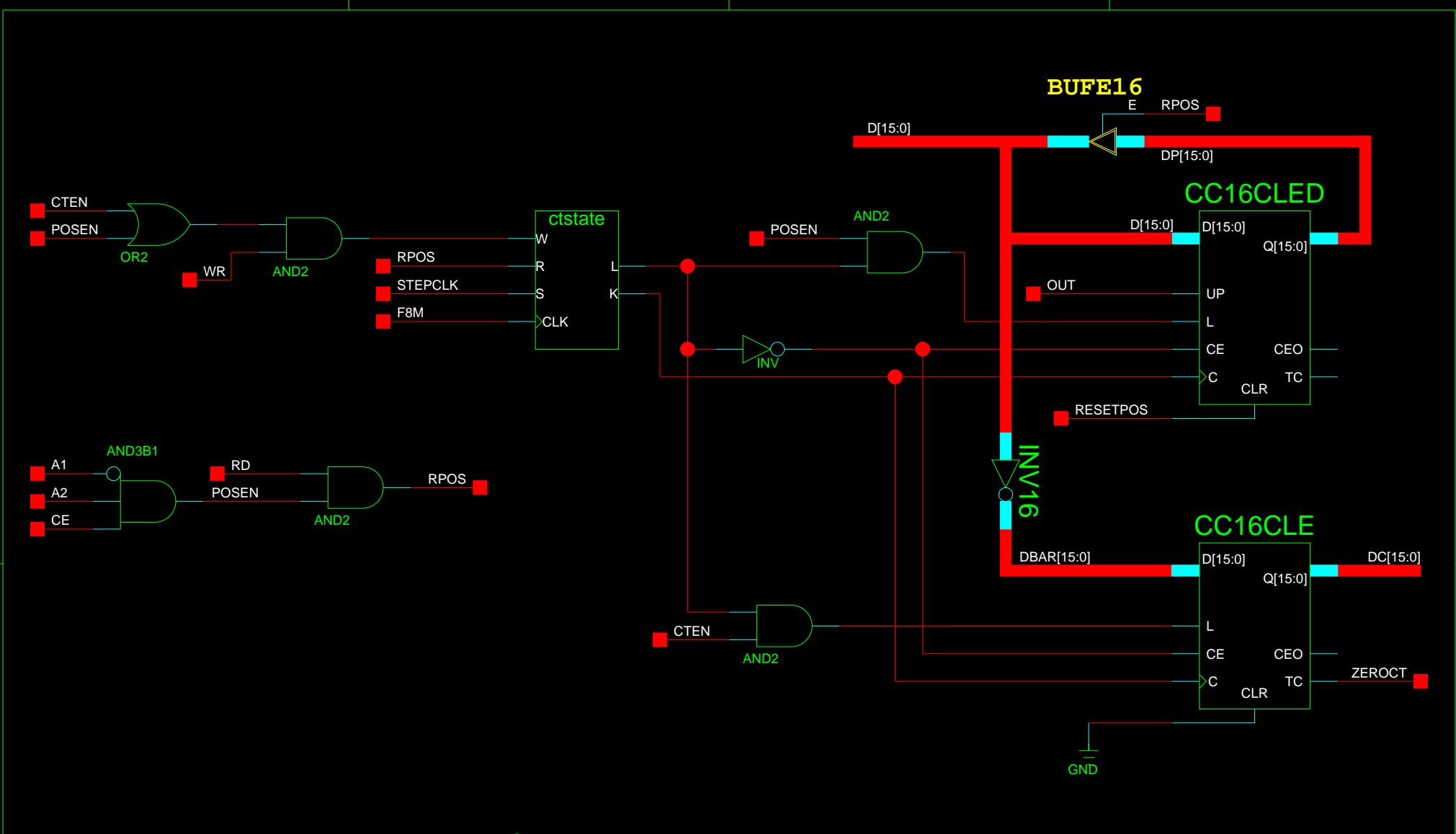
A: ARMZERO Input
 Z: OZ Qualified Zeroref
 00
 — RESETPOS Output
 — B "Zero Armed" State

HARVARD UNIVERSITY
 ATMOSPHERIC RESEARCH PROJECT

TITLE
LIMITS.2
 Indexer Limit Switches

PREPARED
 CHECKED
 ENGINEER **NTA**

LIMITS		size	Code ID No.	Drawing No.	rev
1-19-2006_15:36		AI		A34-7005	A
Scale				sheet 2 of 2	13 of 18



HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

CTRS.1
Indexer Channel Counters

PREPARED		TITLE	
CHECKED		CTRS.1	
ENGINEER	NTA	Indexer Channel Counters	
CTRS		size	Code ID No.
		AI	A34-7005
1-19-2006_15:37		Drawing No.	A
		Scale	sheet 1 of 1
		15 of 18	

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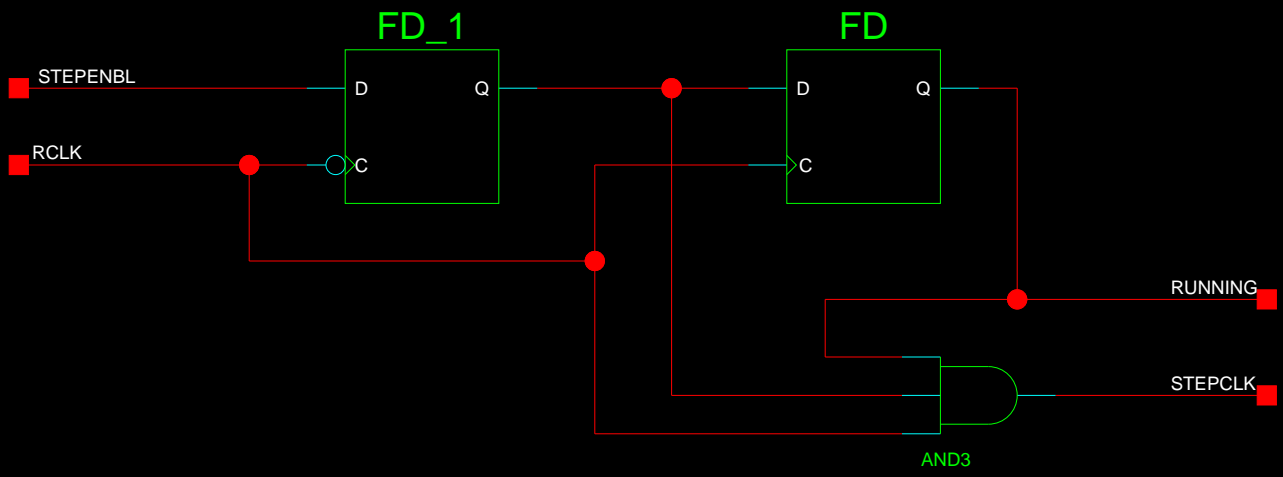
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HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE **STEPCLK.1**

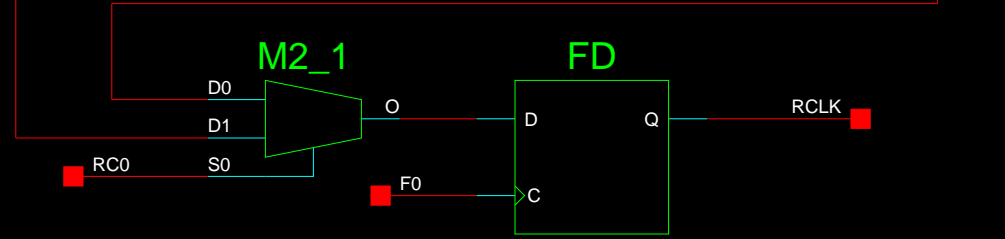
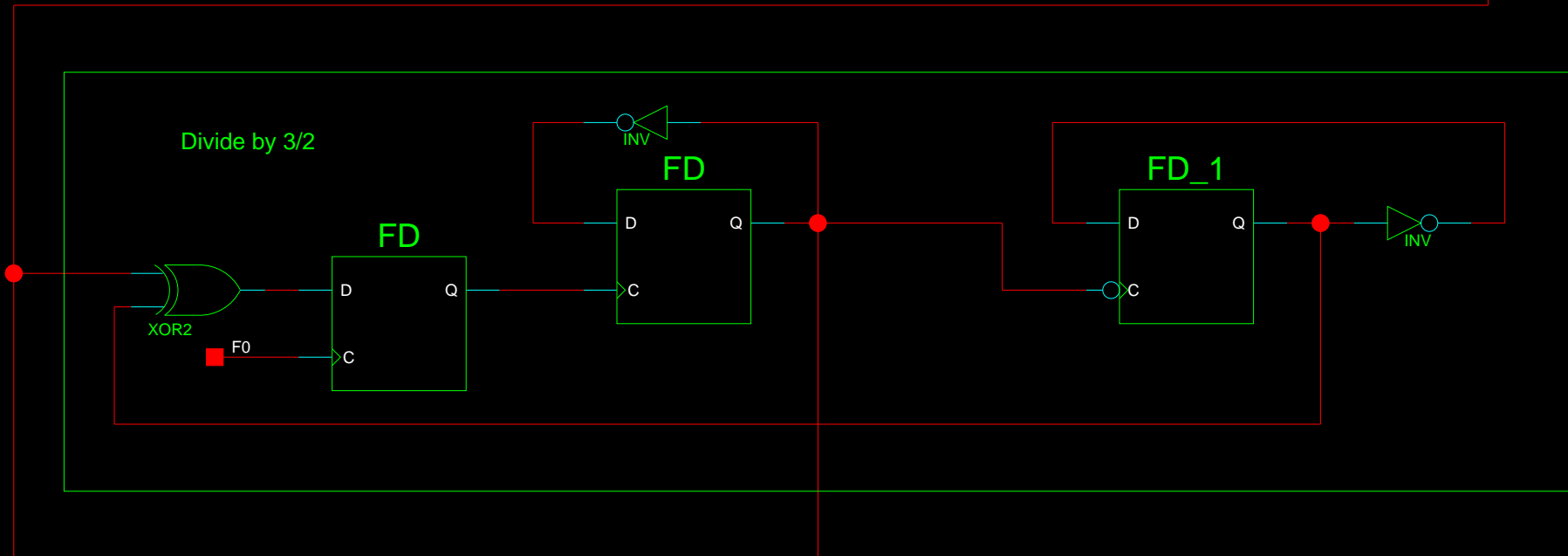
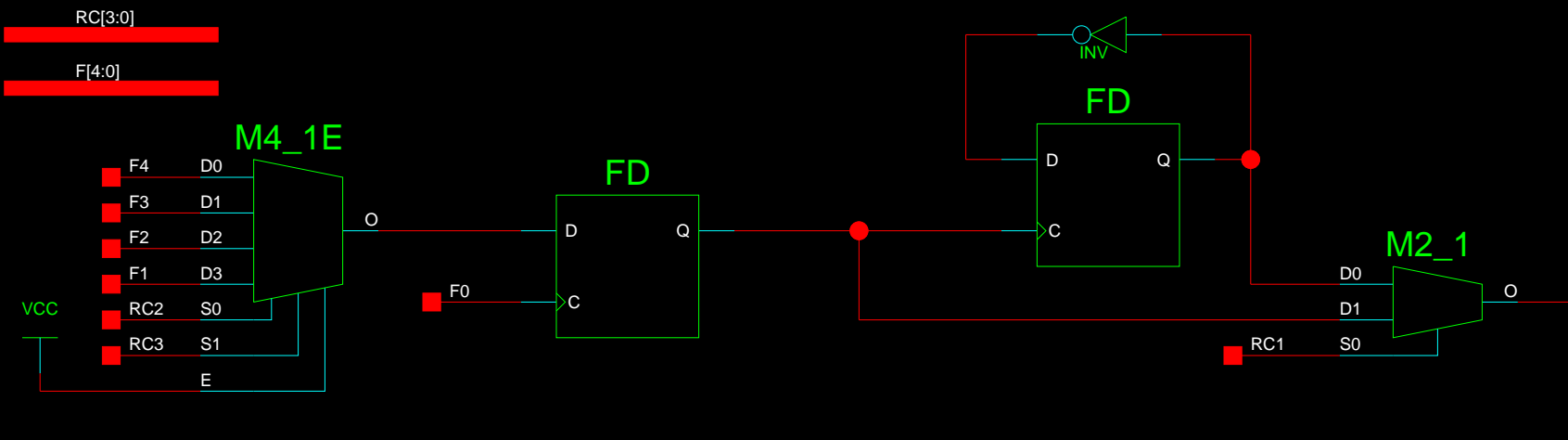
Indexer Channel Step Gen

PREPARED		
CHECKED		
ENGINEER	NTA	
STEPCLK		
1-19-2006_15:35		

size	Code ID No.	Drawing No.	rev
A		A34-7005	A
Scale		sheet	1 of 1

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ATMOSPHERIC RESEARCH PROJECT

TITLE **RATEGEN.1**
Channel Rate Selection

PREPARED			
CHECKED			
ENGINEER	NTA		
RATEGEN		size AI	Code ID No.
1-19-2006_15:37		Scale	Drawing No. A34-7005
			rev A
			sheet 1 of 1 <small>18 of 18</small>

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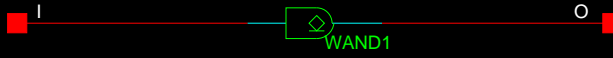
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Title: XC4000 Family BUFOD Macro

Comments: Open-Drain Buffer

Date: 6th April 1993

Ver: 1

Sheet Size: A

Rev: A

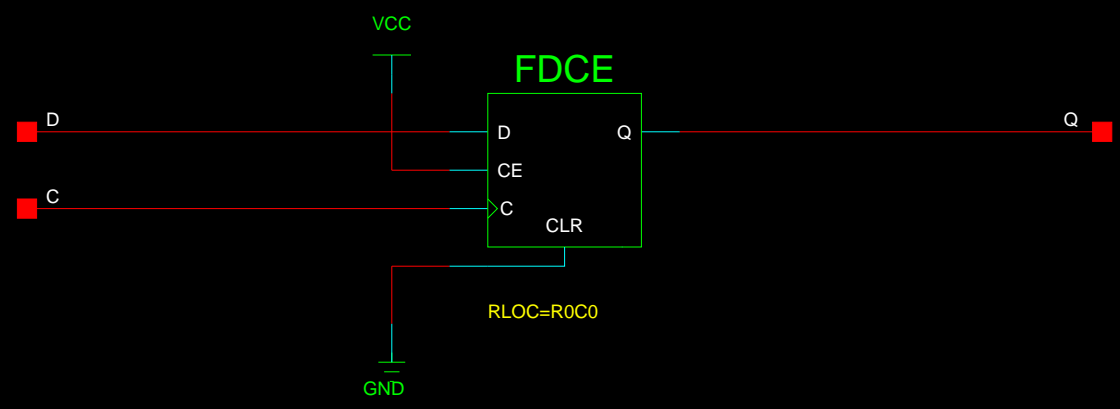
drawn by KS
Copyright (c) 1993, Xilinx Inc.

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Title: XC4000 Family FD Macro

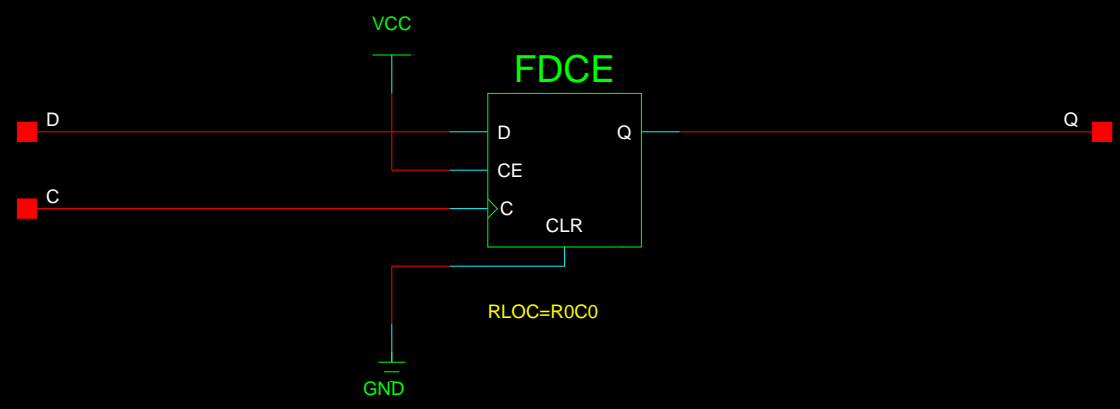
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Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

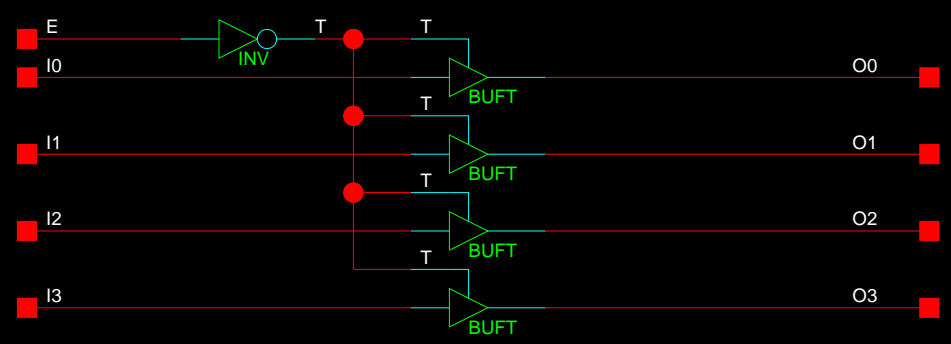
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family BUFE4 Macro

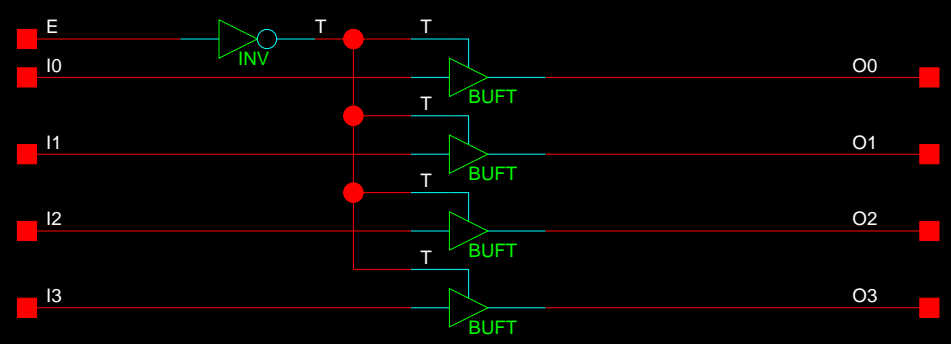
Comments: 4-Bit 3-State Buffer
w/ an Active High Enable

Date: 23rd February 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family BUFE4 Macro

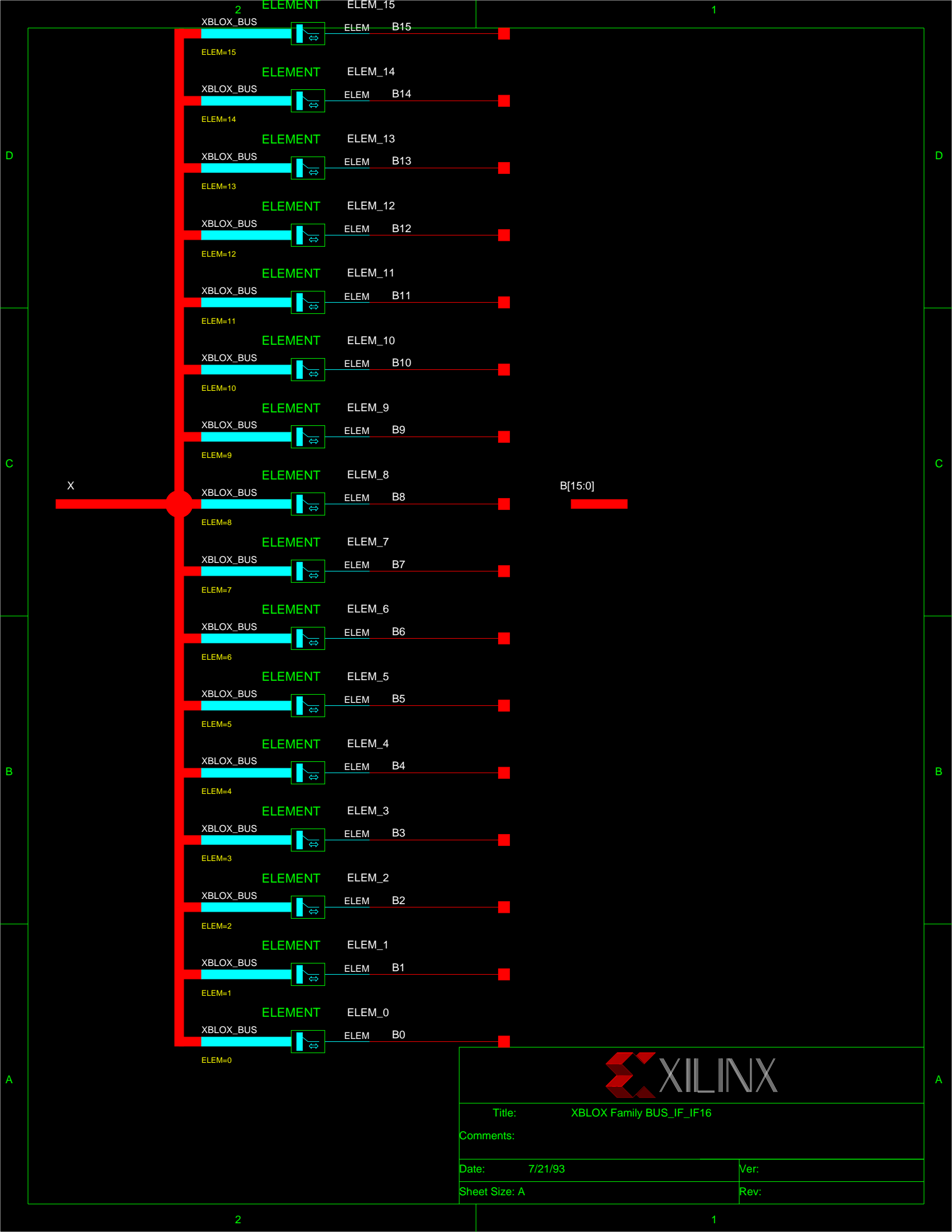
Comments: 4-Bit 3-State Buffer
w/ an Active High Enable

Date: 23rd February 1993

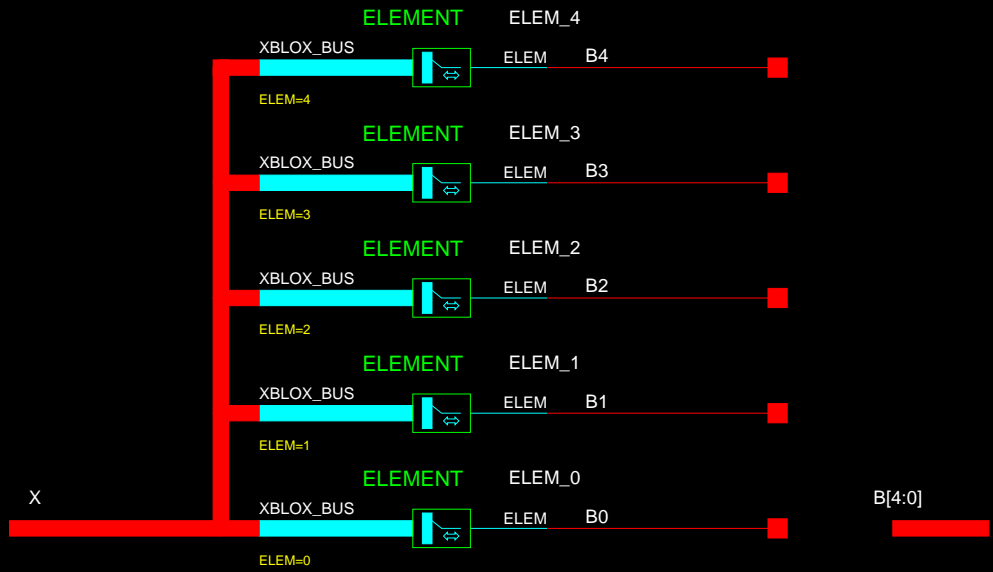
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Rev: A



Title: XBLOX Family BUS_IF_IF16	
Comments:	
Date: 7/21/93	Ver:
Sheet Size: A	Rev:



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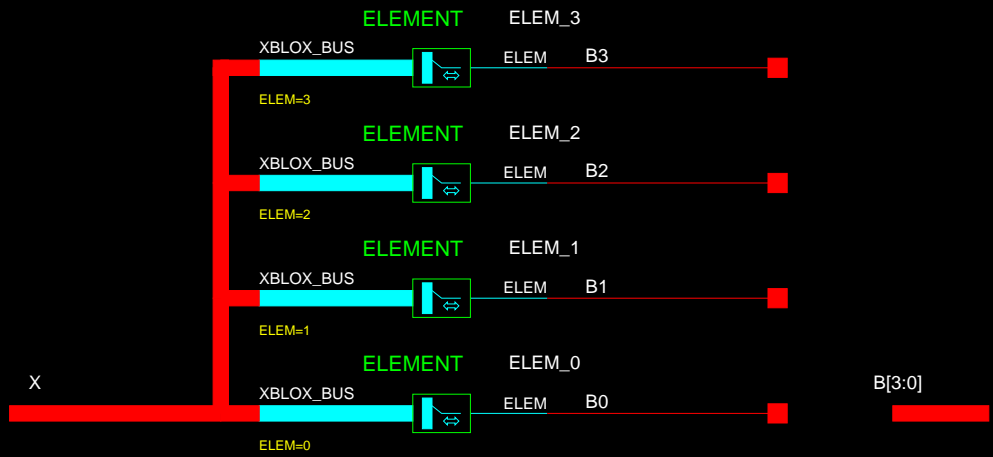
Comments:

Date: 7/21/93

Ver: 1

Sheet Size: A

Rev: A



Title: XBLOX Family BUS_IF04

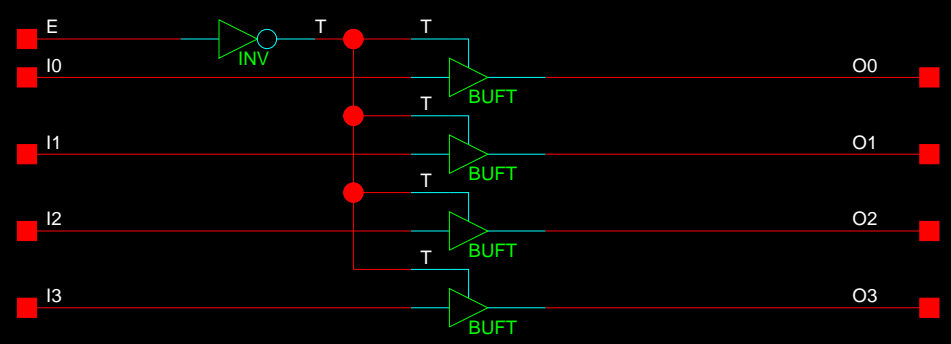
Comments:

Date: 7/21/93

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family BUFE4 Macro

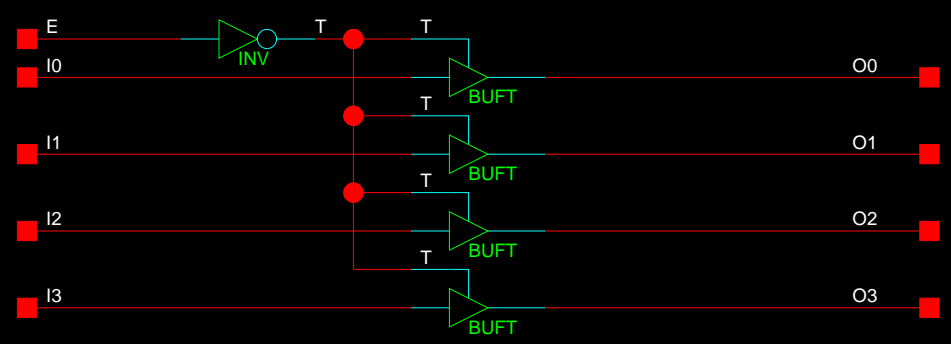
Comments: 4-Bit 3-State Buffer
w/ an Active High Enable

Date: 23rd February 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family BUFE4 Macro

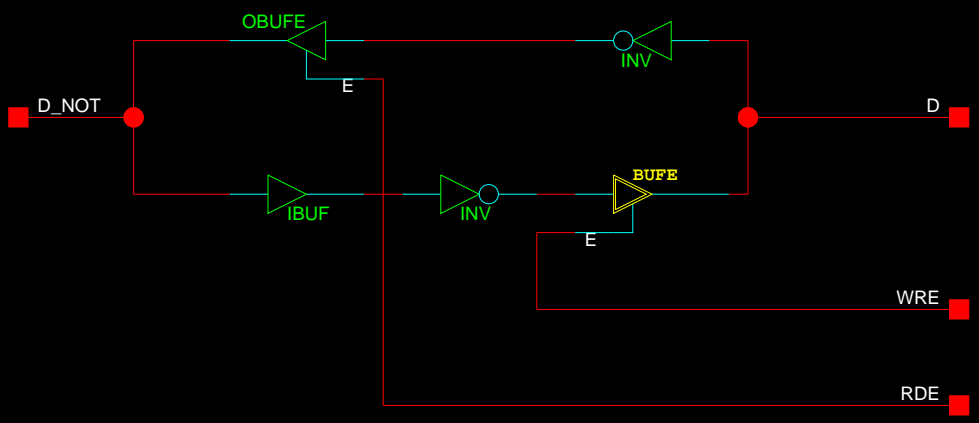
Comments: 4-Bit 3-State Buffer
w/ an Active High Enable

Date: 23rd February 1993

Ver: 1

Sheet Size: A

Rev: A



HARVARD UNIVERSITY
 ATMOSPHERIC RESEARCH PROJECT

TITLE **IOBITA.1**

Data Bus Bit Interface

PREPARED		
CHECKED		
ENGINEER	NTA	

IOBITA

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Code ID No.

Drawing No.
A34-7005

rev
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1-19-2006_15:17

Scale

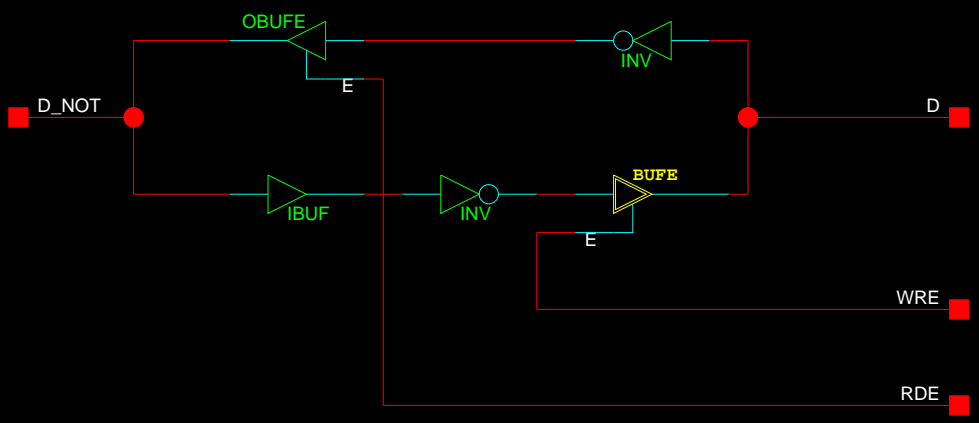
sheet 1 of 1

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HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE **IOBITA.1**

Data Bus Bit Interface

PREPARED		
CHECKED		
ENGINEER	NTA	

IOBITA

size
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Code ID No.

Drawing No.
A34-7005

rev
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1-19-2006_15:17

Scale

sheet

17 of 181

A

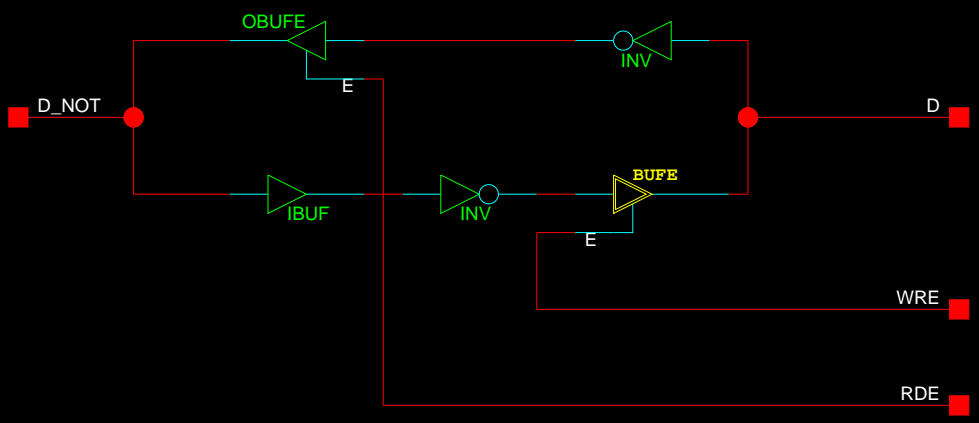
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HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE
IOBITA.1
 Data Bus Bit Interface

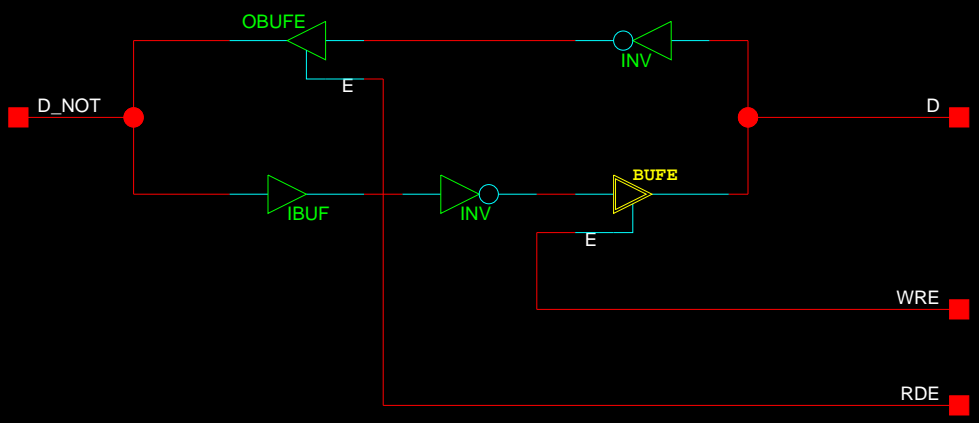
PREPARED		
CHECKED		
ENGINEER	NTA	

IOBITA
 1-19-2006_15:17

size A	Code ID No.	Drawing No. A34-7005	rev A
Scale		sheet	17 of 181

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HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE **IOBITA.1**

Data Bus Bit Interface

PREPARED		
CHECKED		
ENGINEER	NTA	

IOBITA
1-19-2006_15:17

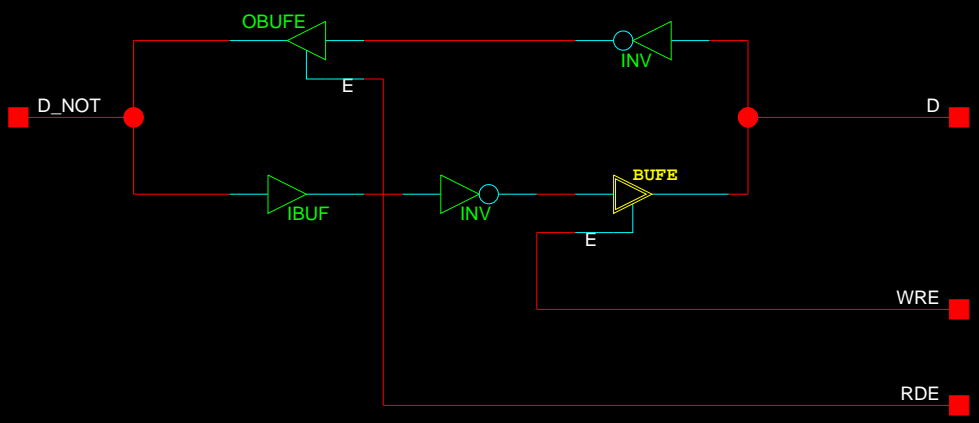
size A	Code ID No.	Drawing No. A34-7005	rev A
Scale		sheet	17 of 181

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HARVARD UNIVERSITY
 ATMOSPHERIC RESEARCH PROJECT

TITLE
IOBITA.1
 Data Bus Bit Interface

PREPARED		
CHECKED		
ENGINEER	NTA	

IOBITA
 1-19-2006_15:17

size A	Code ID No.	Drawing No. A34-7005	rev A
Scale		sheet	17 of 181

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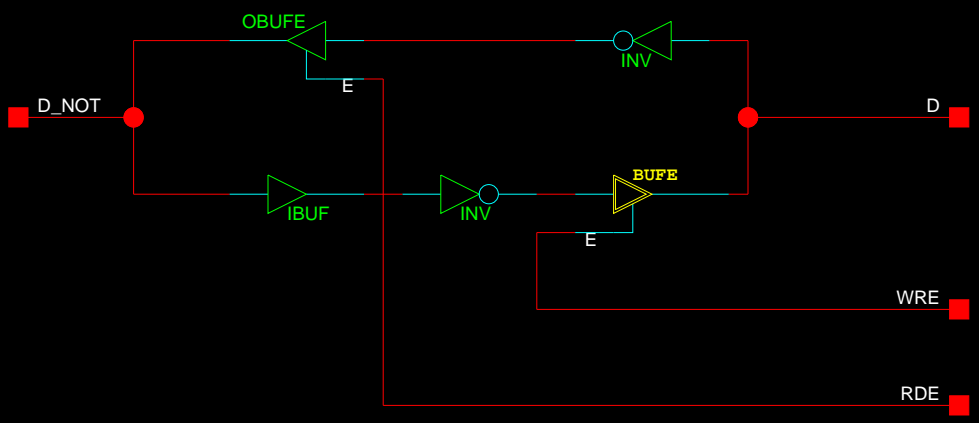
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HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE **IOBITA.1**

Data Bus Bit Interface

PREPARED		
CHECKED		
ENGINEER	NTA	

IOBITA

size
A

Code ID No.

Drawing No.
A34-7005

rev
A

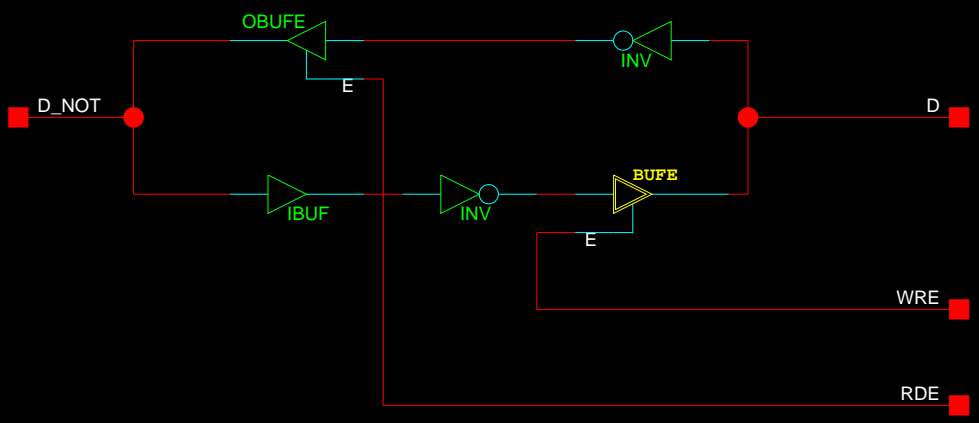
1-19-2006_15:17

Scale

sheet **17 of 181**

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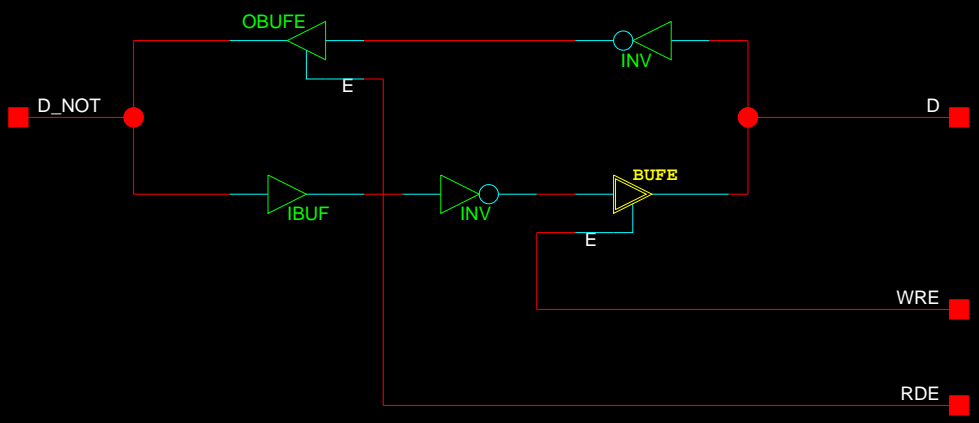
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ATMOSPHERIC RESEARCH PROJECT

TITLE
IOBITA.1
 Data Bus Bit Interface

PREPARED		
CHECKED		
ENGINEER	NTA	

IOBITA
 1-19-2006_15:17

size A	Code ID No.	Drawing No. A34-7005	rev A
Scale		sheet	17 of 181



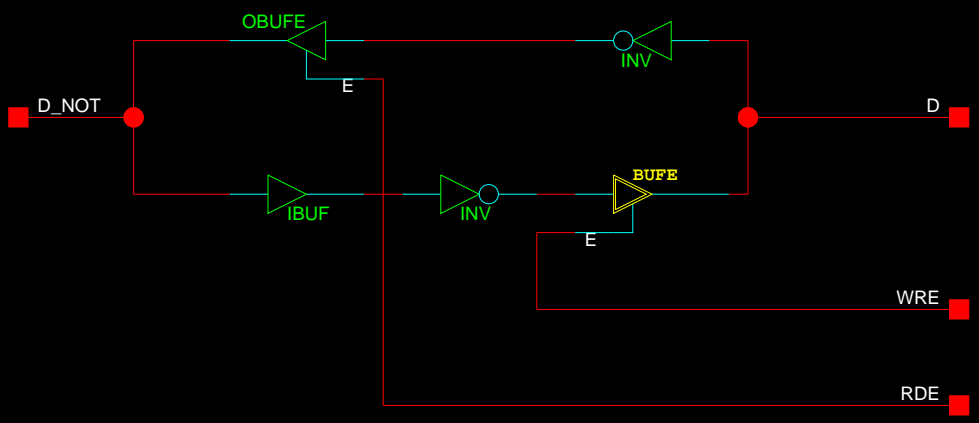
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ATMOSPHERIC RESEARCH PROJECT

TITLE
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 Data Bus Bit Interface

PREPARED		
CHECKED		
ENGINEER	NTA	

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size A	Code ID No.	Drawing No. A34-7005	rev A
Scale		sheet	17 of 181



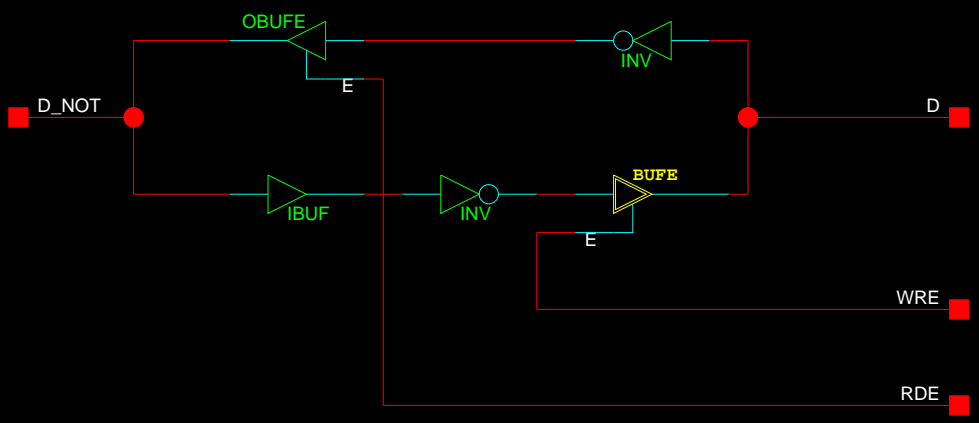
HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE
IOBITA.1
 Data Bus Bit Interface

PREPARED		
CHECKED		
ENGINEER	NTA	

IOBITA
 1-19-2006_15:17

size A	Code ID No.	Drawing No. A34-7005	rev A
Scale		sheet 1 of 1	



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ATMOSPHERIC RESEARCH PROJECT

TITLE **IOBITA.1**

Data Bus Bit Interface

PREPARED		
CHECKED		
ENGINEER	NTA	

IOBITA

size **A**

Code ID No.

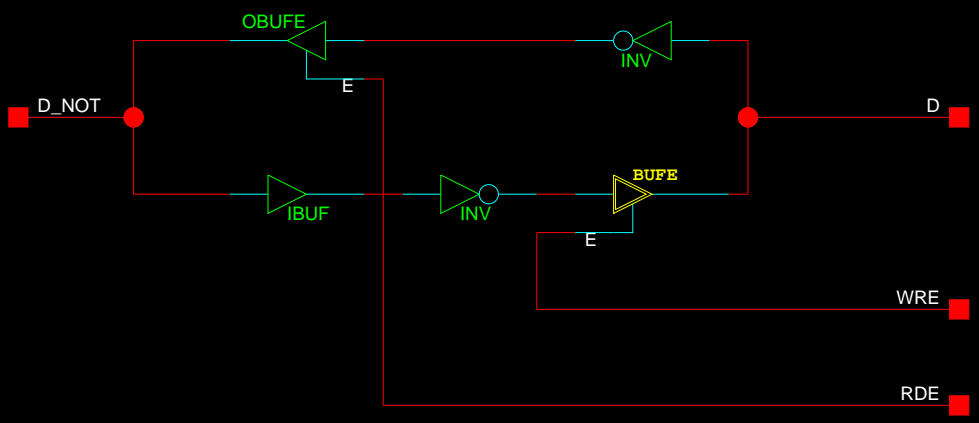
Drawing No. **A34-7005**

rev **A**

1-19-2006_15:17

Scale

sheet 1 of 1



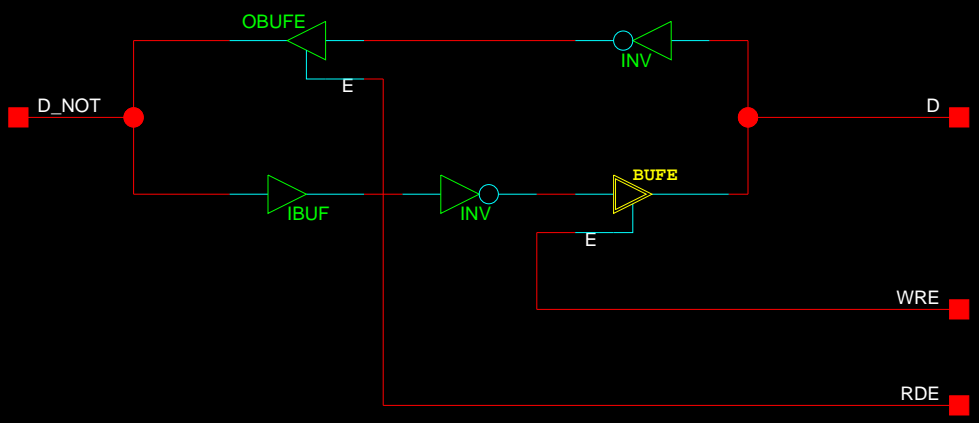
HARVARD UNIVERSITY
 ATMOSPHERIC RESEARCH PROJECT

TITLE
IOBITA.1
 Data Bus Bit Interface

PREPARED		
CHECKED		
ENGINEER	NTA	

IOBITA
 1-19-2006_15:17

size A	Code ID No.	Drawing No. A34-7005	rev A
Scale		sheet 1 of 1	



HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE **IOBITA.1**

Data Bus Bit Interface

PREPARED		
CHECKED		
ENGINEER	NTA	

IOBITA

size **A**

Code ID No.

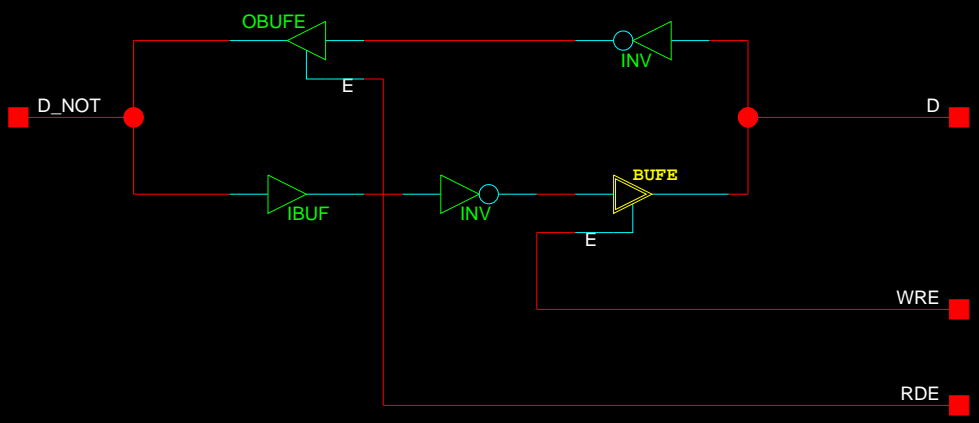
Drawing No. **A34-7005**

rev **A**

1-19-2006_15:17

Scale

sheet 1 of 1



HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE **IOBITA.1**

Data Bus Bit Interface

PREPARED		
CHECKED		
ENGINEER	NTA	

IOBITA

size
A

Code ID No.

Drawing No.
A34-7005

rev
A

1-19-2006_15:17

Scale

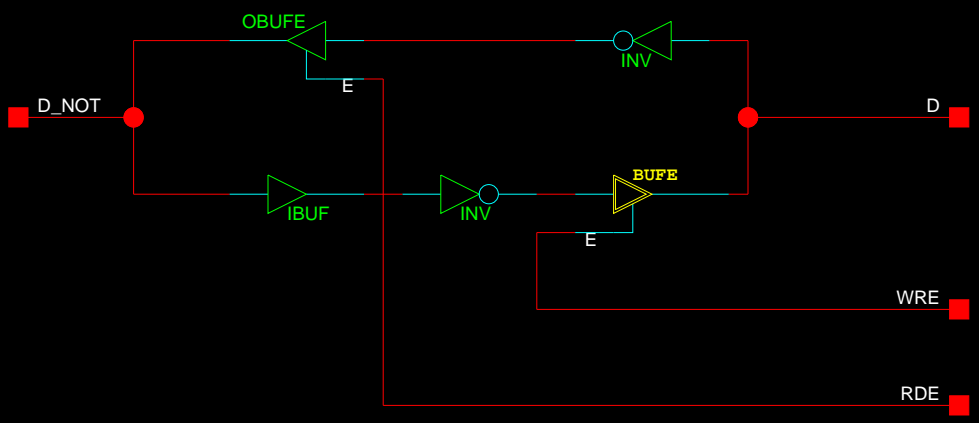
sheet **17 of 181**

A

B

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HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE
IOBITA.1
 Data Bus Bit Interface

PREPARED		
CHECKED		
ENGINEER	NTA	

IOBITA
 1-19-2006_15:17

size A	Code ID No.	Drawing No. A34-7005	rev A
Scale		sheet	17 of 181

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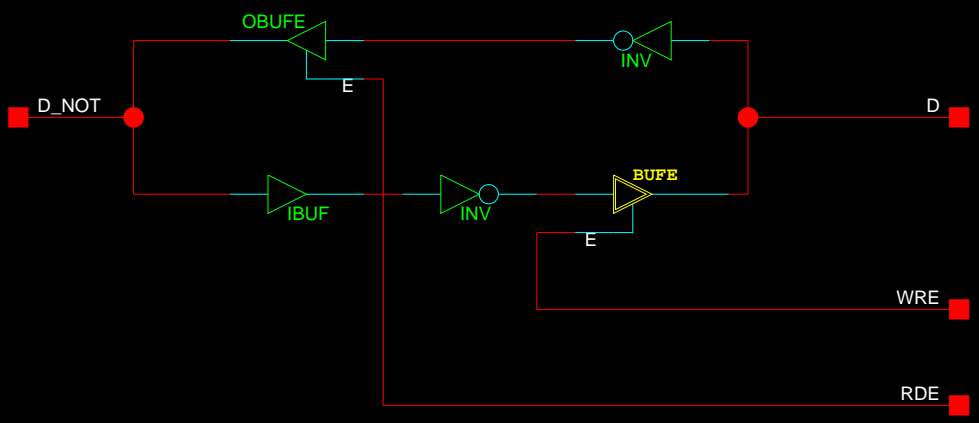
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HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE
IOBITA.1
 Data Bus Bit Interface

PREPARED		
CHECKED		
ENGINEER	NTA	

IOBITA
 1-19-2006_15:17

size A	Code ID No.	Drawing No. A34-7005	rev A
Scale		sheet	17 of 181

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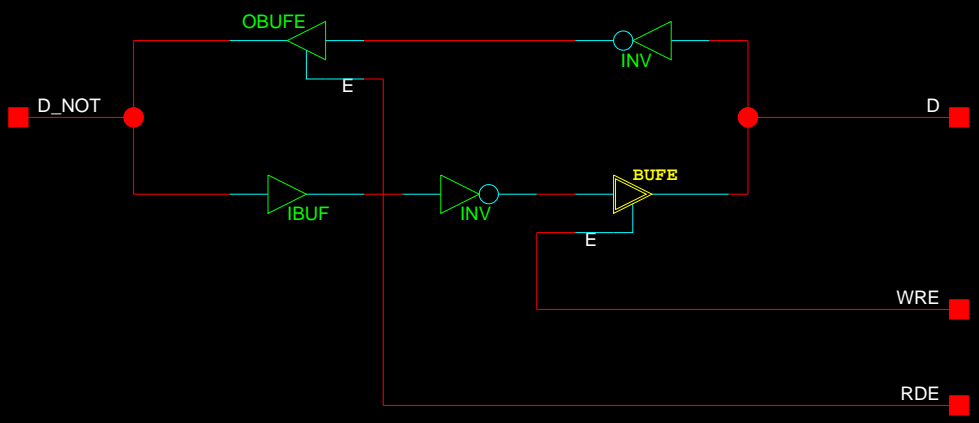
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HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE
IOBITA.1
 Data Bus Bit Interface

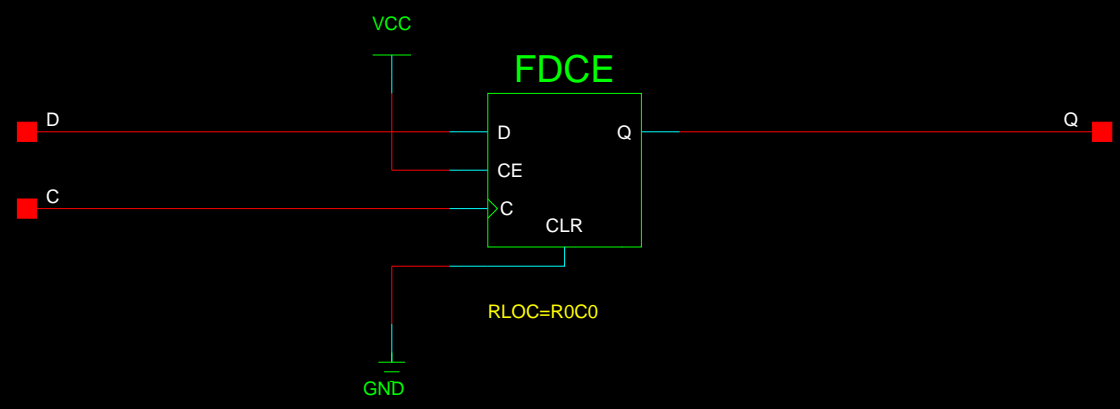
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ENGINEER	NTA	

IOBITA
 1-19-2006_15:17

size A	Code ID No.	Drawing No. A34-7005	rev A
Scale		sheet	17 of 181

A

B



Title: XC4000 Family FD Macro

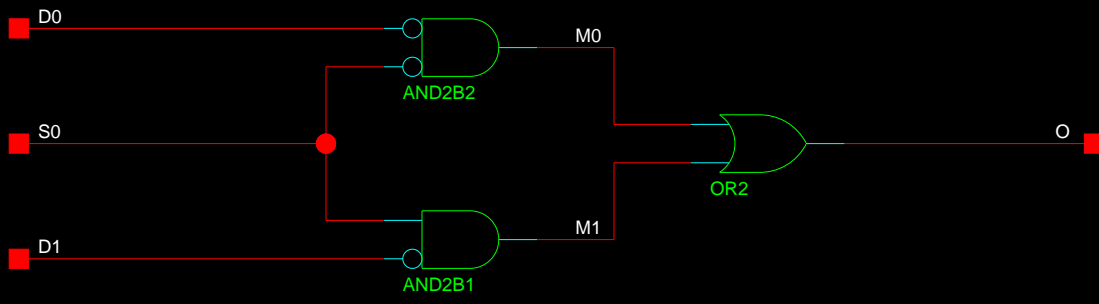
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Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M2_1B2 Macro

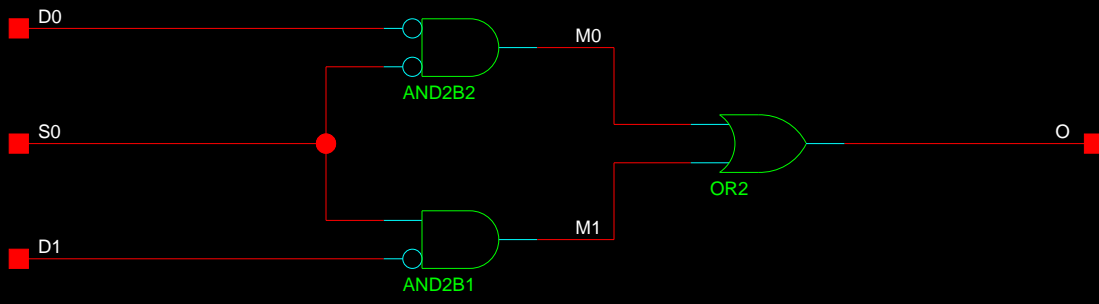
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Date: 1st March 1993

Ver: 1

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Rev: A



Title: XC4000 Family M2_1B2 Macro

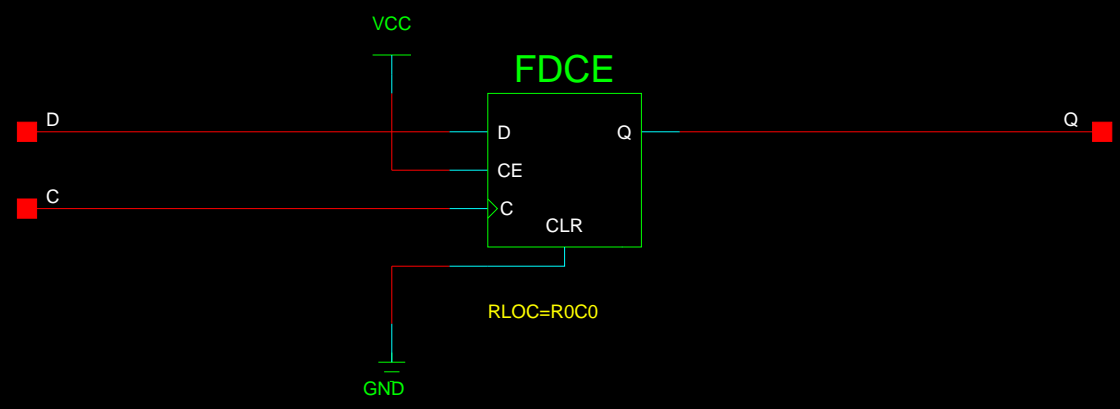
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Rev: A



Title: XC4000 Family FD Macro

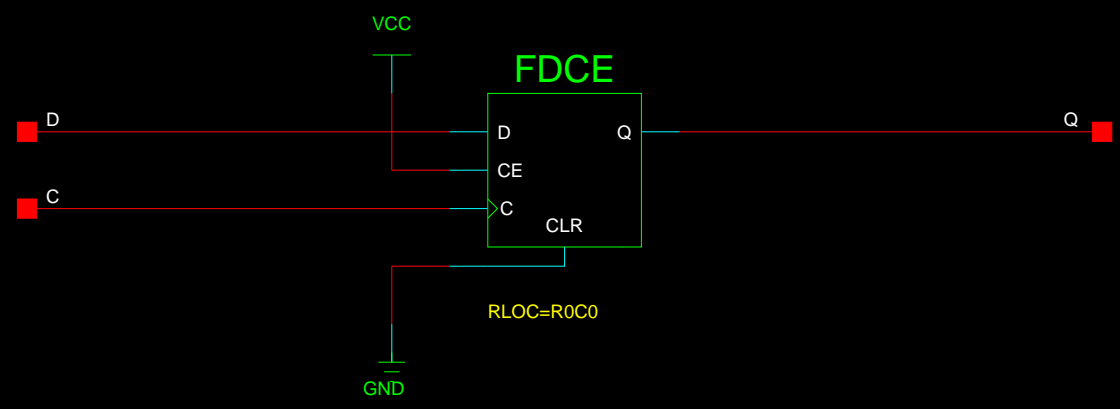
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Rev: A



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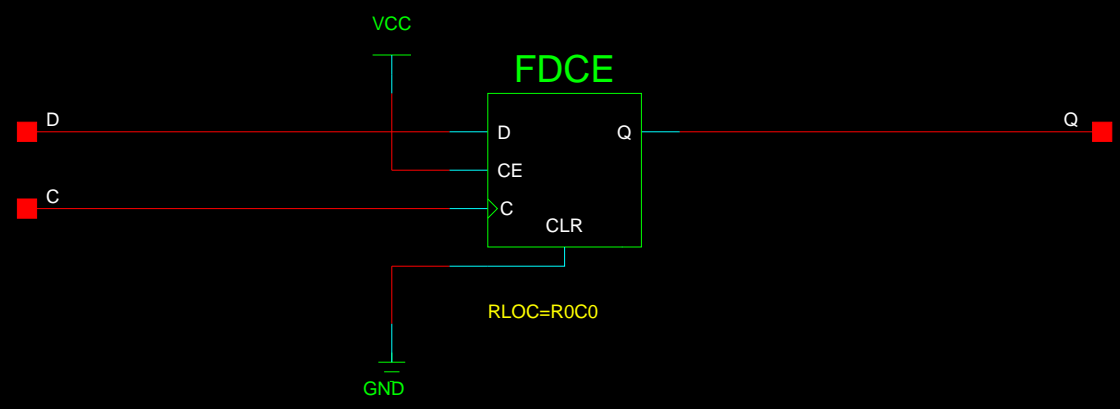
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Rev: A



Title: XC4000 Family FD Macro

Comments: D Flip-Flop

Date: 23rd December 1992

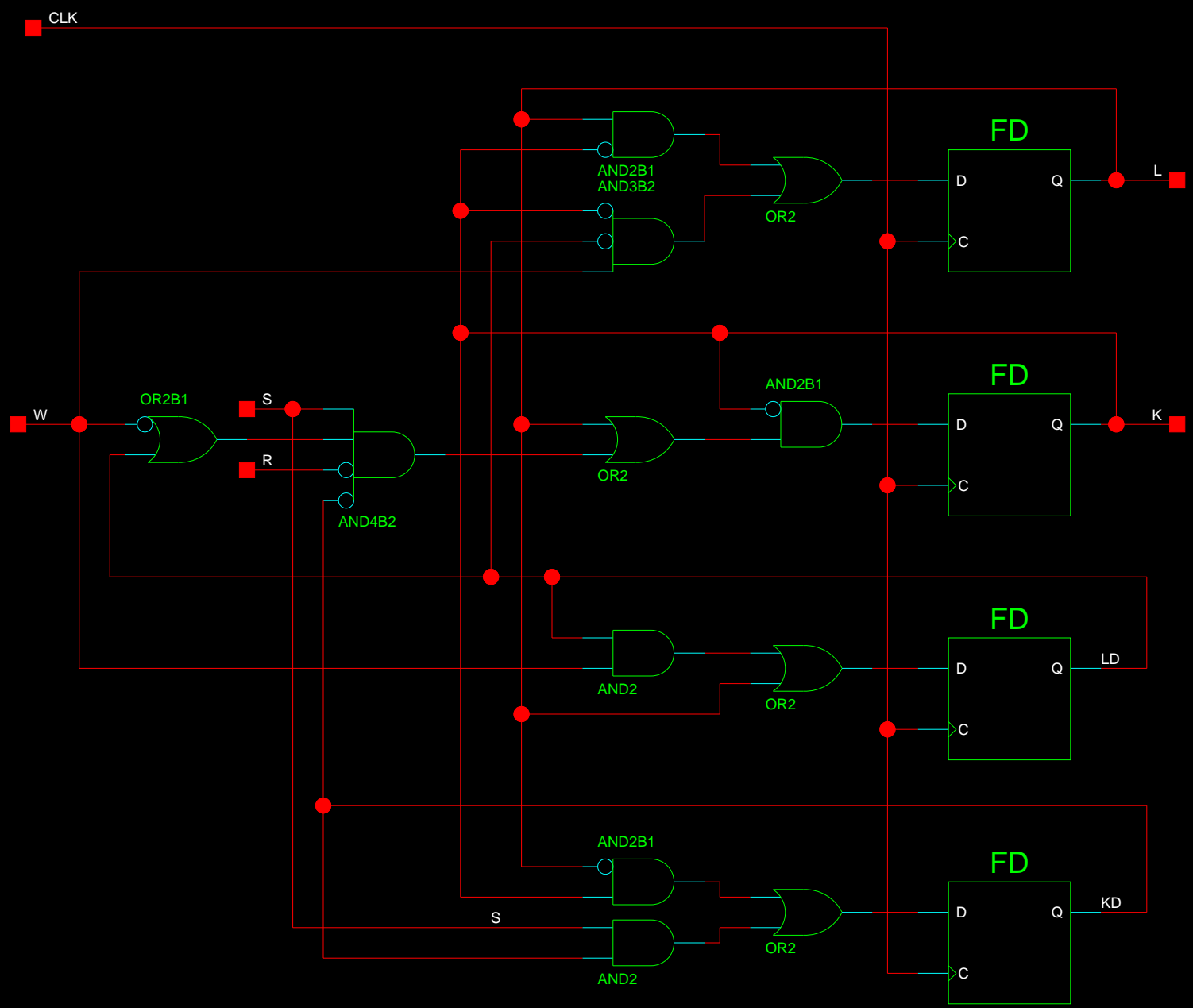
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Rev: A

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B



HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE

CTSTATE.1

State Machine for Counter Latch

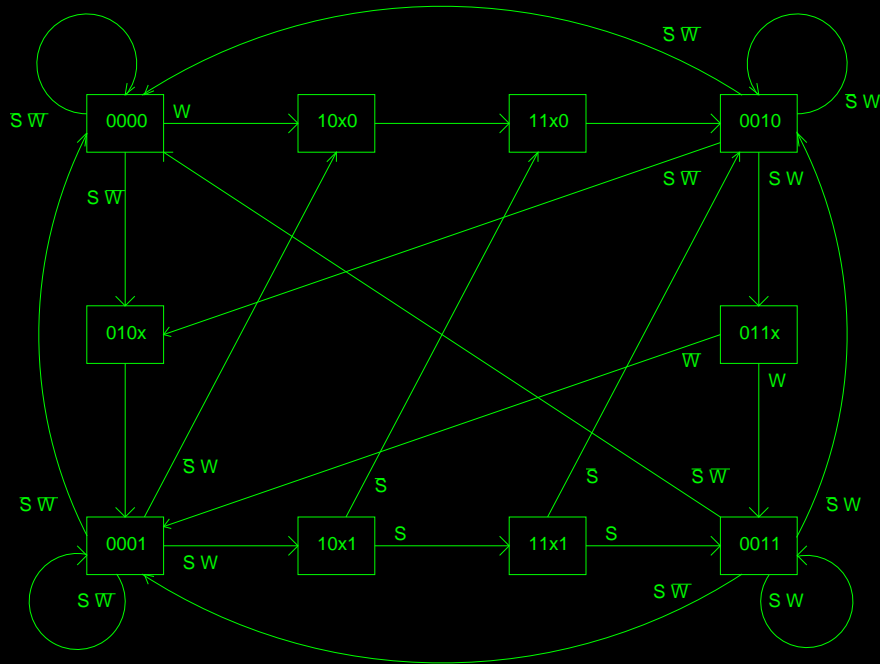
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CHECKED		
ENGINEER	NTA	

ASHEET

4-20-2005_16:20

size	Code ID No.	Drawing No.	rev
A		A34-7005	A

Scale sheet 1 of 1



LEGEND:

S: Step Clock Input

W: Write Input

L: Latch Output

K: Counter Clock Output

LD: Latched State

KD: Clocked State

10x0

x in a state block indicates
"either" as a destination and
"both" as a source.

HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE

CTSTATE.2

State Machine for Counter Latch

PREPARED		
CHECKED		
ENGINEER	NTA	

ASHEET		size	Code ID No.	Drawing No.	rev
4-20-2005_16:20		A		A34-7005	A
Scale			sheet 1 of 1		

D

D

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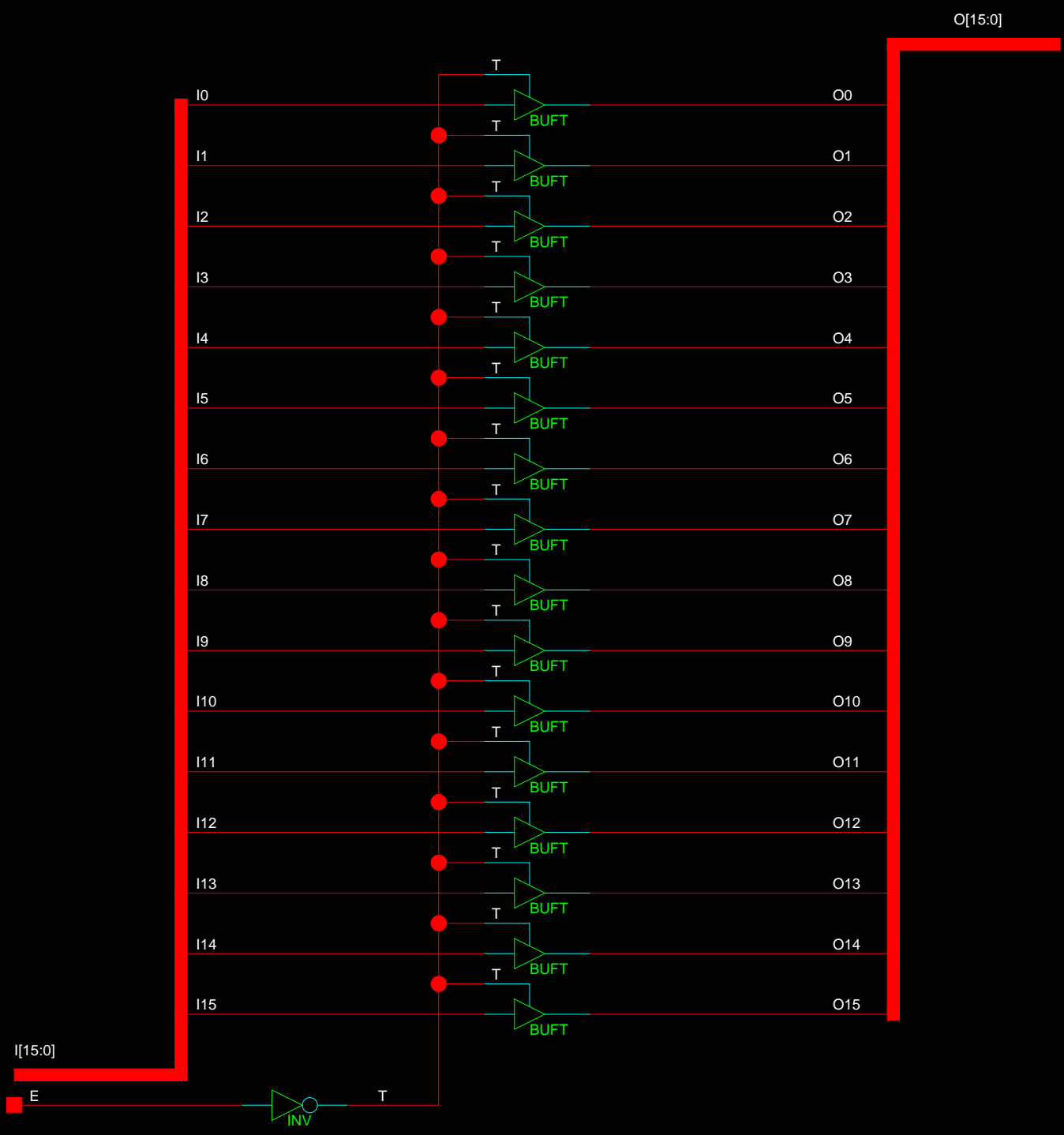
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Title:	XC4000 Family BUFE16 Macro		
Comments:	16-Bit 3-State Buffer w/ an Active High Enable		
Date:	23rd February 1993	Ver:	1
Sheet Size:	A	Rev:	A

D

D

C

C

B

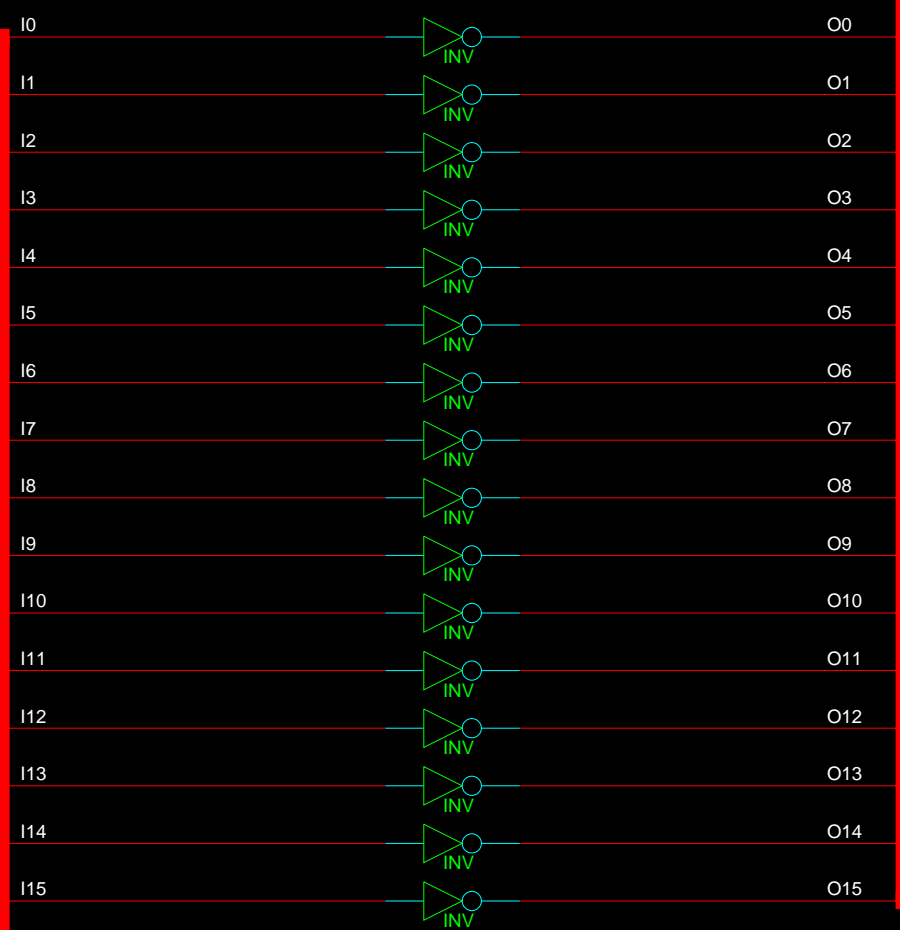
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A

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O[15:0]

I[15:0]

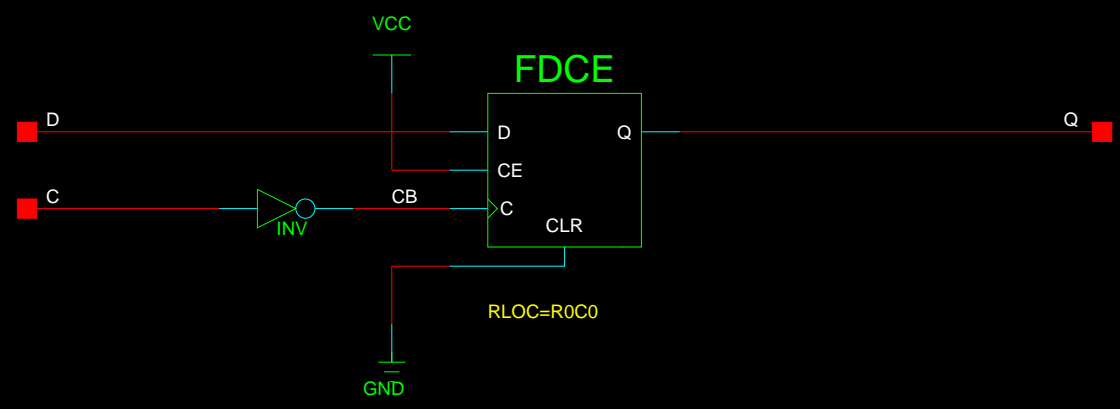


Title: XC4000 Family INV16 Macro

Comments: 16-bit Inverter

Date:	22nd February 1993	Ver:	1
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Sheet Size: A	Rev:	A
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Title: XC4000 Family FD_1 Macro

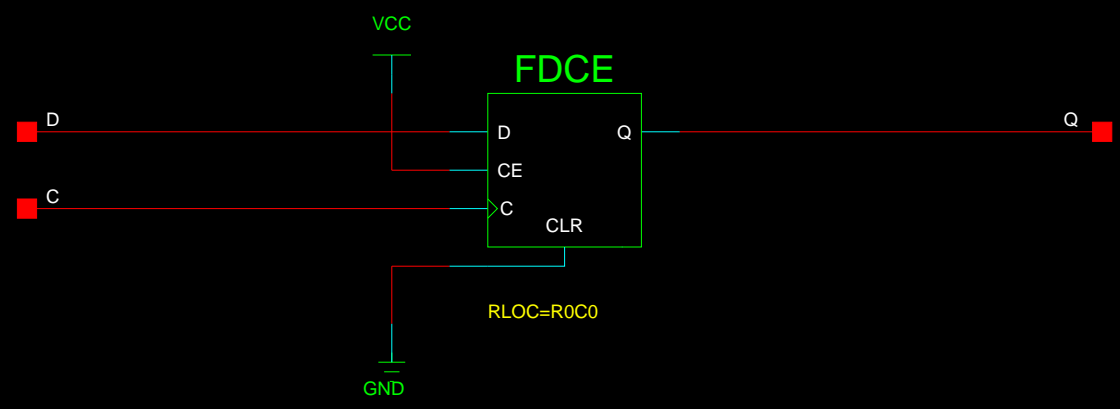
Comments: D Flip-Flop w/ an inverted Clock

Date: 1st March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

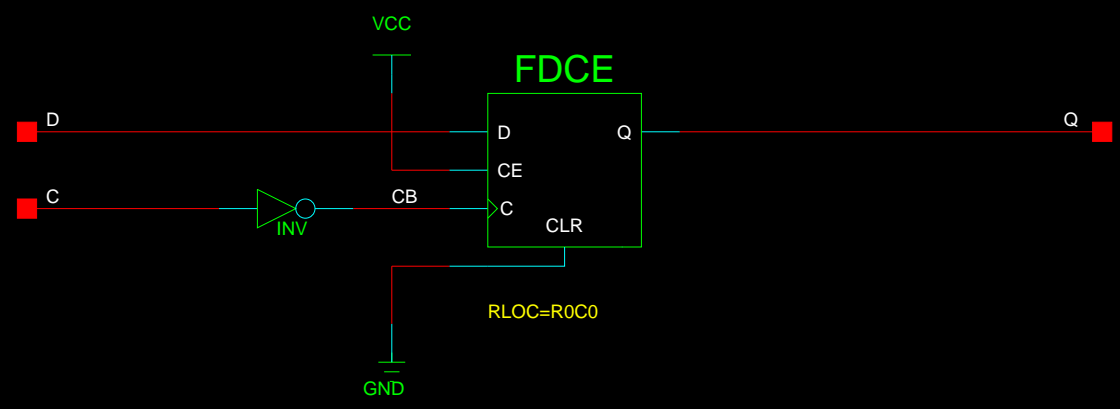
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD_1 Macro

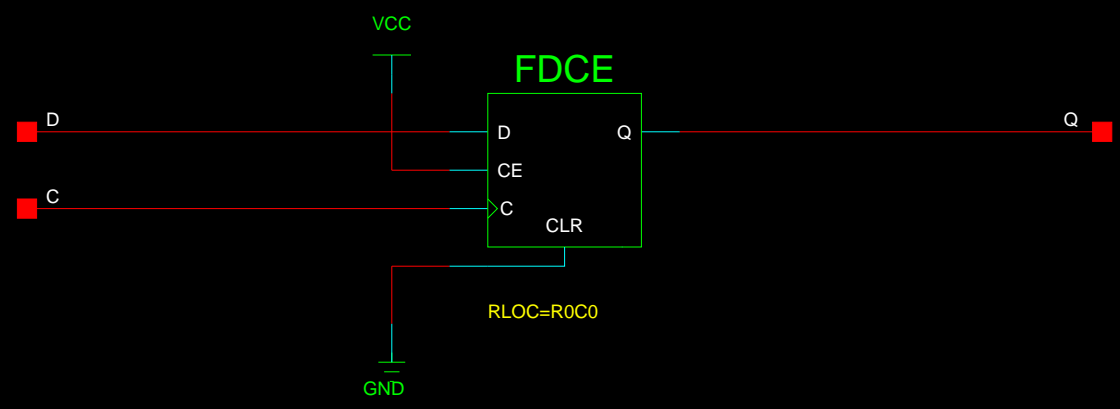
Comments: D Flip-Flop w/ an inverted Clock

Date: 1st March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

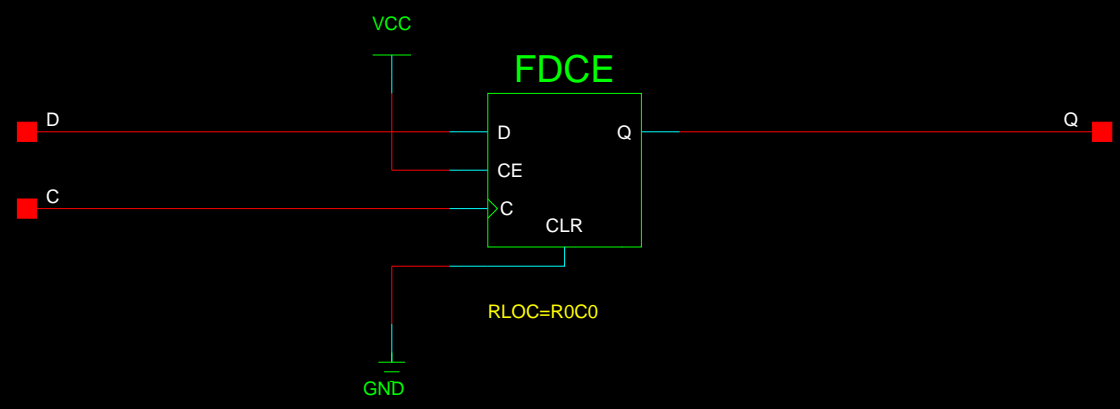
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Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

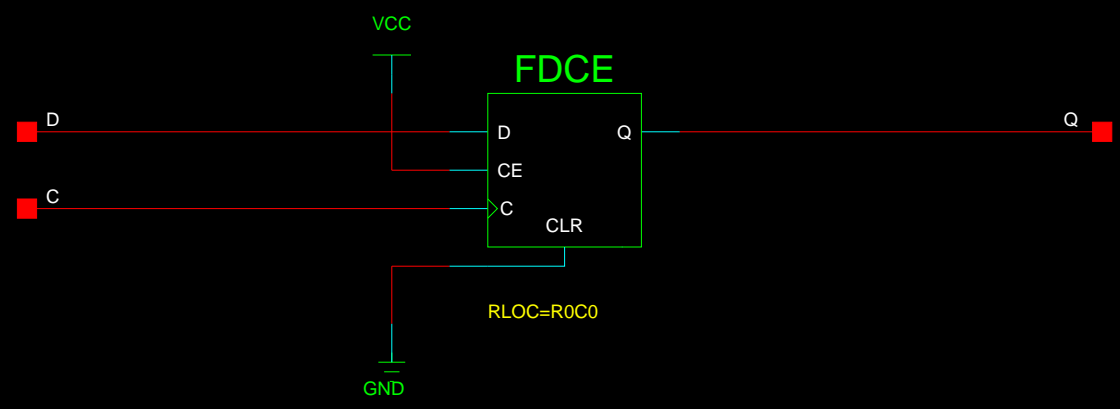
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Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

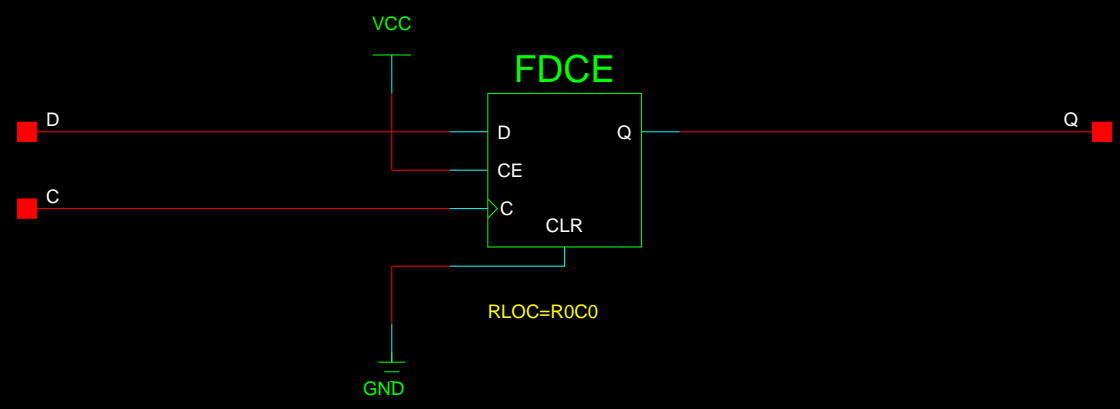
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M2_1 Macro

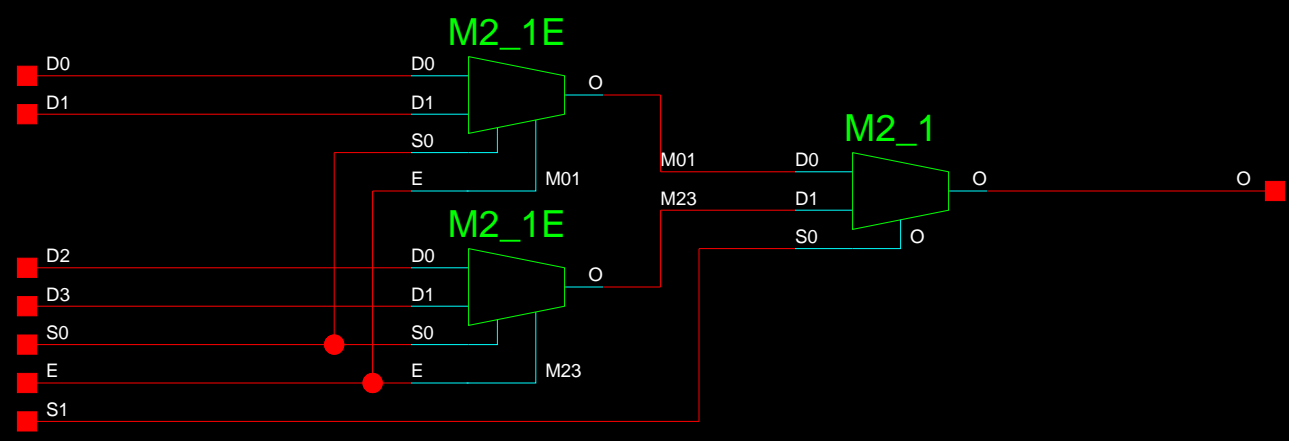
Comments: 2-to-1 Multiplexer

Date: 14th December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M4_1E Macro

Comments: 4-to-1 Multiplexer with Enable

Date: 1st December 1994

Ver: 1

Sheet Size: A

Rev: B



Title: XC4000 Family M2_1 Macro

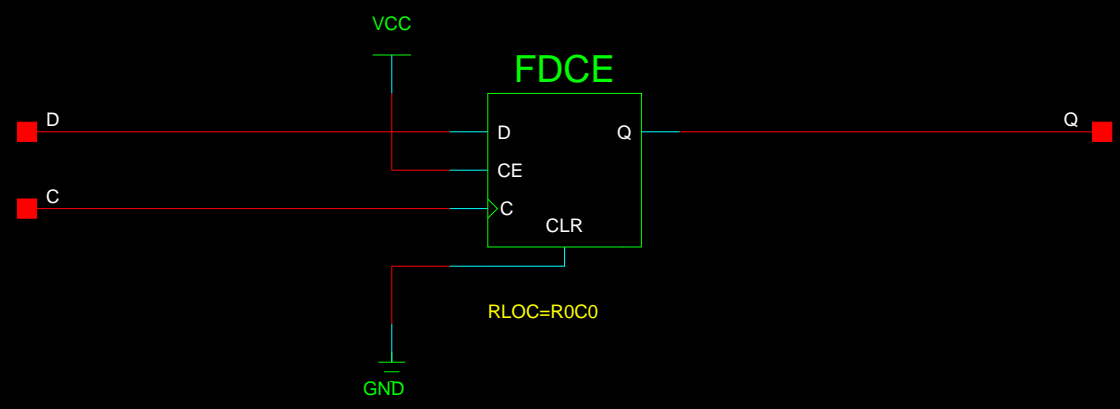
Comments: 2-to-1 Multiplexer

Date: 14th December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

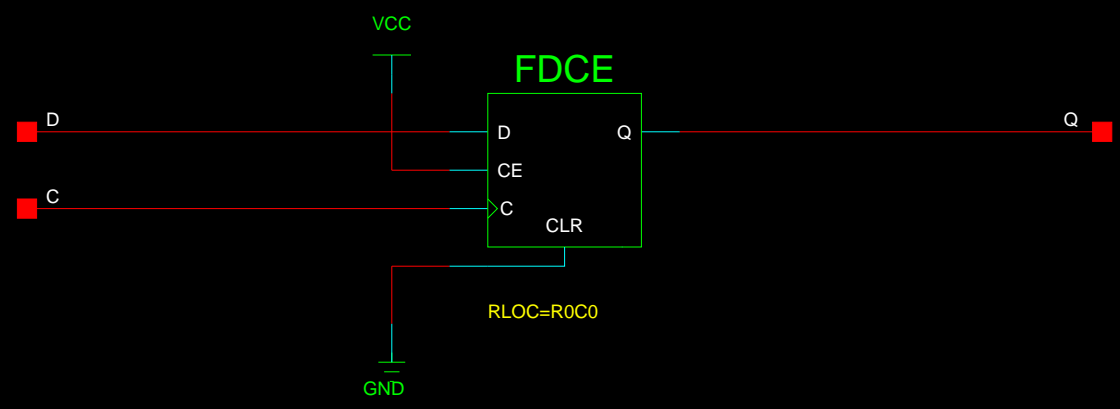
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Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

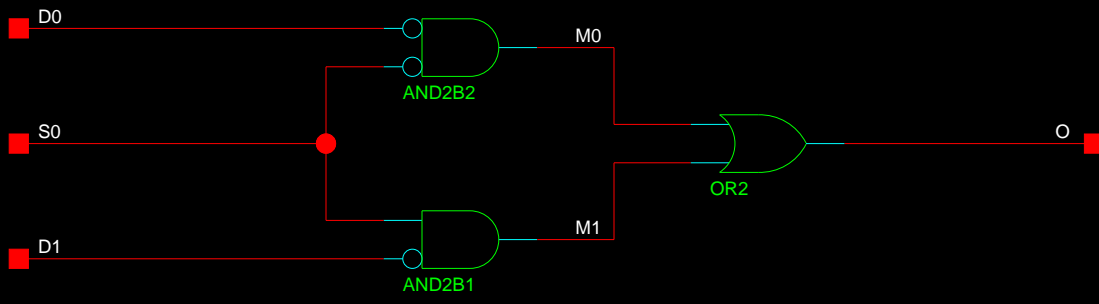
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Date: 23rd December 1992

Ver: 1

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Rev: A



Title: XC4000 Family M2_1B2 Macro

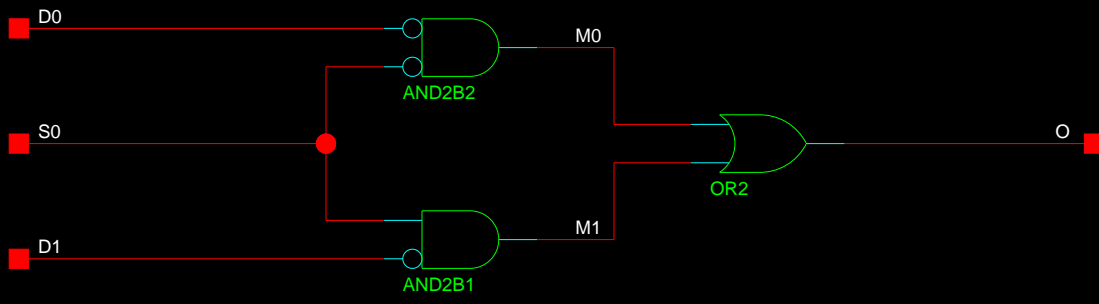
Comments: 2-to-1 Multiplexer w/
D0 & D1 Inverted

Date: 1st March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M2_1B2 Macro

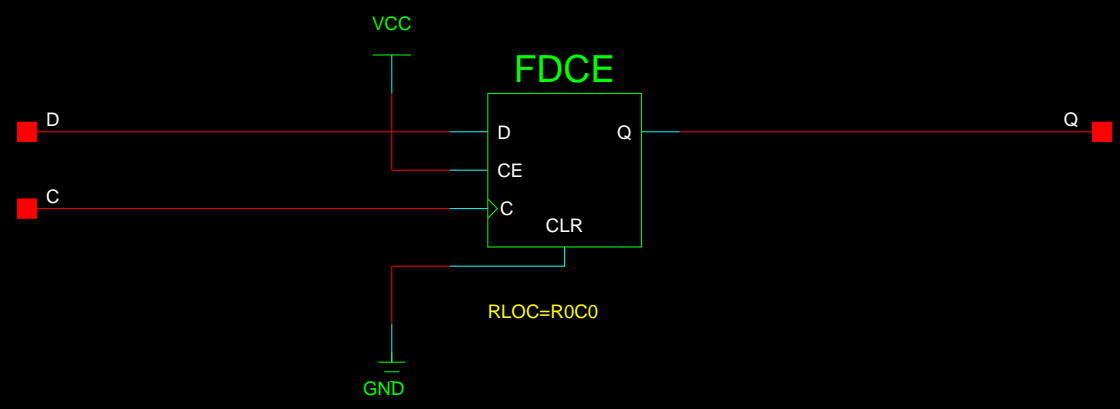
Comments: 2-to-1 Multiplexer w/
D0 & D1 Inverted

Date: 1st March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

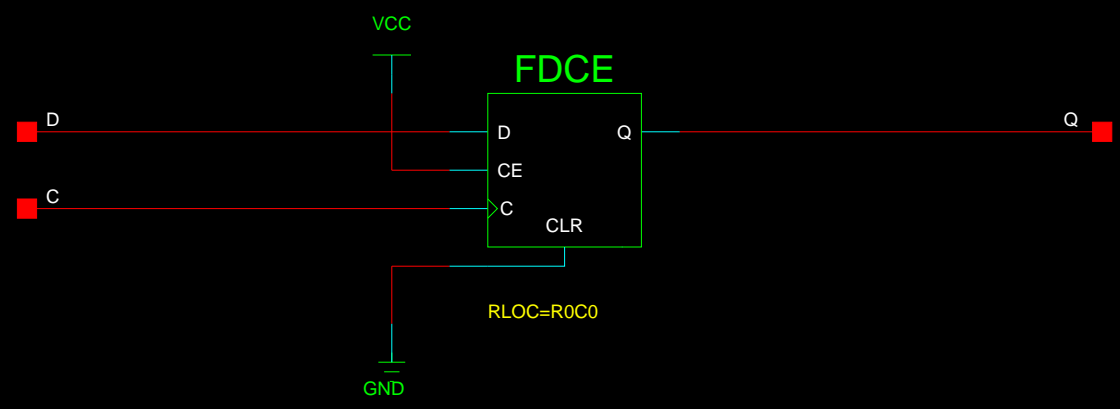
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Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

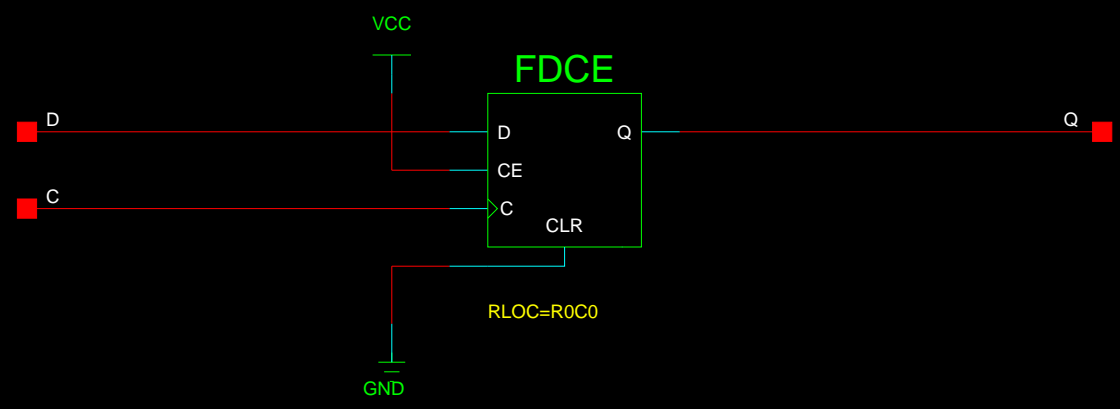
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Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

Comments: D Flip-Flop

Date: 23rd December 1992

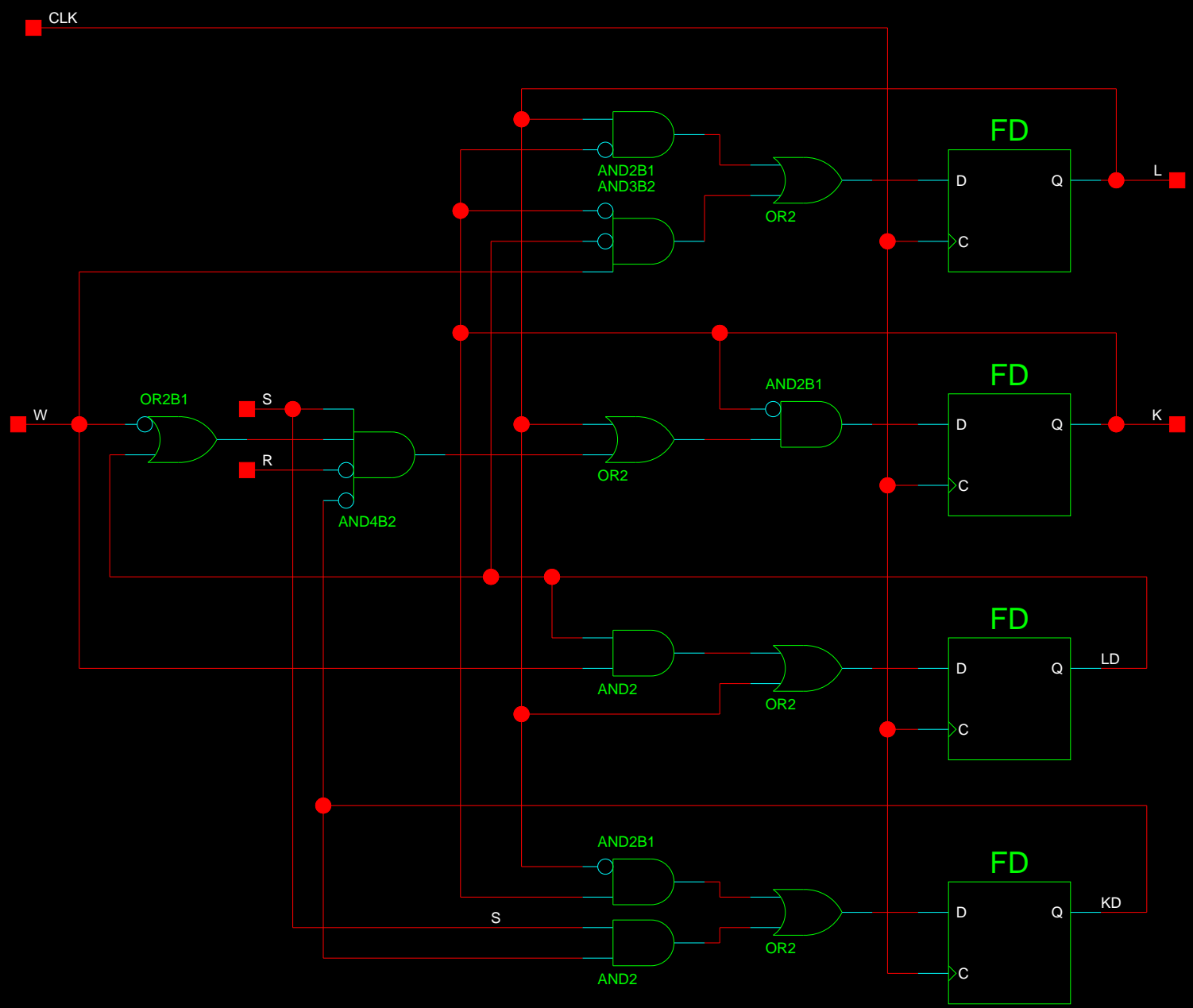
Ver: 1

Sheet Size: A

Rev: A

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HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE

CTSTATE.1

State Machine for Counter Latch

PREPARED		
CHECKED		
ENGINEER	NTA	

ASHEET

4-20-2005_16:20

size	Code ID No.	Drawing No.	rev
A		A34-7005	A

Scale sheet 1 of 1

A

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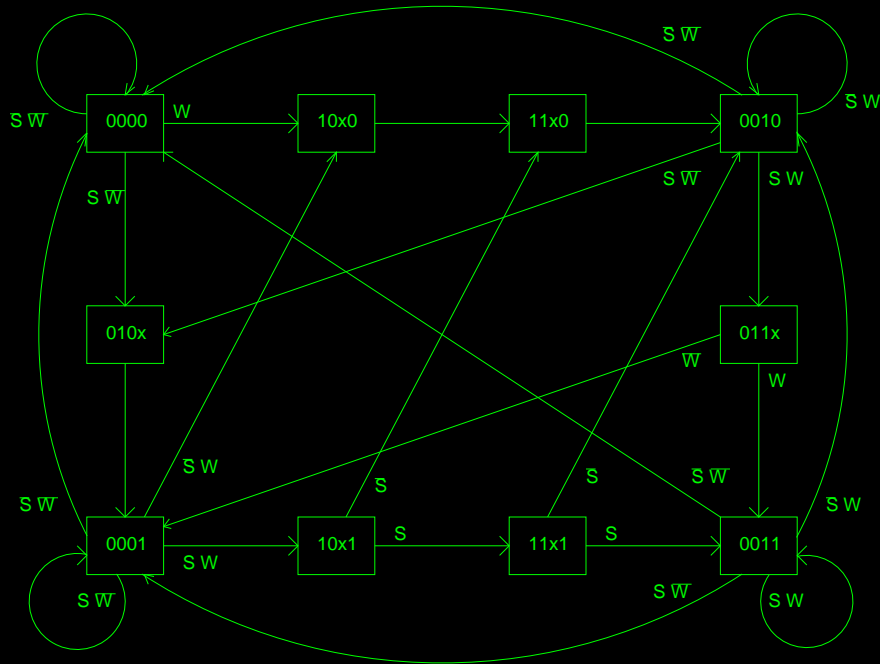
3

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LEGEND:

S: Step Clock Input

W: Write Input

L: Latch Output

K: Counter Clock Output

LD: Latched State

KD: Clocked State

10x0

x in a state block indicates
"either" as a destination and
"both" as a source.

HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE

CTSTATE.2

State Machine for Counter Latch

PREPARED		
CHECKED		
ENGINEER	NTA	

ASHEET

4-20-2005_16:20

size	Code ID No.	Drawing No.	rev
A		A34-7005	A
Scale	sheet 1 of 1		17 of 18

D

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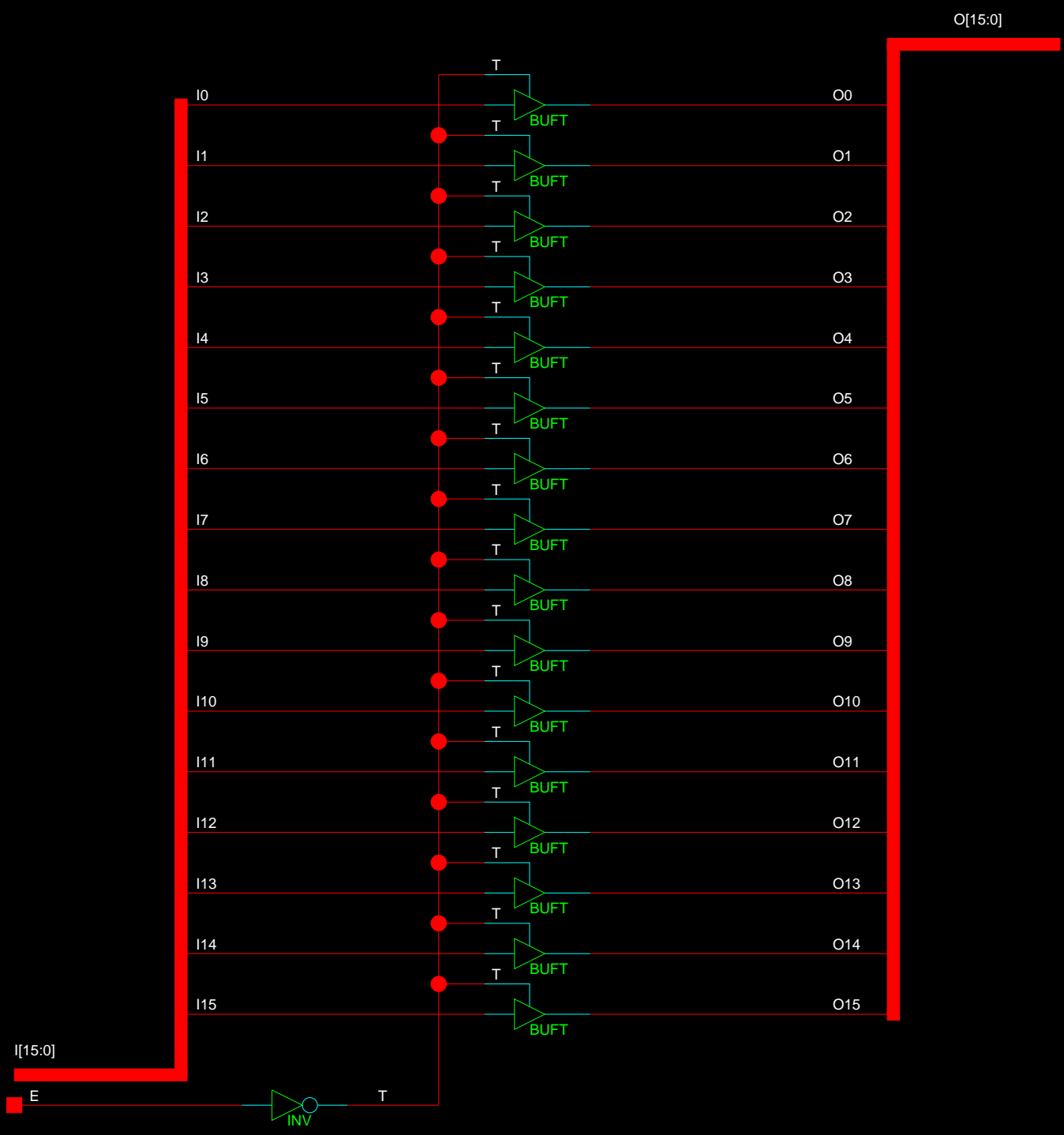
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Title:	XC4000 Family BUFE16 Macro		
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Date:	23rd February 1993	Ver:	1
Sheet Size:	A	Rev:	A

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D

C

C

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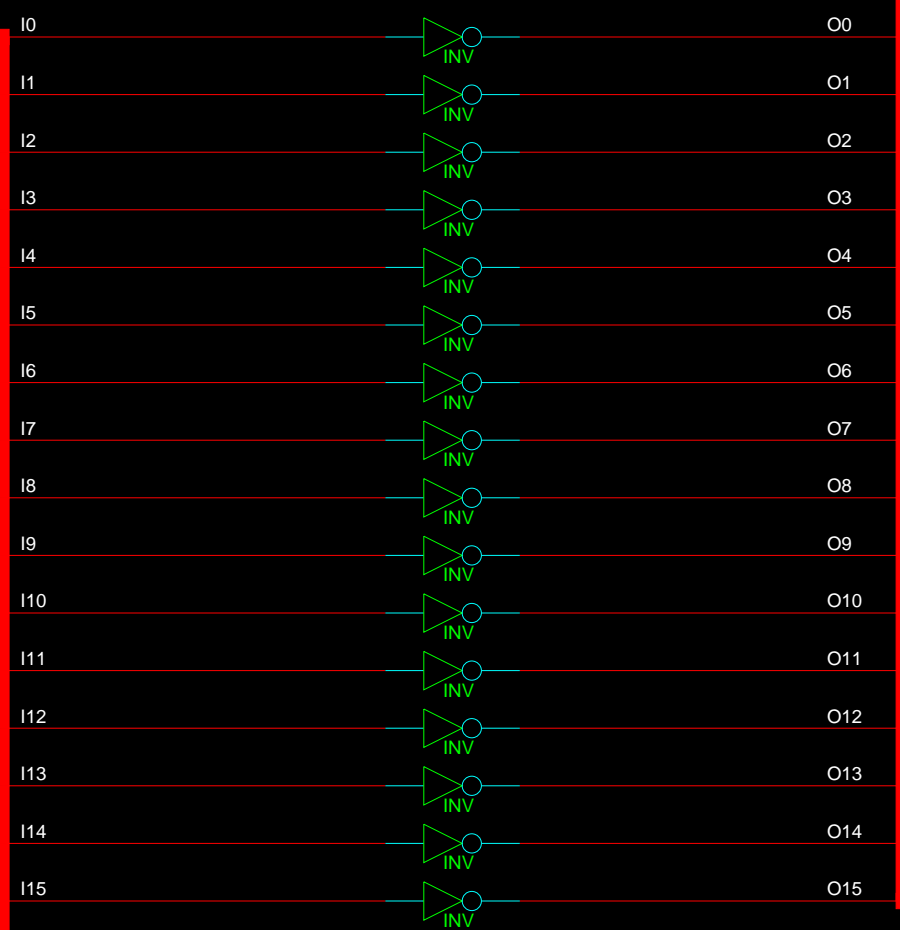
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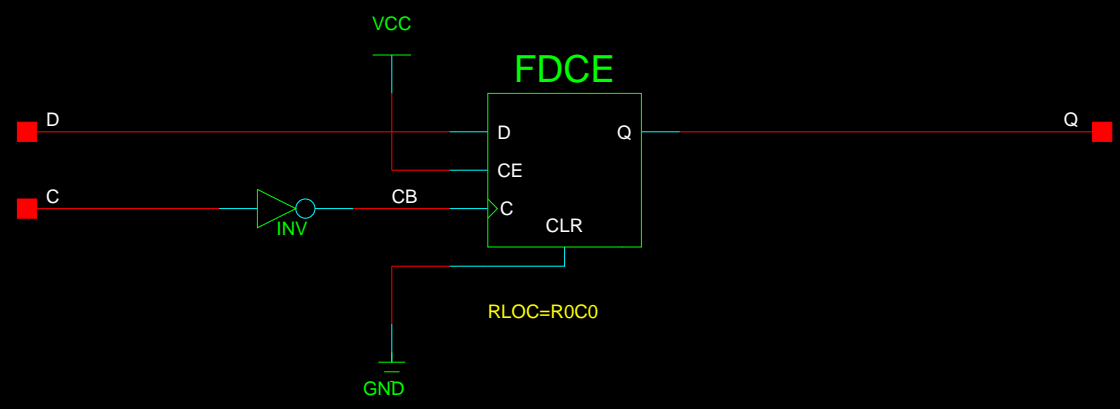


Title: XC4000 Family INV16 Macro

Comments: 16-bit Inverter

Date:	22nd February 1993	Ver:	1
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Sheet Size: A	Rev:	A
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Title: XC4000 Family FD_1 Macro

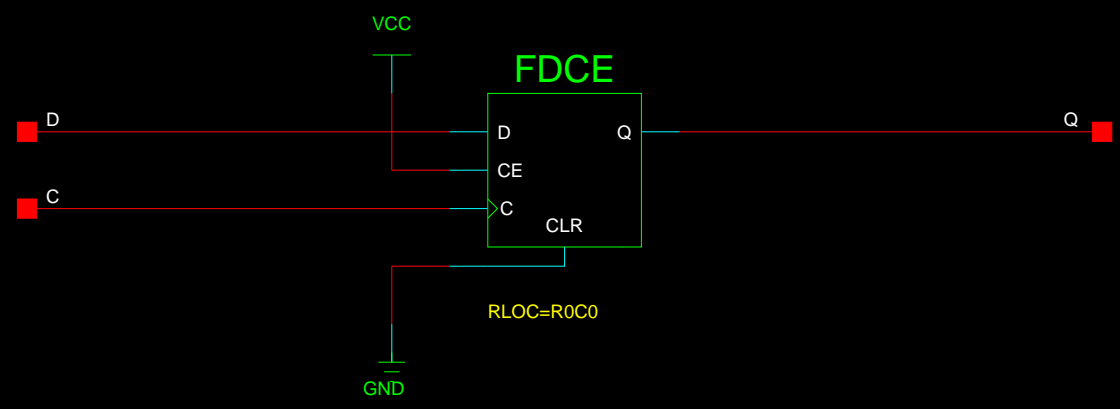
Comments: D Flip-Flop w/ an inverted Clock

Date: 1st March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

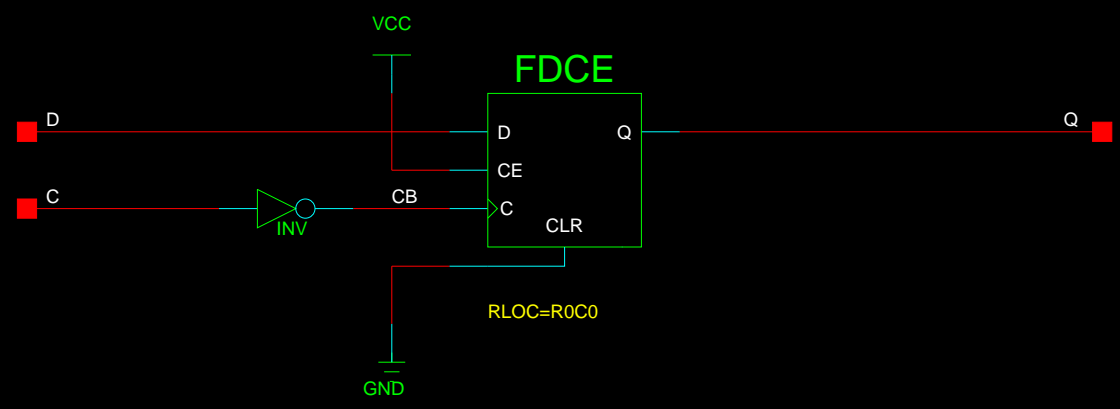
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD_1 Macro

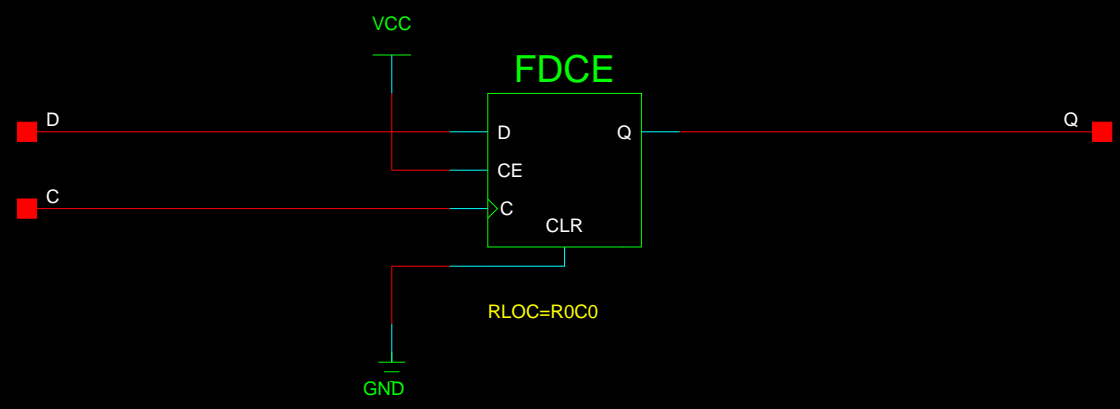
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Date: 1st March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

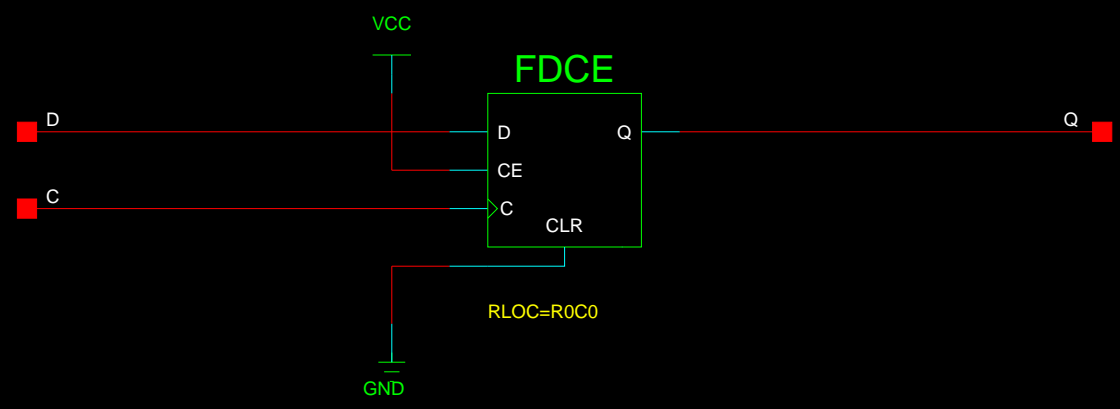
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Rev: A



Title: XC4000 Family FD Macro

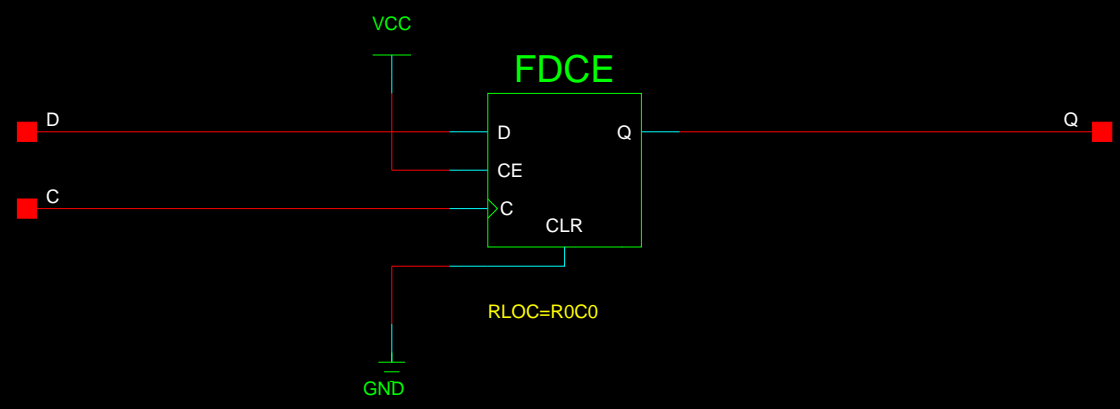
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Date: 23rd December 1992

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Rev: A



Title: XC4000 Family FD Macro

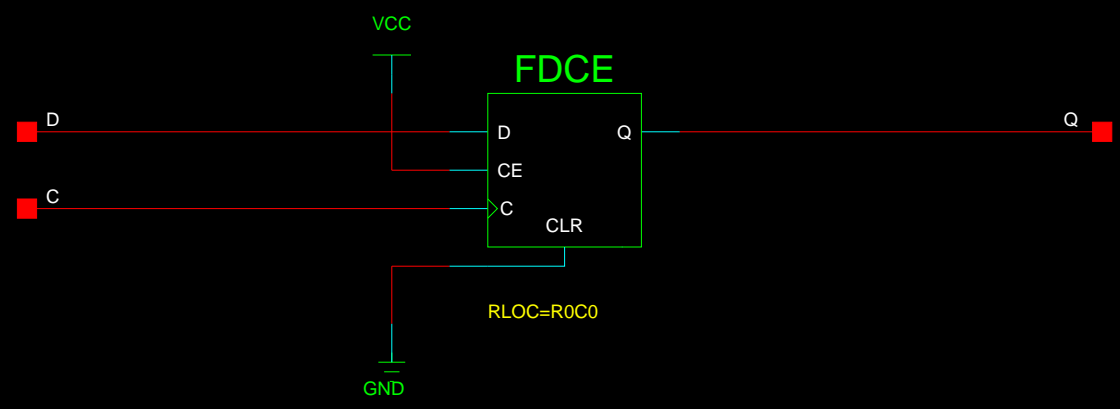
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M2_1 Macro

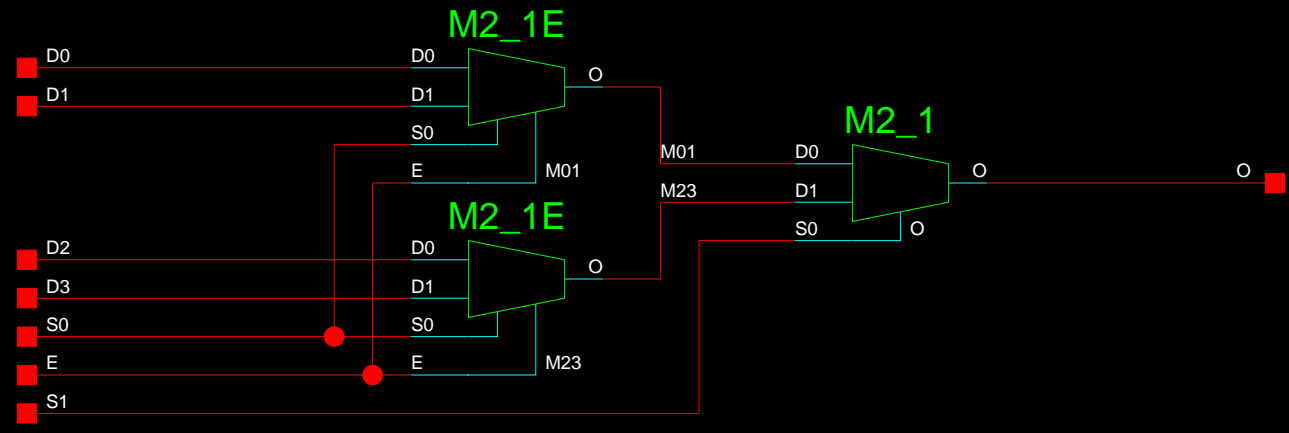
Comments: 2-to-1 Multiplexer

Date: 14th December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M4_1E Macro

Comments: 4-to-1 Multiplexer with Enable

Date: 1st December 1994

Ver: 1

Sheet Size: A

Rev: B



Title: XC4000 Family M2_1 Macro

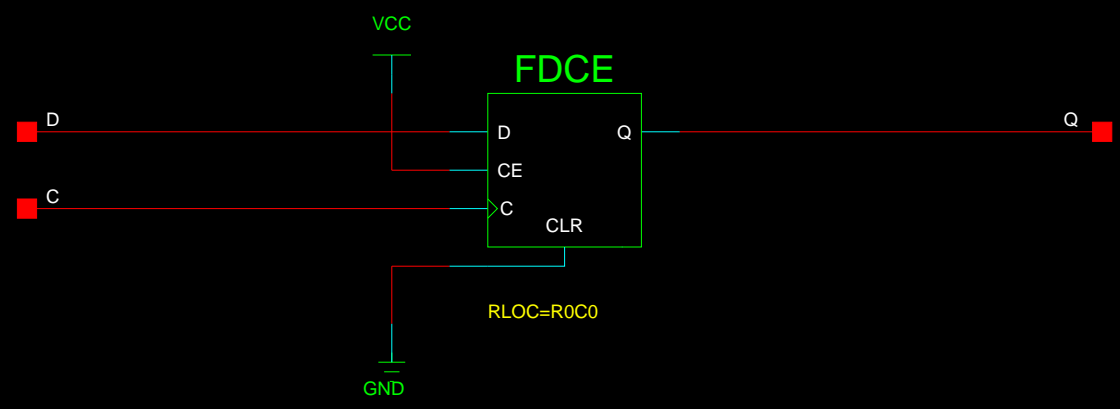
Comments: 2-to-1 Multiplexer

Date: 14th December 1992

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Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

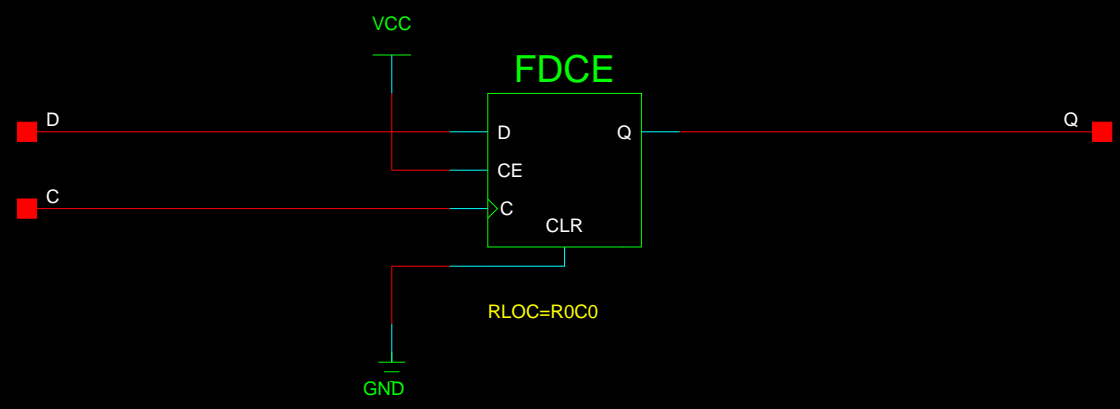
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

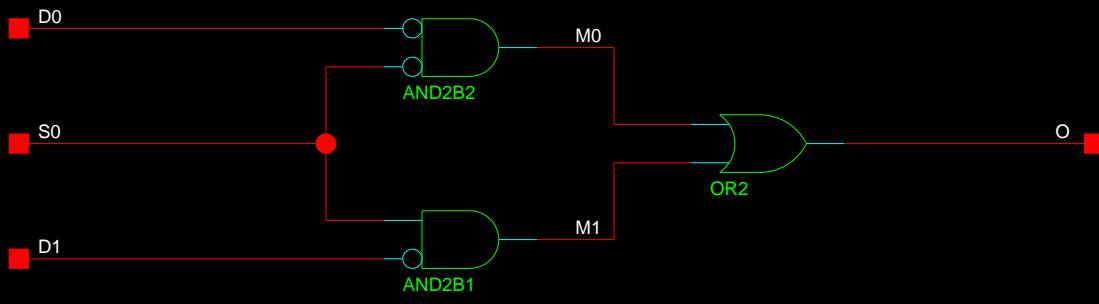
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Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M2_1B2 Macro

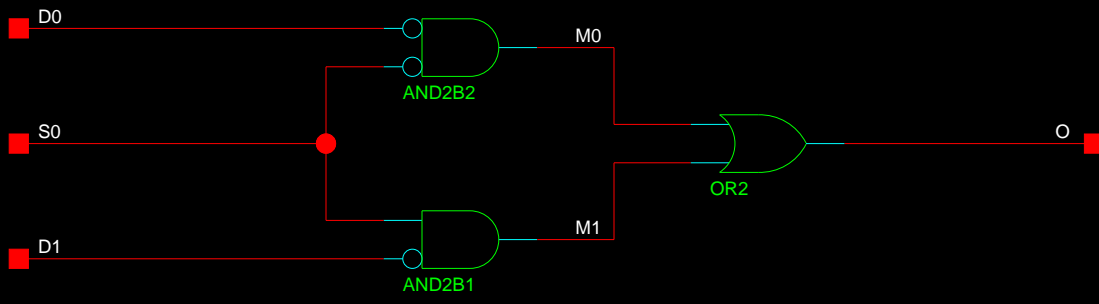
Comments: 2-to-1 Multiplexer w/
D0 & D1 Inverted

Date: 1st March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M2_1B2 Macro

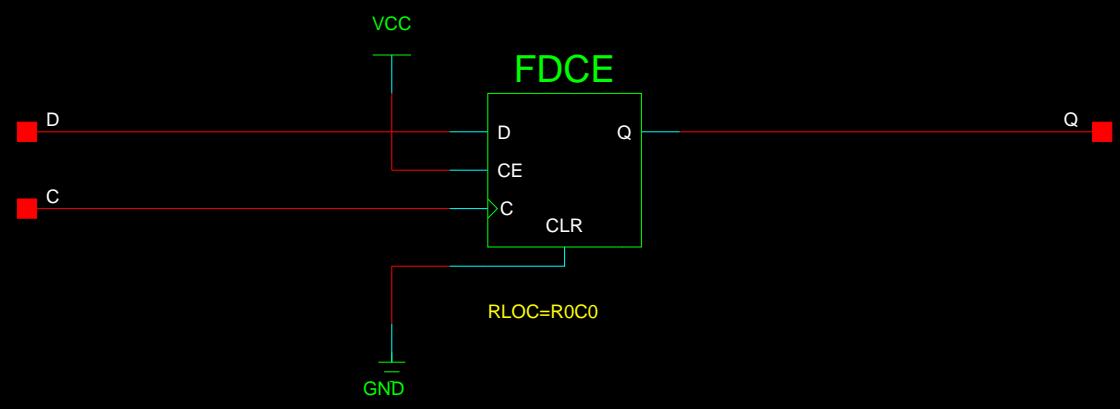
Comments: 2-to-1 Multiplexer w/
D0 & D1 Inverted

Date: 1st March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

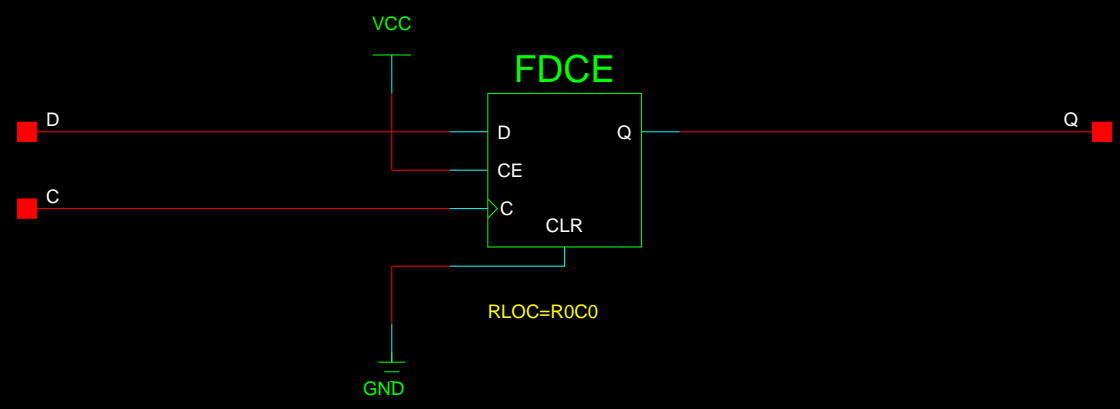
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

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Rev: A



Title: XC4000 Family FD Macro

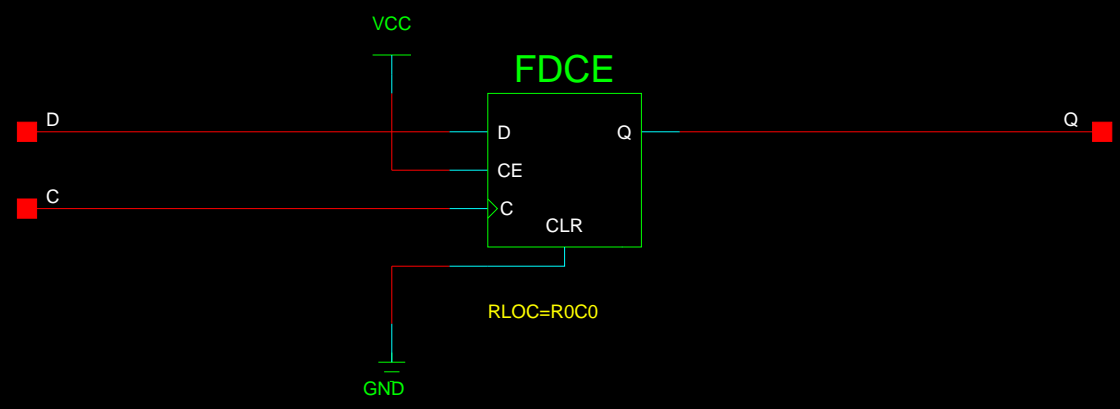
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Date: 23rd December 1992

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Rev: A



Title: XC4000 Family FD Macro

Comments: D Flip-Flop

Date: 23rd December 1992

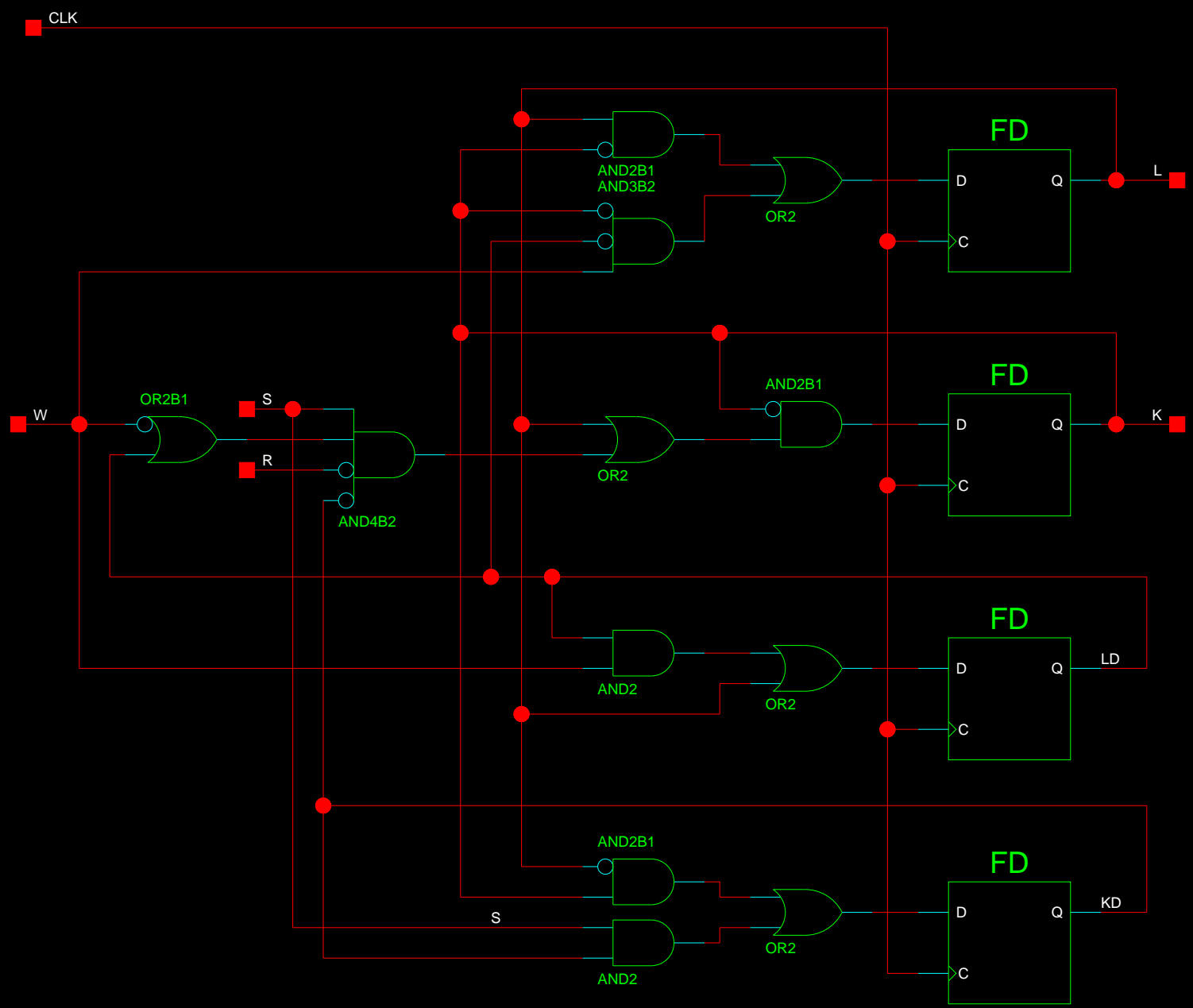
Ver: 1

Sheet Size: A

Rev: A

A

B



HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE
CTSTATE.1
 State Machine for Counter Latch

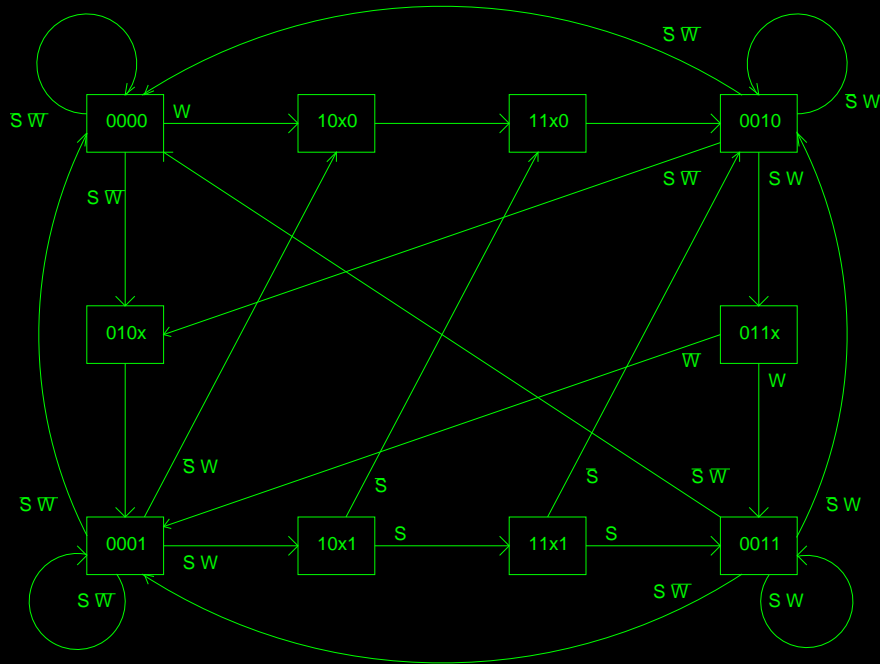
PREPARED		
CHECKED		
ENGINEER	NTA	

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 4-20-2005_16:20

size A	Code ID No.	Drawing No. A34-7005	rev A
Scale	sheet 1 of 1		16 of 18

A

B



LEGEND:

S: Step Clock Input

W: Write Input

L: Latch Output

K: Counter Clock Output

LD: Latched State

KD: Clocked State

10x0

x in a state block indicates
"either" as a destination and
"both" as a source.

HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE

CTSTATE.2

State Machine for Counter Latch

PREPARED		
CHECKED		
ENGINEER	NTA	

ASHEET	size	Code ID No.	Drawing No.	rev
	A		A34-7005	A
4-20-2005_16:20	Scale		sheet 1 of 1	17 of 18

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D

C

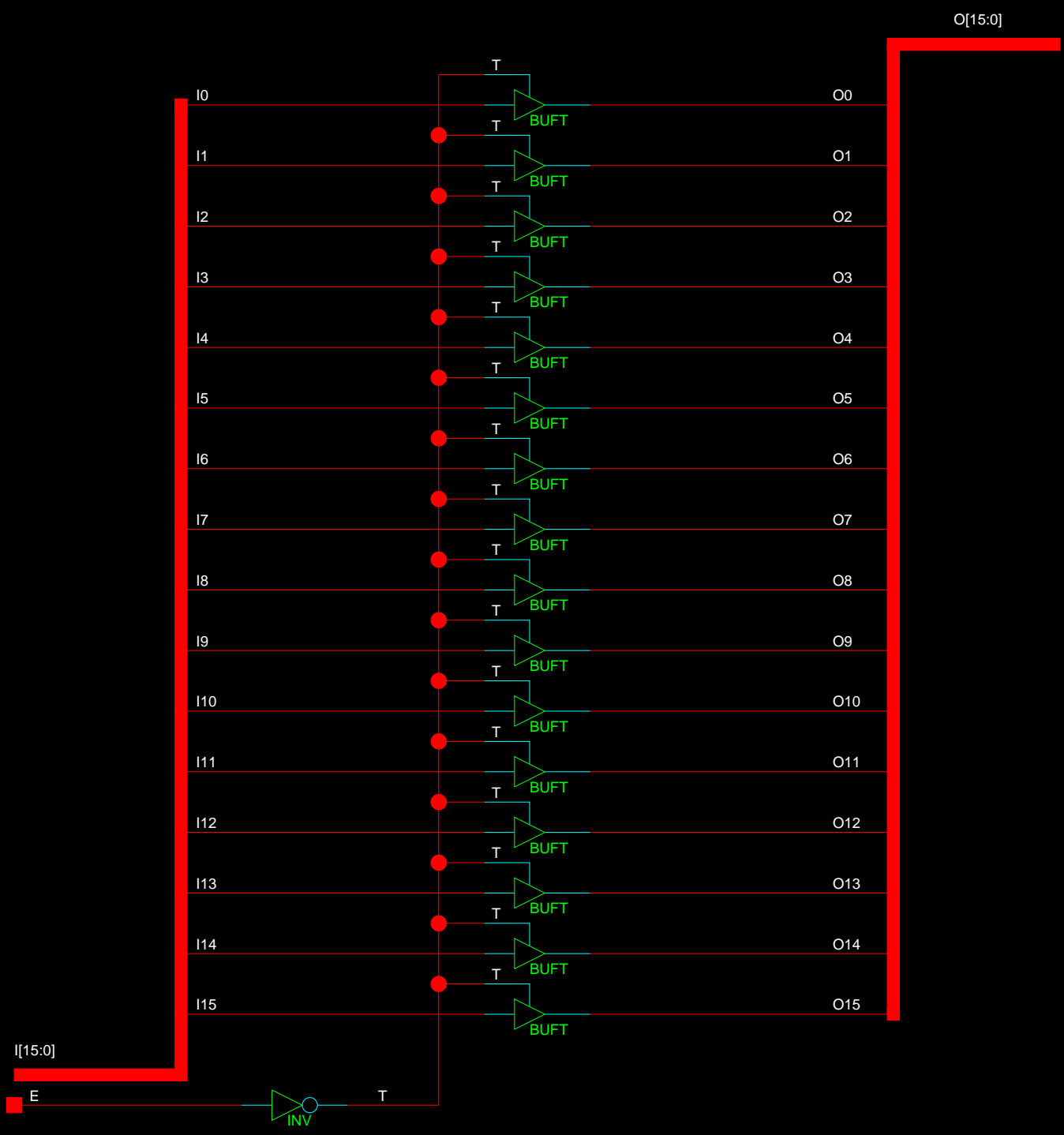
C

B

B

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A



Title:	XC4000 Family BUFE16 Macro		
Comments:	16-Bit 3-State Buffer w/ an Active High Enable		
Date:	23rd February 1993	Ver:	1
Sheet Size:	A	Rev:	A

D

D

C

C

B

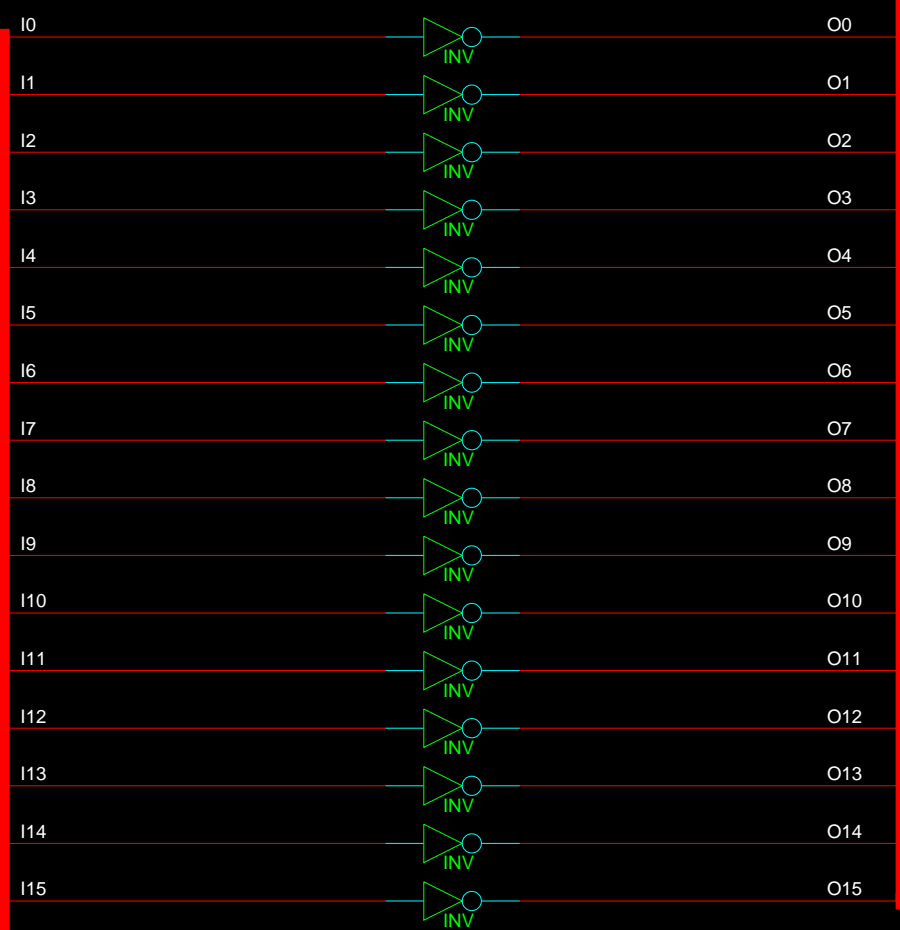
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A

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O[15:0]

I[15:0]

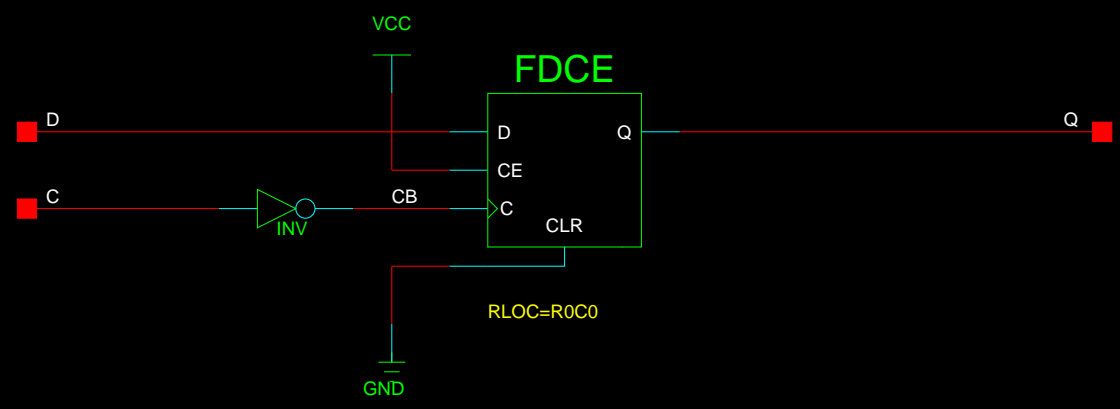


Title: XC4000 Family INV16 Macro

Comments: 16-bit Inverter

Date:	22nd February 1993	Ver:	1
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Sheet Size: A	Rev:	A
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Title: XC4000 Family FD_1 Macro

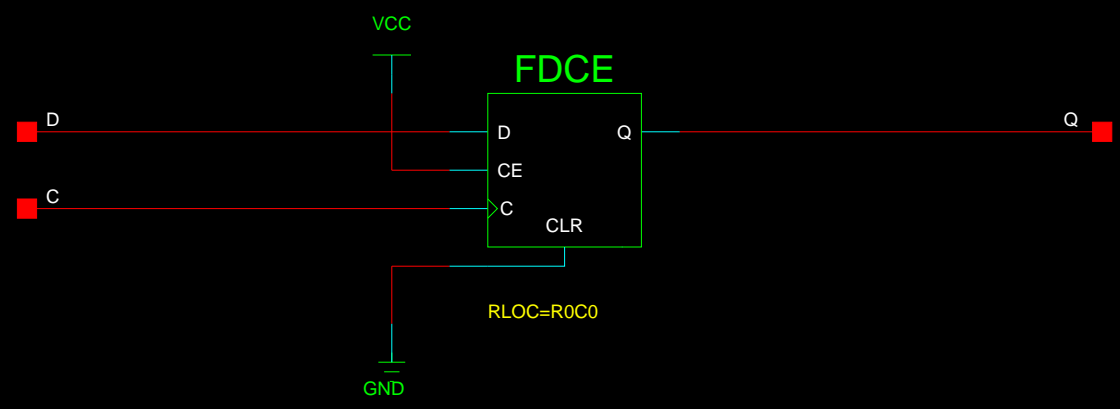
Comments: D Flip-Flop w/ an inverted Clock

Date: 1st March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

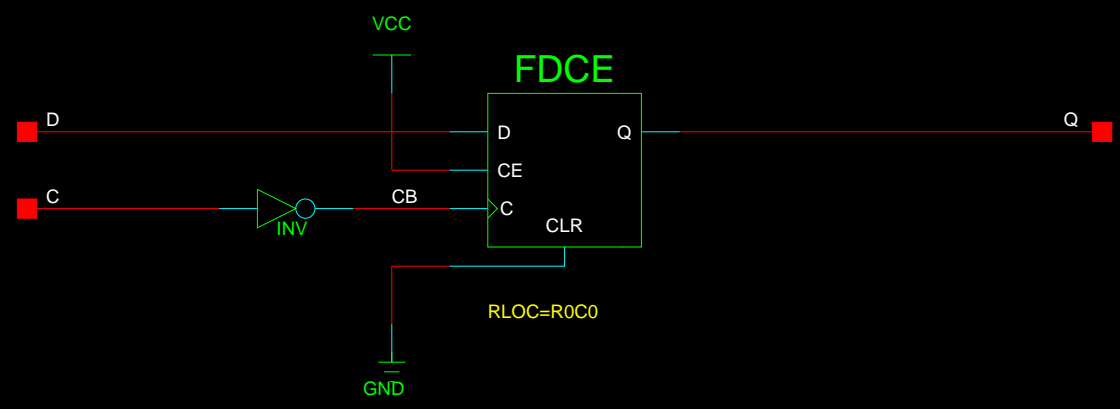
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD_1 Macro

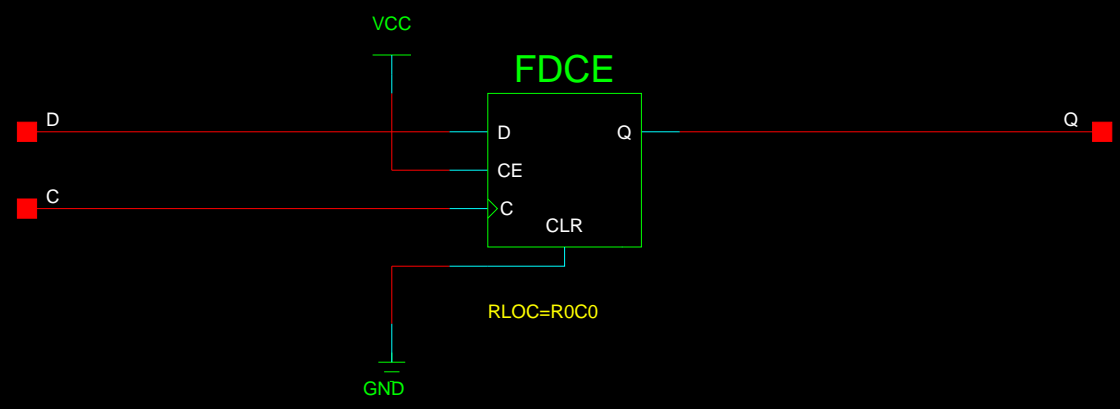
Comments: D Flip-Flop w/ an inverted Clock

Date: 1st March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

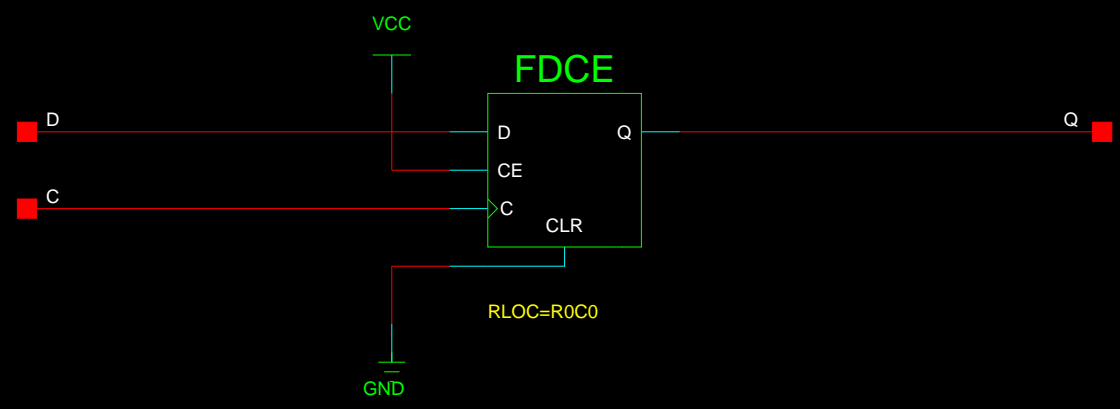
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

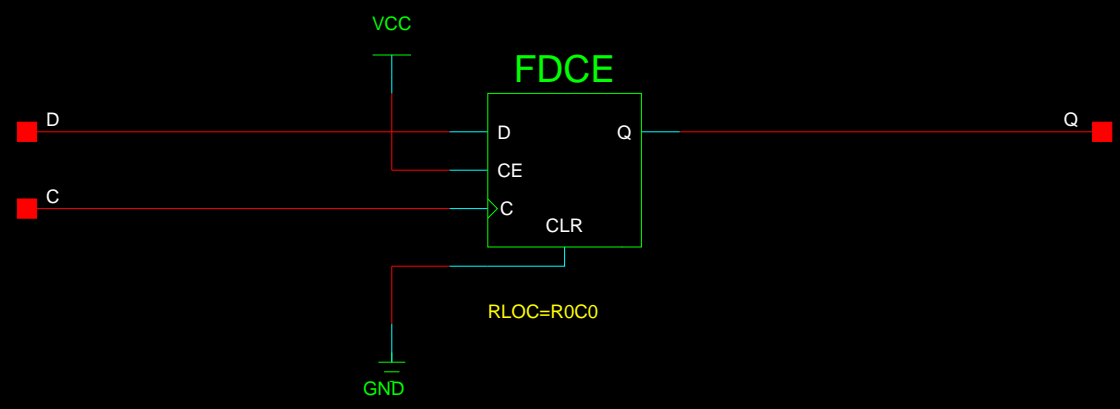
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

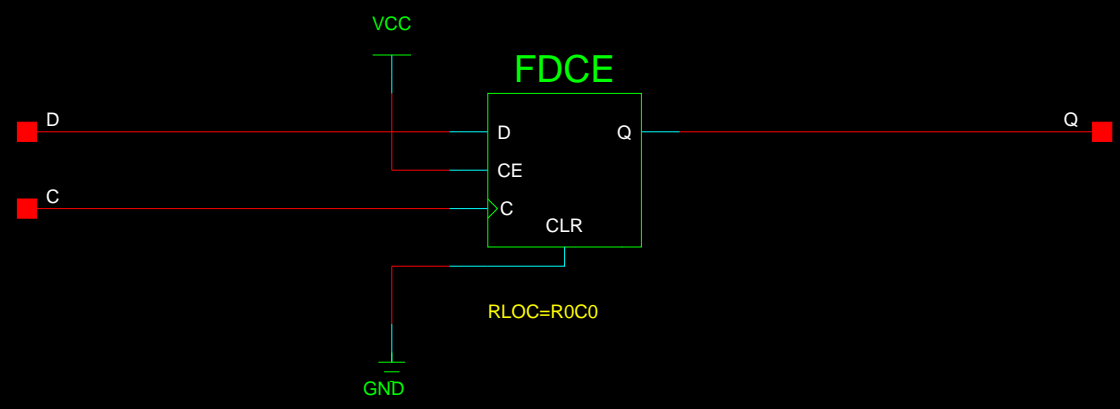
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M2_1 Macro

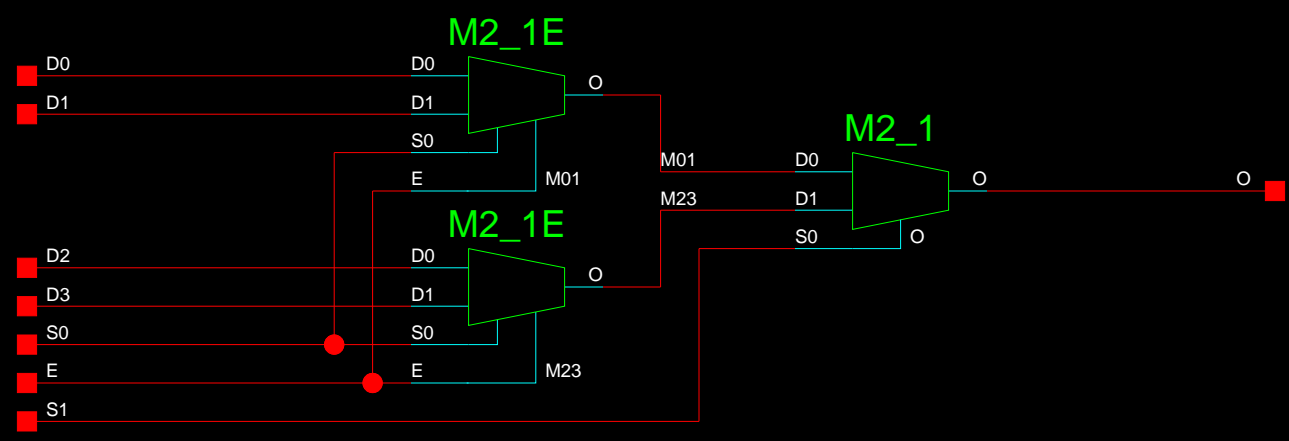
Comments: 2-to-1 Multiplexer

Date: 14th December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M4_1E Macro

Comments: 4-to-1 Multiplexer with Enable

Date: 1st December 1994

Ver: 1

Sheet Size: A

Rev: B



Title: XC4000 Family M2_1 Macro

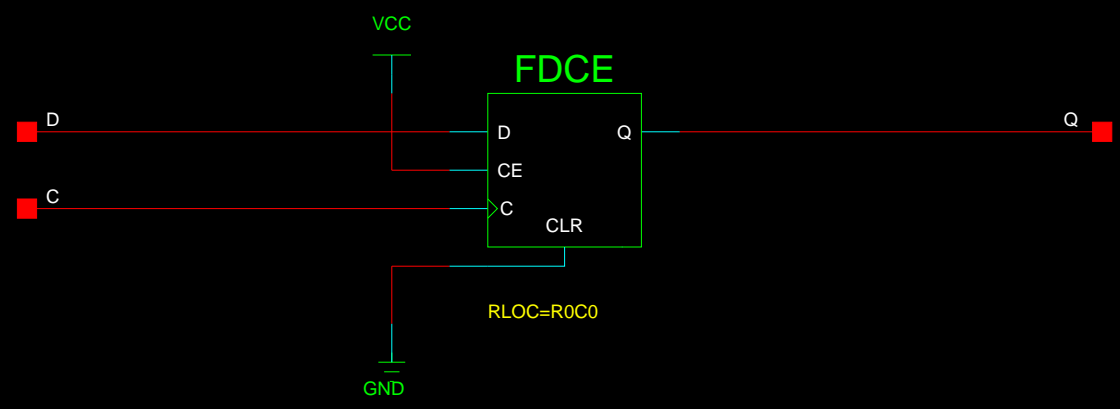
Comments: 2-to-1 Multiplexer

Date: 14th December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

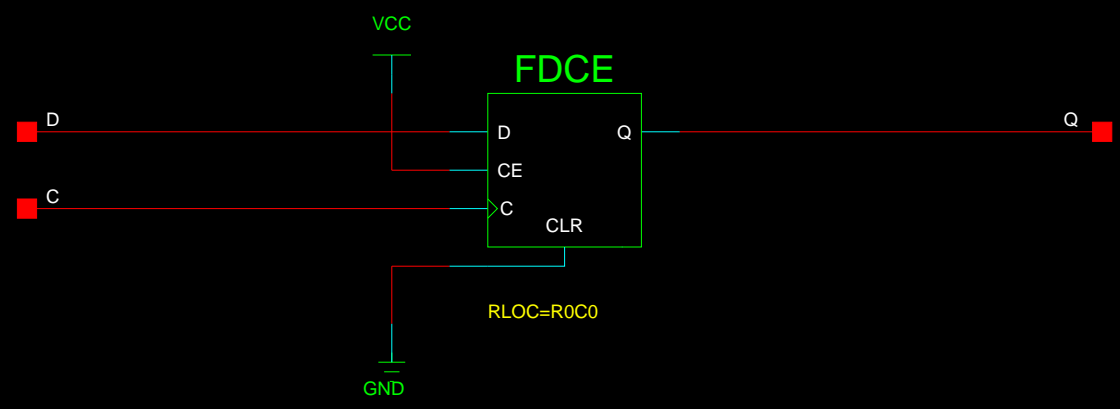
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

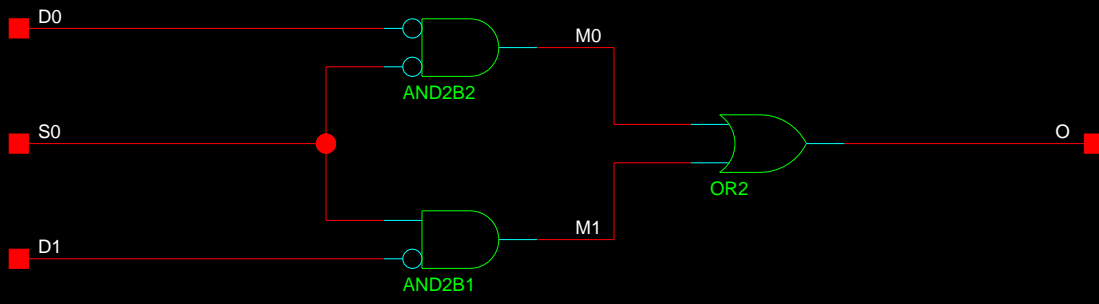
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M2_1B2 Macro

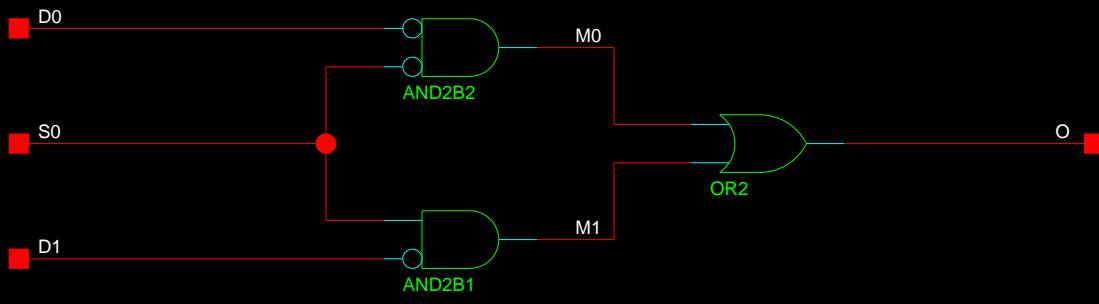
Comments: 2-to-1 Multiplexer w/
D0 & D1 Inverted

Date: 1st March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M2_1B2 Macro

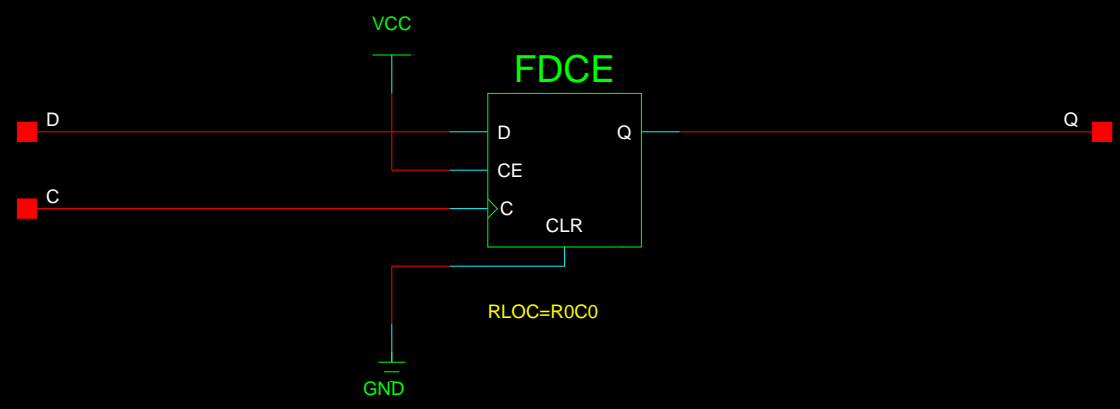
Comments: 2-to-1 Multiplexer w/
D0 & D1 Inverted

Date: 1st March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

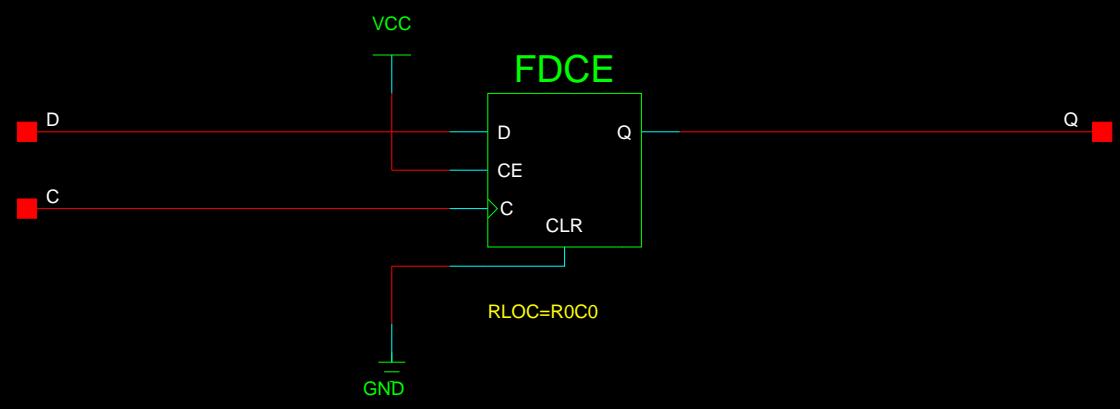
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

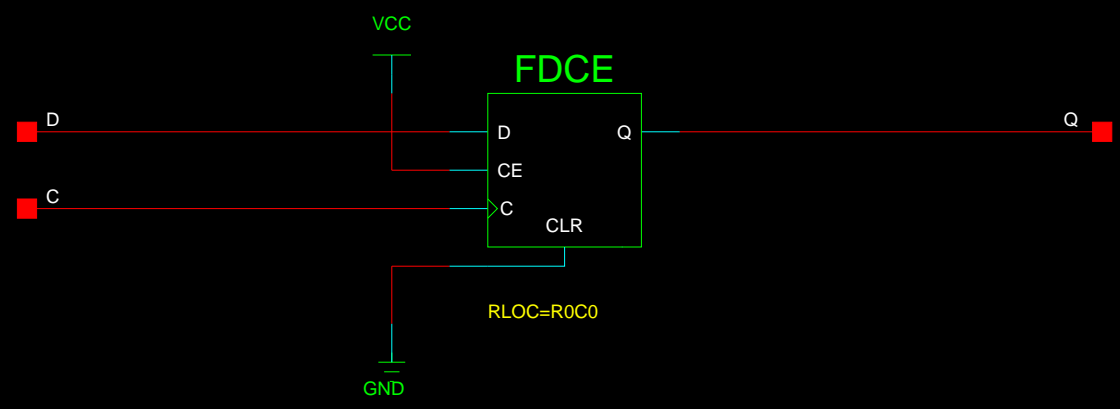
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

Comments: D Flip-Flop

Date: 23rd December 1992

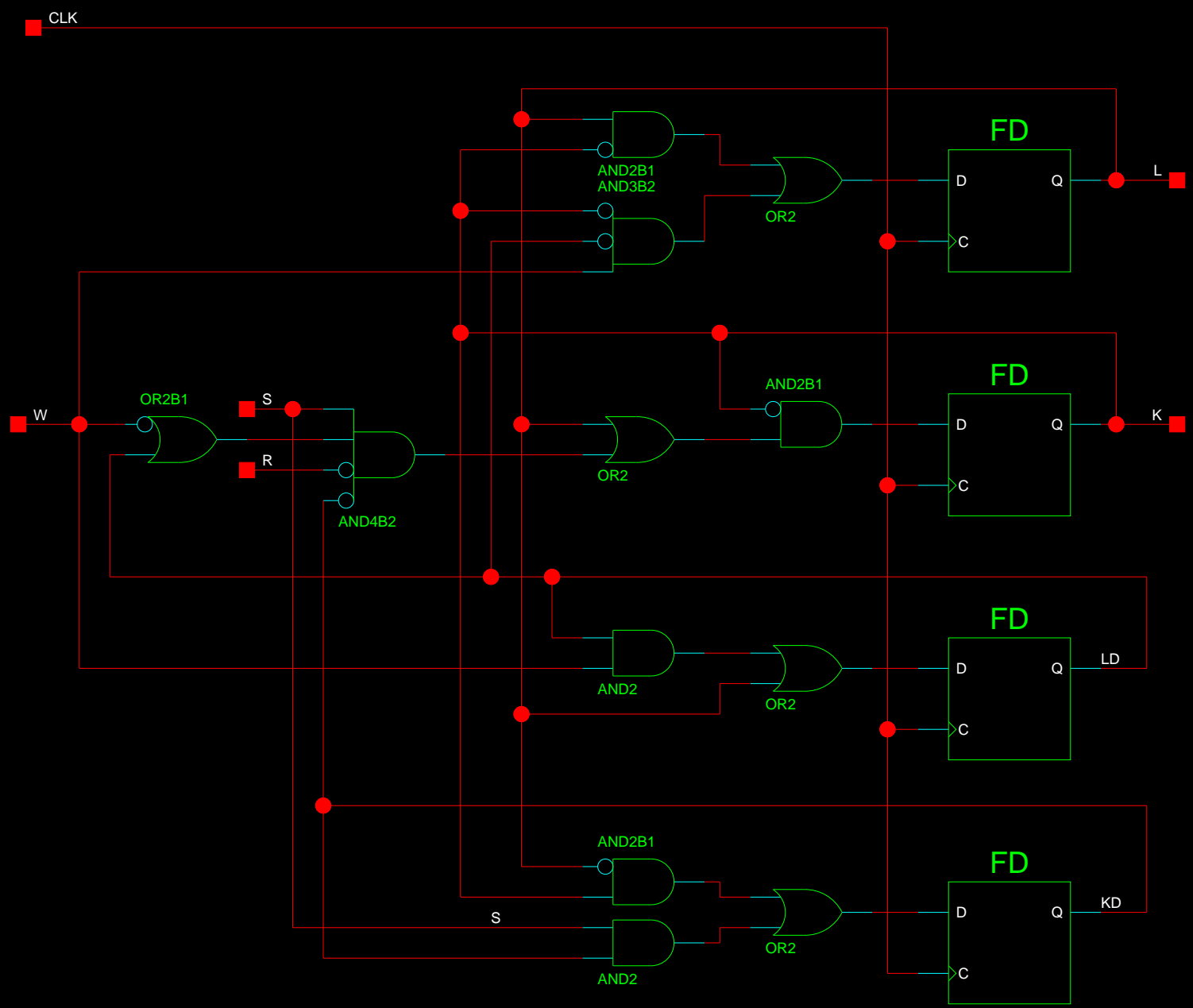
Ver: 1

Sheet Size: A

Rev: A

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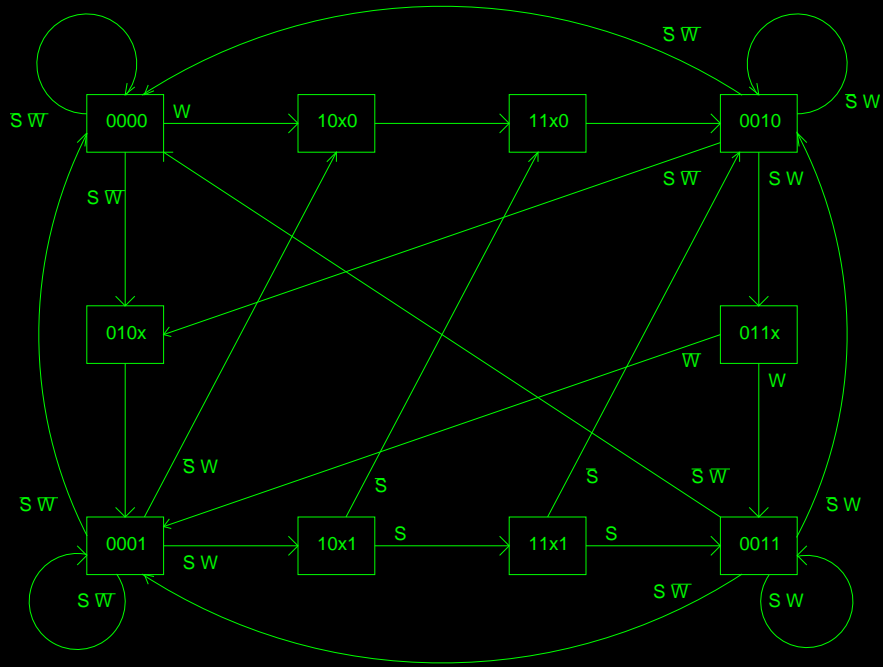
HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE
CTSTATE.1
 State Machine for Counter Latch

PREPARED		
CHECKED		
ENGINEER	NTA	

ASHEET
 4-20-2005_16:20

size A	Code ID No.	Drawing No. A34-7005	rev A
Scale	sheet 1 of 1		16 of 18



LEGEND:

- S: Step Clock Input
- W: Write Input
- L: Latch Output
- K: Counter Clock Output
- LD: Latched State
- KD: Clocked State

10x0

x in a state block indicates "either" as a destination and "both" as a source.

HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE
CTSTATE.2

State Machine for Counter Latch

PREPARED		
CHECKED		
ENGINEER	NTA	

ASHEET

4-20-2005_16:20

size A	Code ID No.	Drawing No. A34-7005	rev A
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Scale sheet 1 of 1

D

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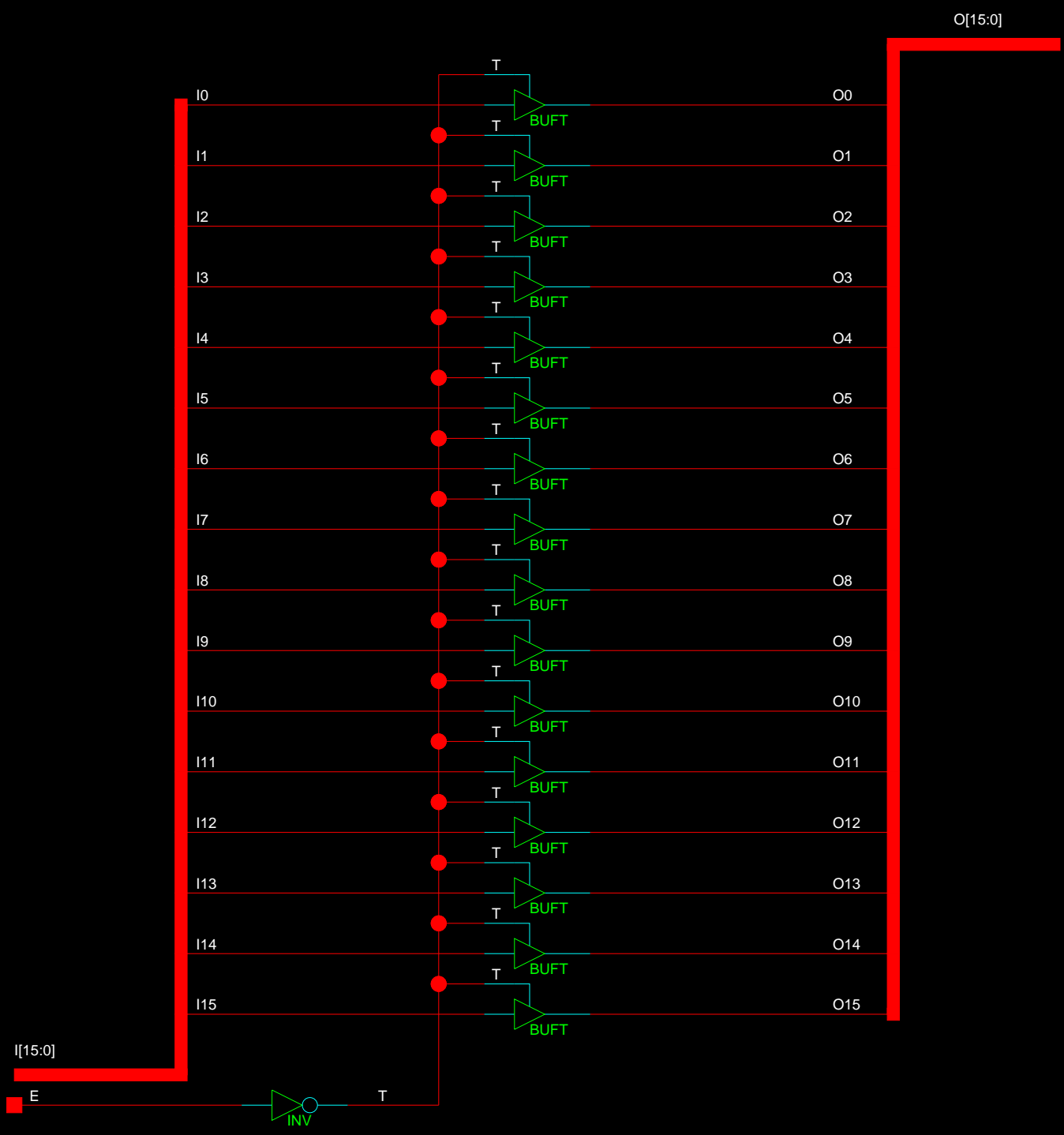
C

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Title:	XC4000 Family BUFE16 Macro		
Comments:	16-Bit 3-State Buffer w/ an Active High Enable		
Date:	23rd February 1993	Ver:	1
Sheet Size:	A	Rev:	A

D

D

C

C

B

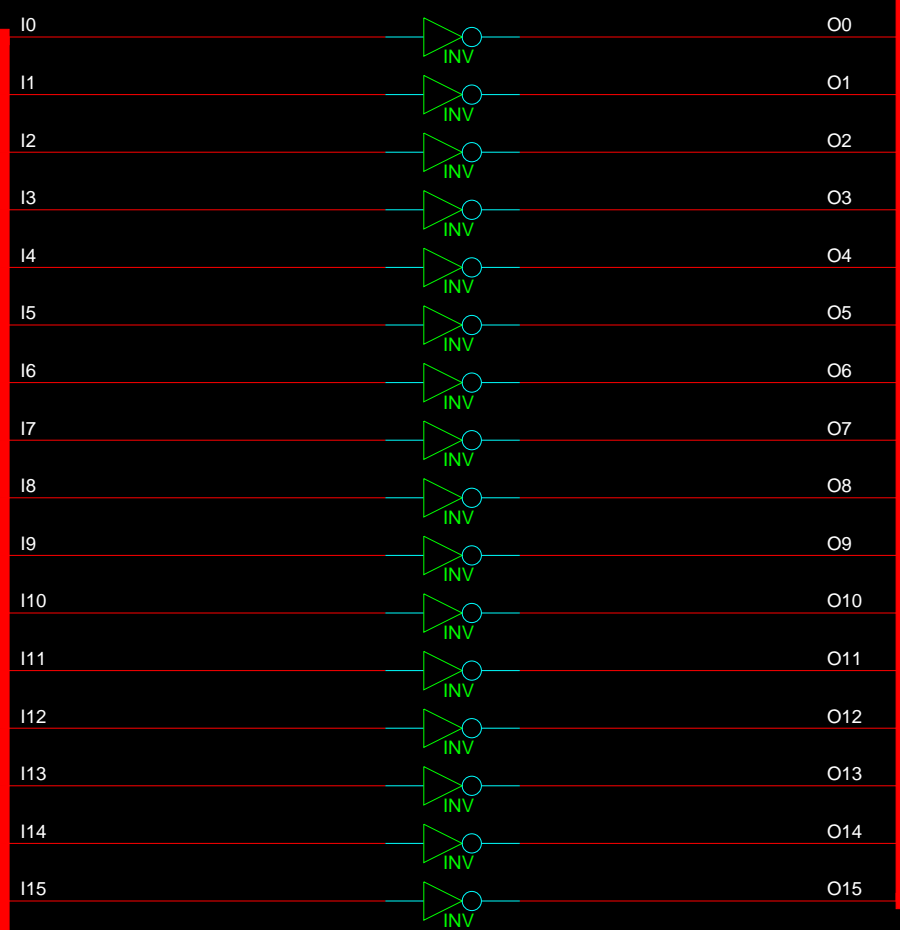
B

A

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O[15:0]

I[15:0]

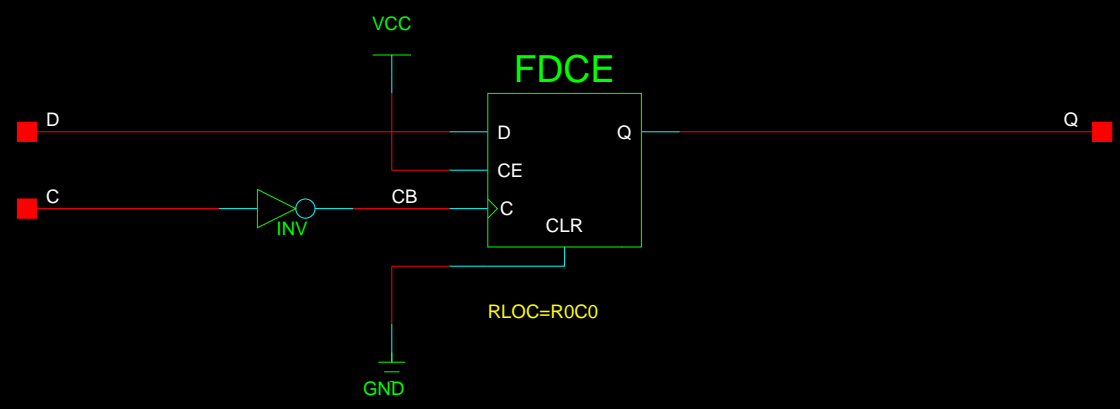


Title: XC4000 Family INV16 Macro

Comments: 16-bit Inverter

Date:	22nd February 1993	Ver:	1
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Sheet Size: A	Rev:	A
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Title: XC4000 Family FD_1 Macro

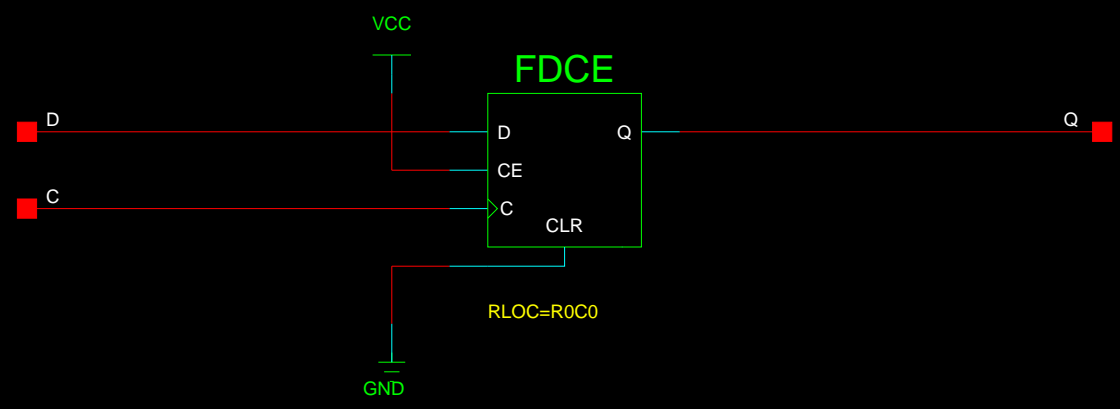
Comments: D Flip-Flop w/ an inverted Clock

Date: 1st March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

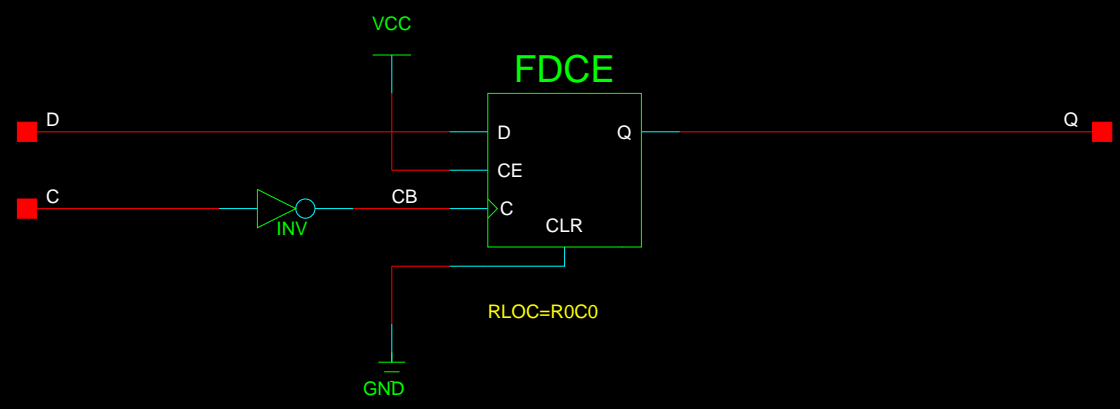
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD_1 Macro

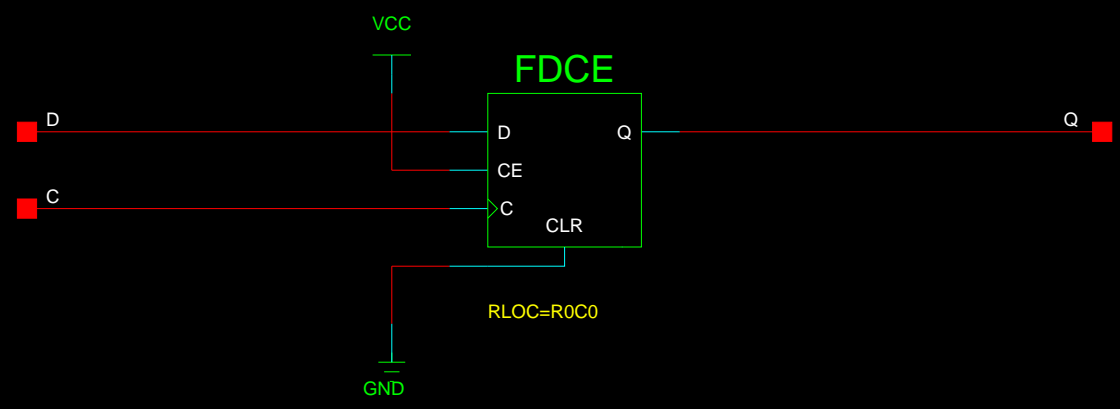
Comments: D Flip-Flop w/ an inverted Clock

Date: 1st March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

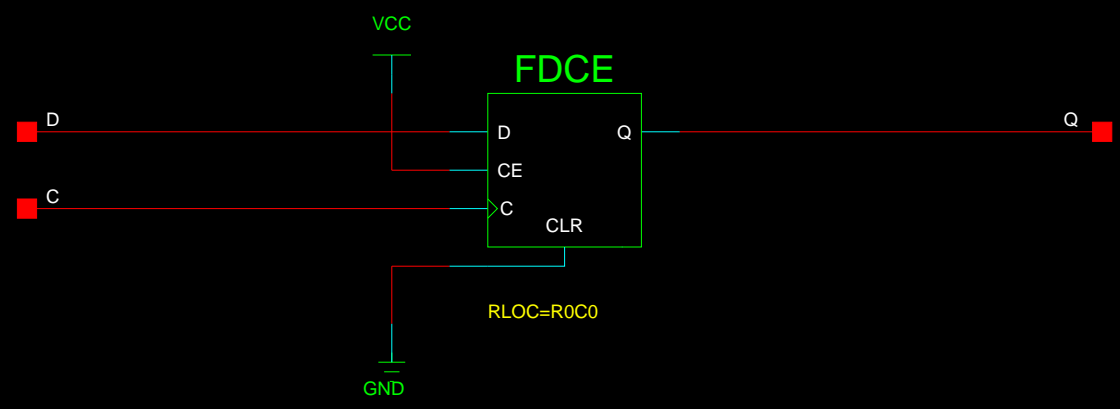
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

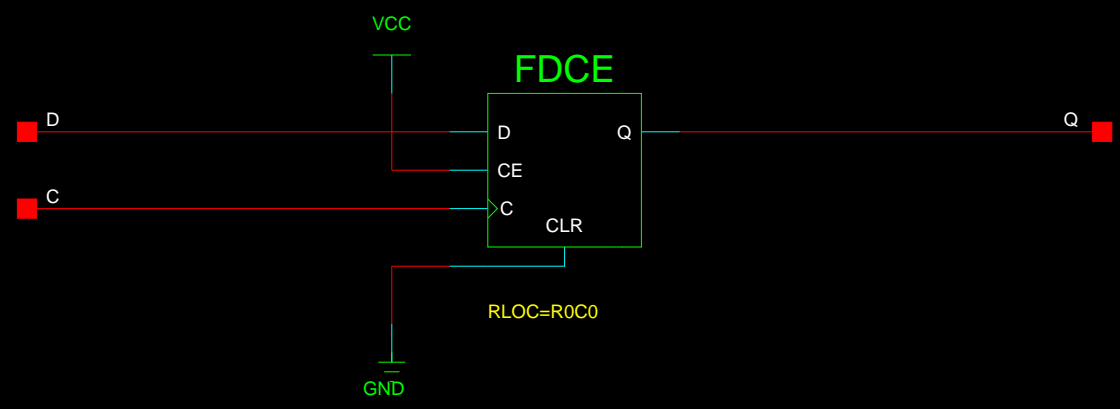
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

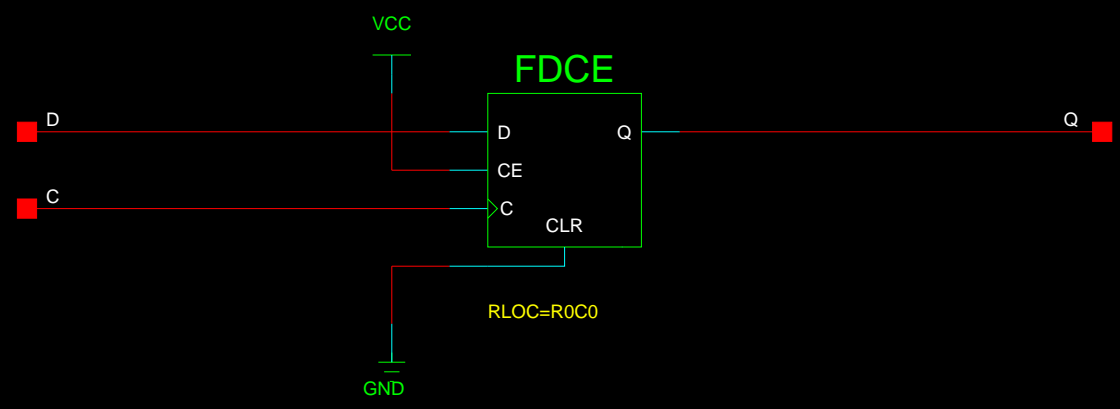
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M2_1 Macro

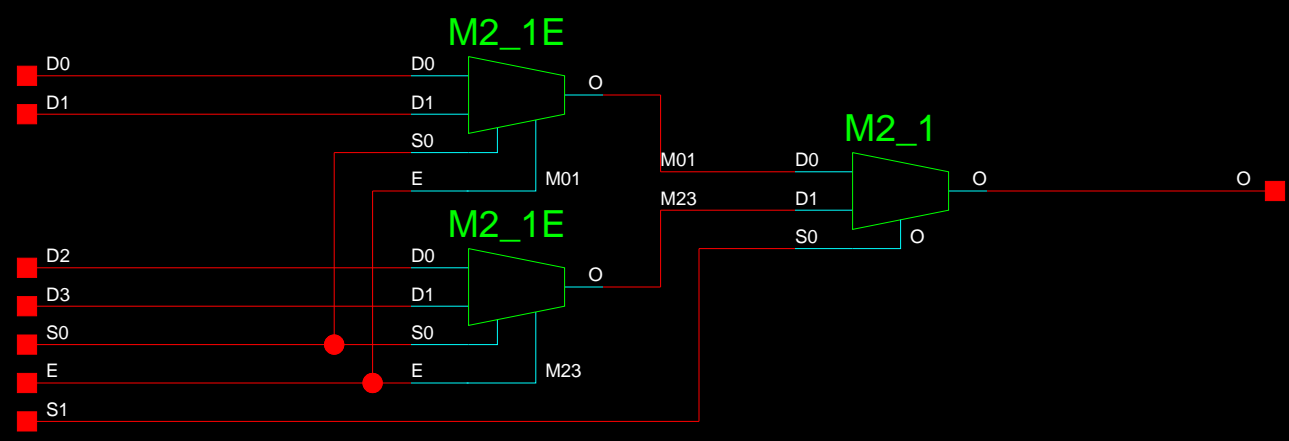
Comments: 2-to-1 Multiplexer

Date: 14th December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M4_1E Macro

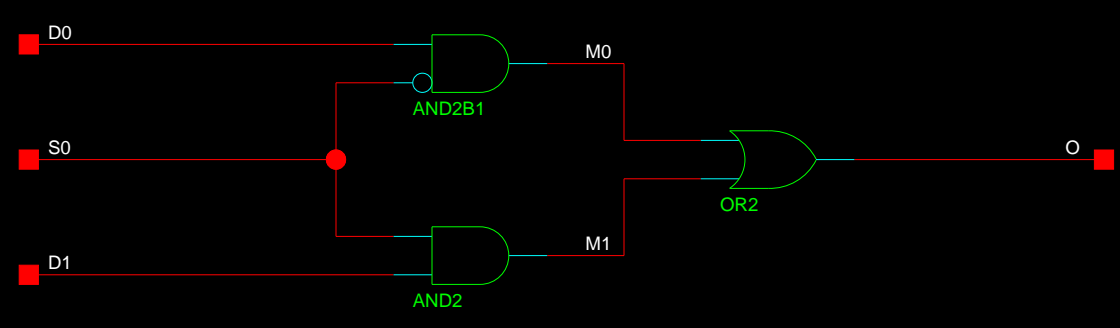
Comments: 4-to-1 Multiplexer with Enable

Date: 1st December 1994

Ver: 1

Sheet Size: A

Rev: B



Title: XC4000 Family M2_1 Macro

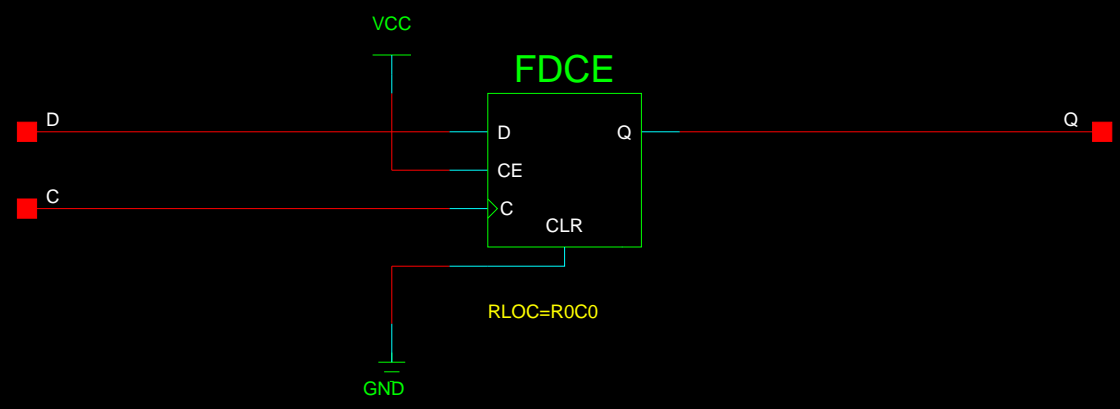
Comments: 2-to-1 Multiplexer

Date: 14th December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

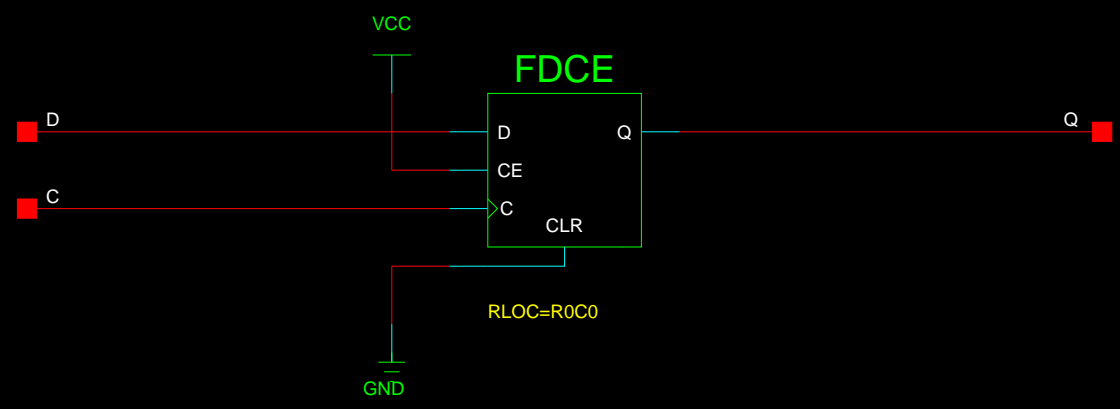
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

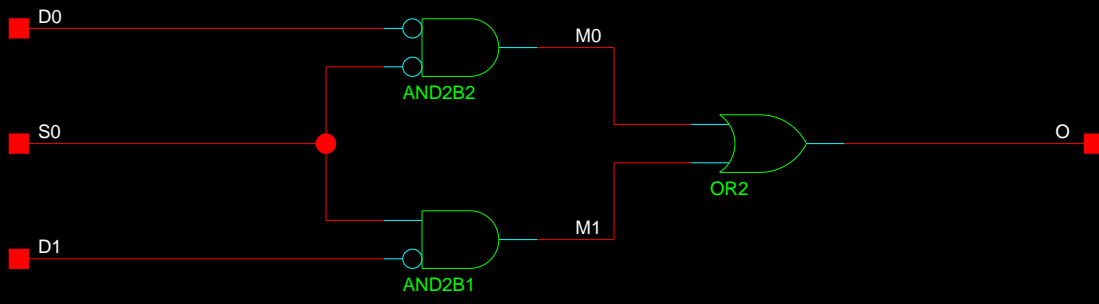
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M2_1B2 Macro

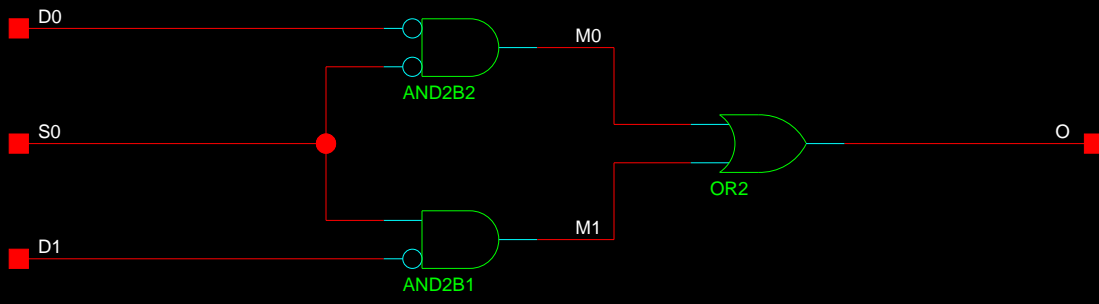
Comments: 2-to-1 Multiplexer w/
D0 & D1 Inverted

Date: 1st March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M2_1B2 Macro

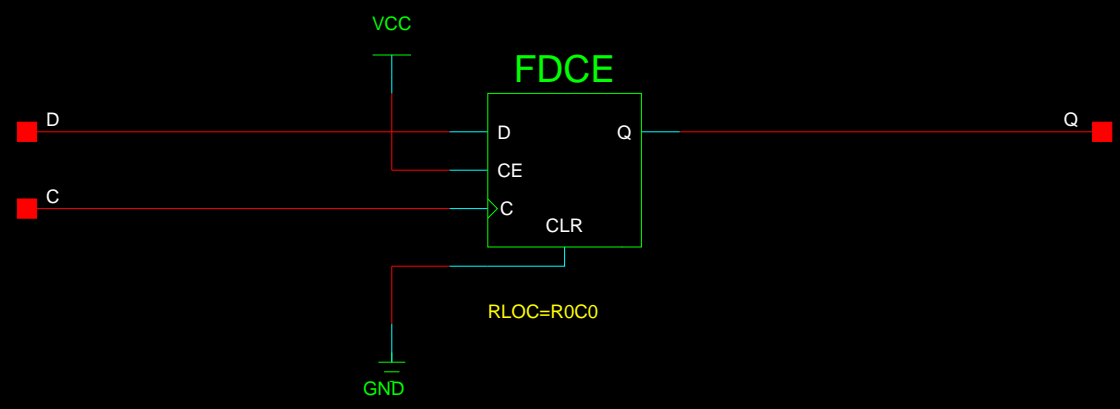
Comments: 2-to-1 Multiplexer w/
D0 & D1 Inverted

Date: 1st March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

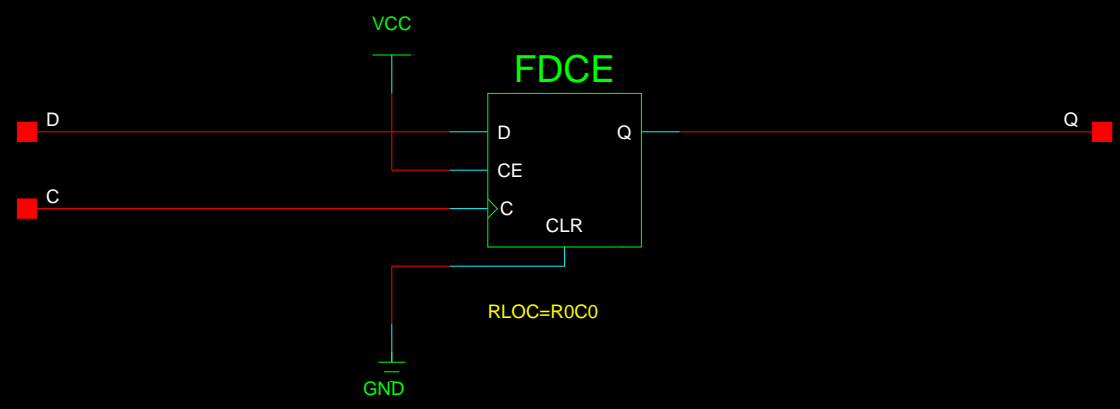
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

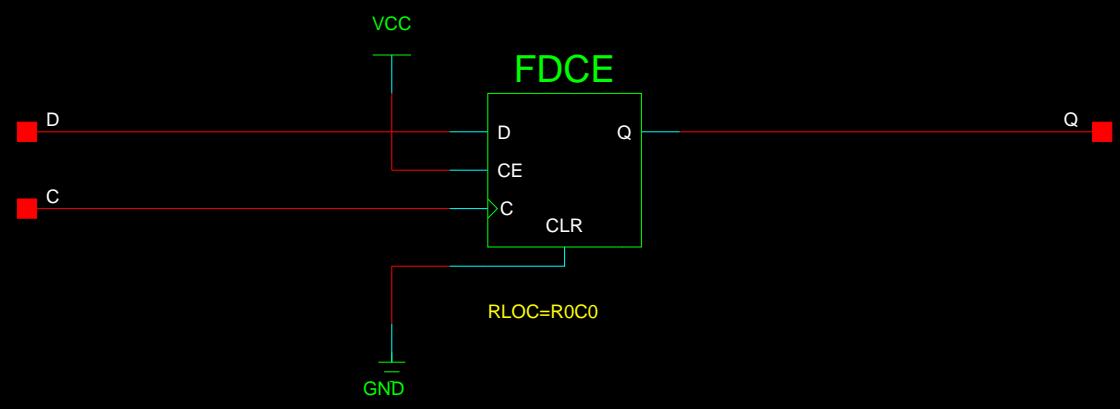
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

Comments: D Flip-Flop

Date: 23rd December 1992

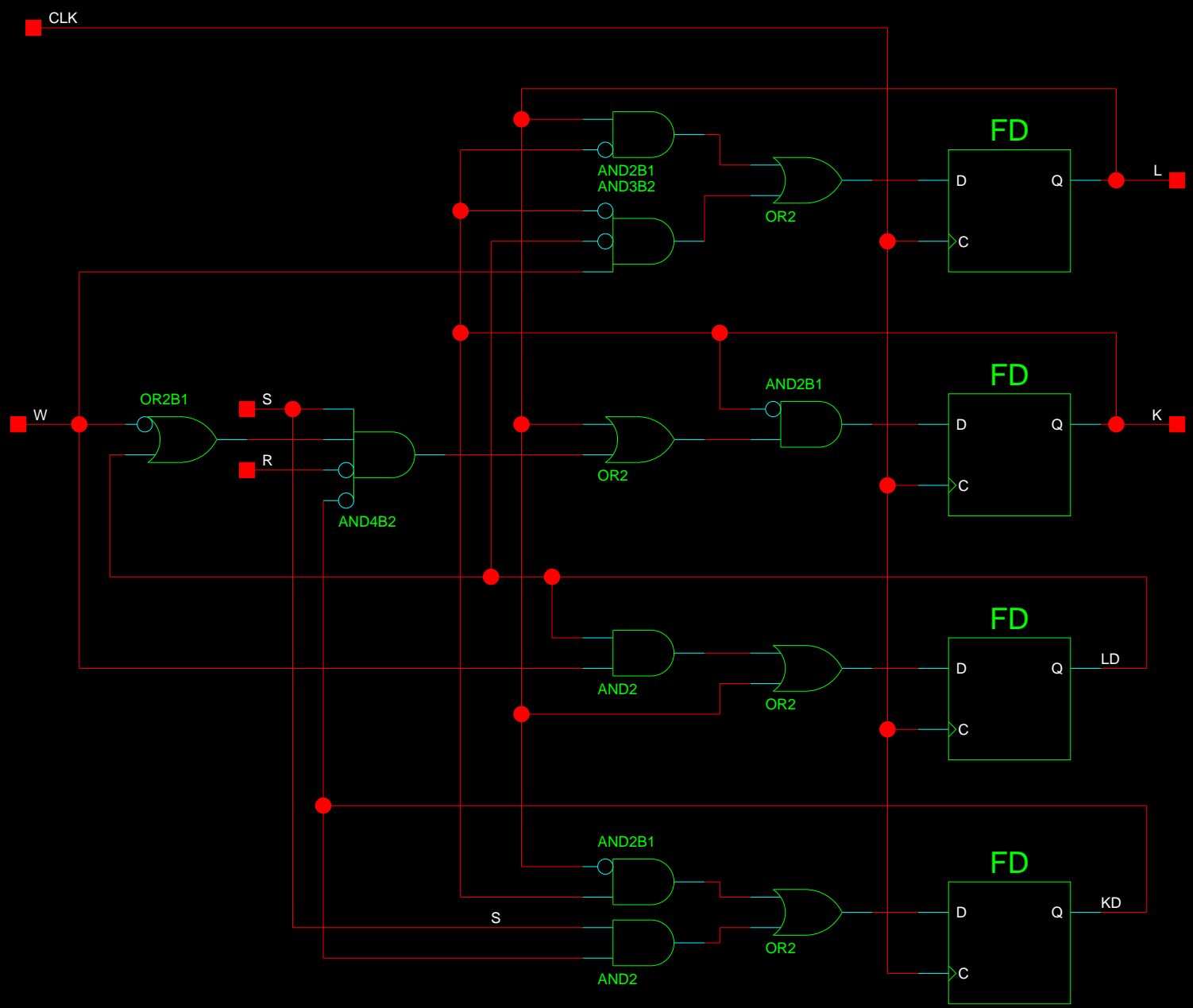
Ver: 1

Sheet Size: A

Rev: A

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HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE
CTSTATE.1
 State Machine for Counter Latch

PREPARED		
CHECKED		
ENGINEER	NTA	

ASHEET
 4-20-2005_16:20

size	Code ID No.	Drawing No.	rev
A		A34-7005	A
Scale	sheet 1 of 1		16 of 18

A

B

1

1

2

2

3

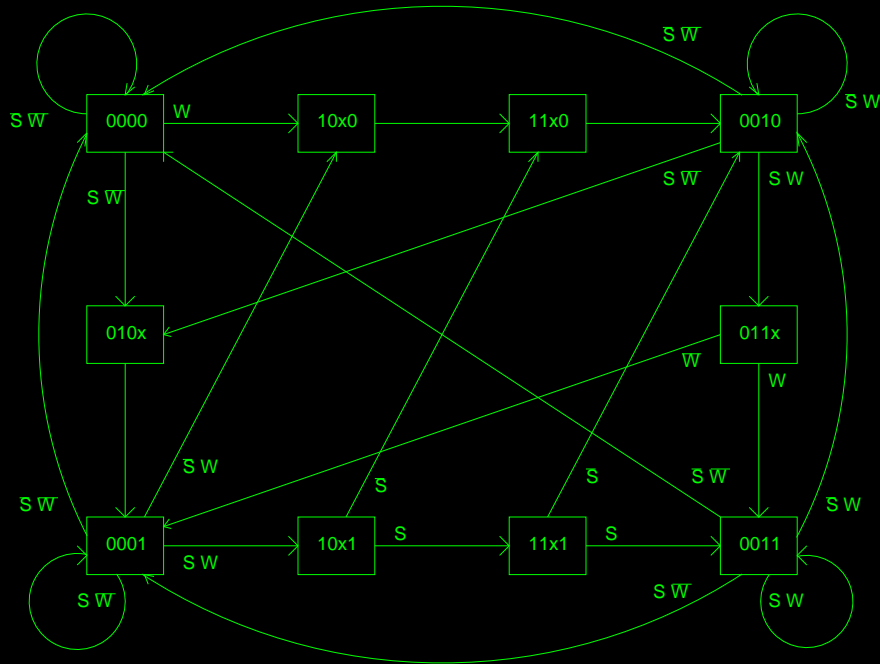
3

4

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5

5



LEGEND:

S: Step Clock Input

W: Write Input

L: Latch Output

K: Counter Clock Output

LD: Latched State

KD: Clocked State

10x0

x in a state block indicates
"either" as a destination and
"both" as a source.

HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE

CTSTATE.2

State Machine for Counter Latch

PREPARED		
CHECKED		
ENGINEER	NTA	

ASHEET

4-20-2005_16:20

size	Code ID No.	Drawing No.	rev
A		A34-7005	A
Scale	sheet 1 of 1		17 of 18

D

D

C

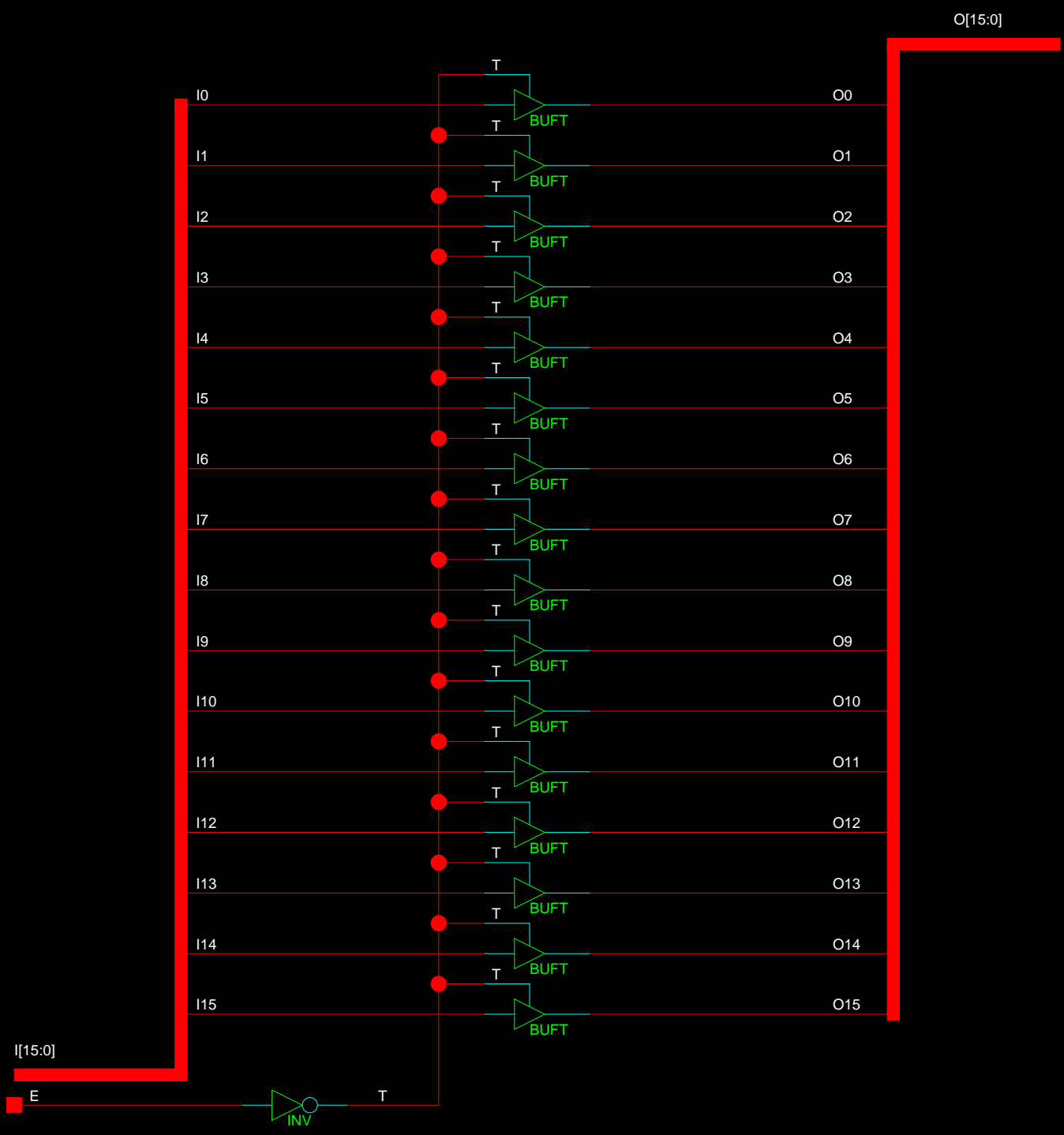
C

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B

A

A



Title:	XC4000 Family BUFE16 Macro		
Comments:	16-Bit 3-State Buffer w/ an Active High Enable		
Date:	23rd February 1993	Ver:	1
Sheet Size:	A	Rev:	A

D

D

C

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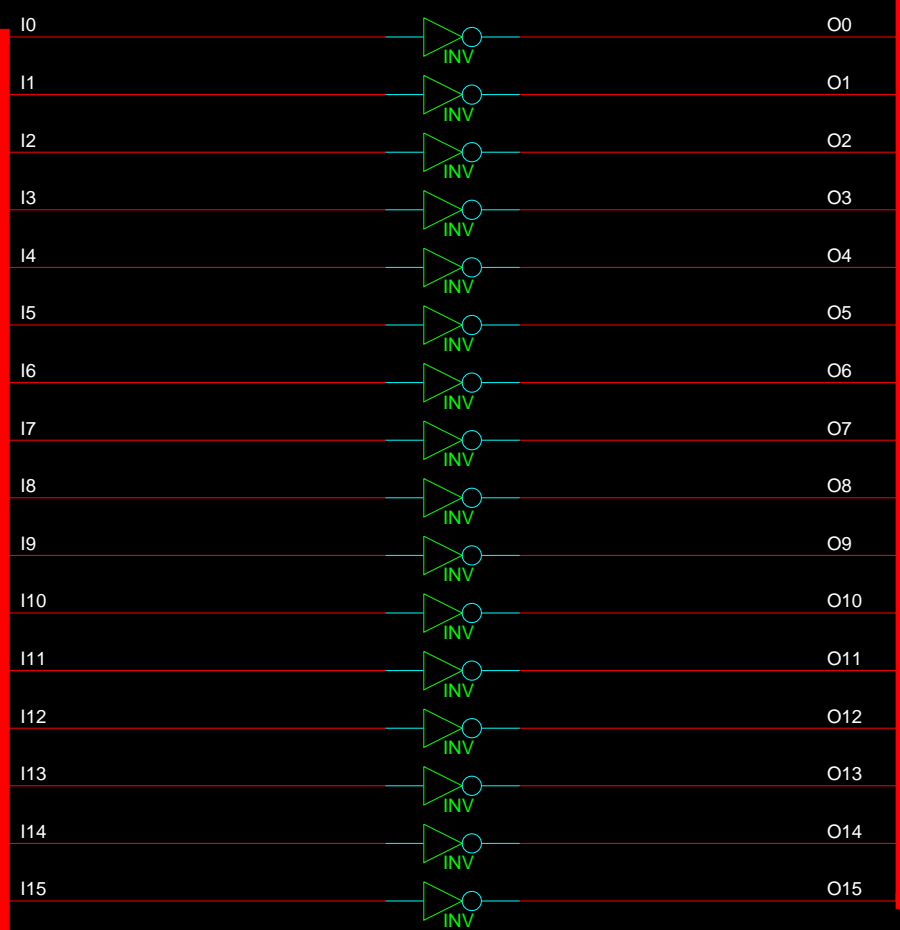
B

A

A

O[15:0]

I[15:0]

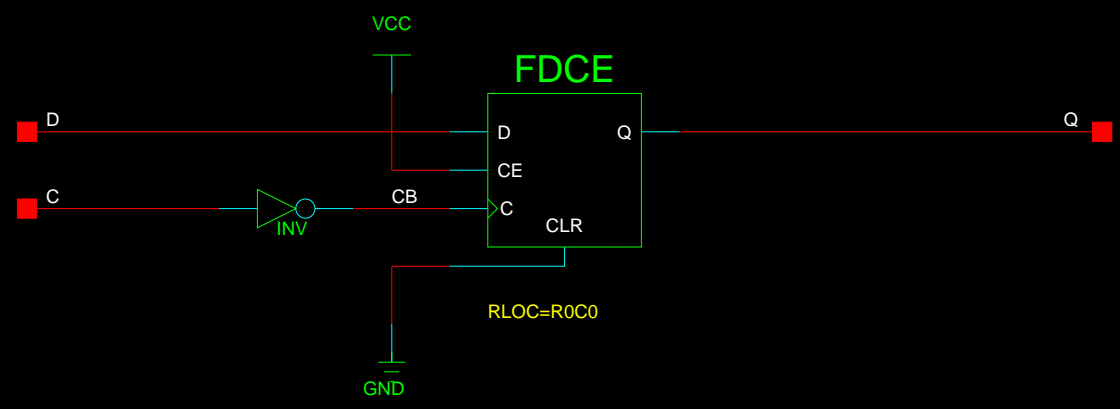


Title: XC4000 Family INV16 Macro

Comments: 16-bit Inverter

Date:	22nd February 1993	Ver:	1
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Sheet Size: A	Rev:	A
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Title: XC4000 Family FD_1 Macro

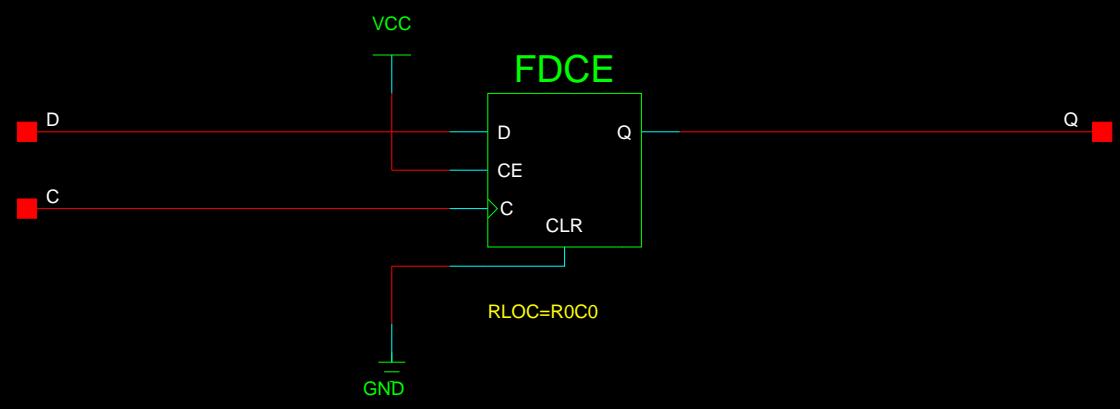
Comments: D Flip-Flop w/ an inverted Clock

Date: 1st March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

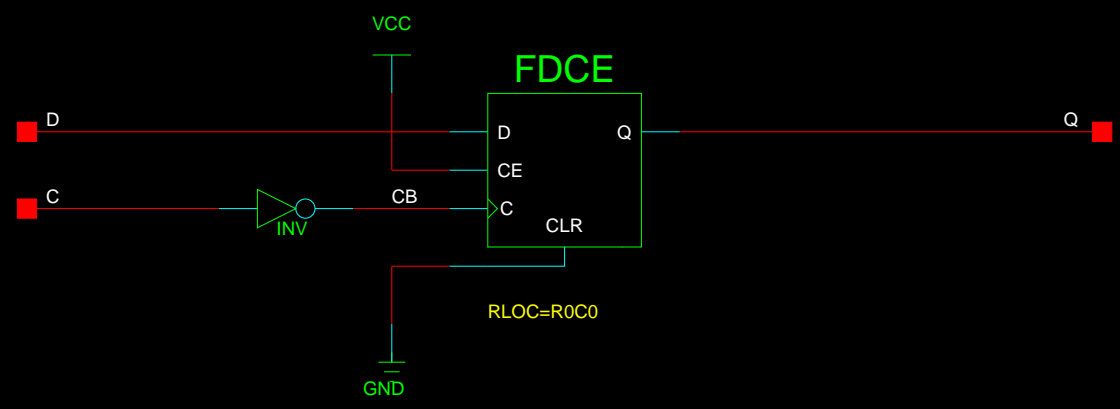
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD_1 Macro

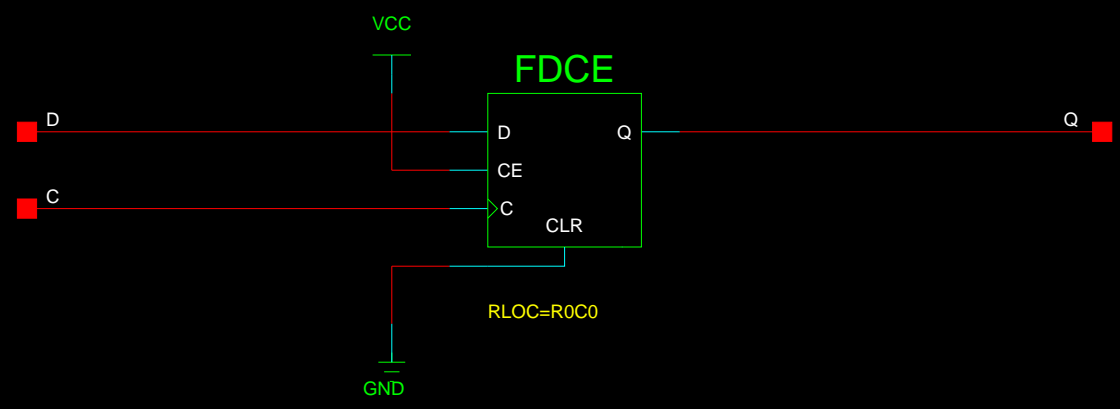
Comments: D Flip-Flop w/ an inverted Clock

Date: 1st March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

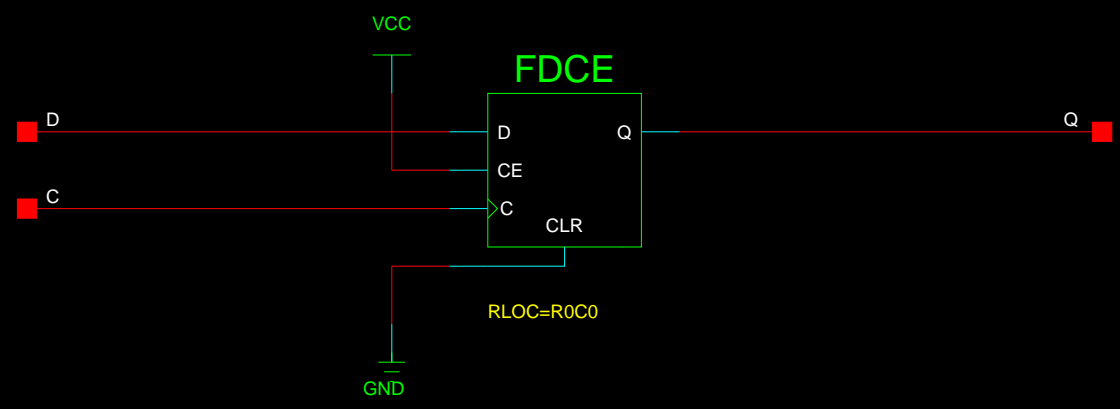
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

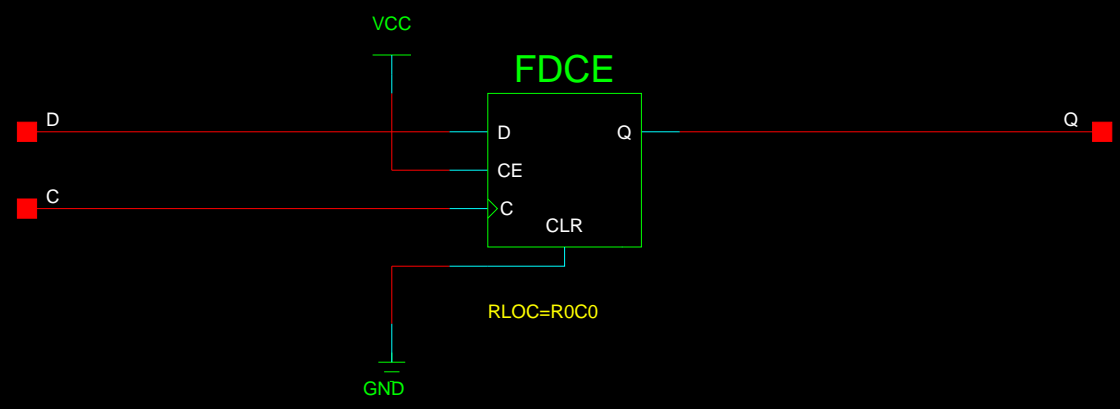
Comments: D Flip-Flop

Date: 23rd December 1992

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Rev: A



Title: XC4000 Family FD Macro

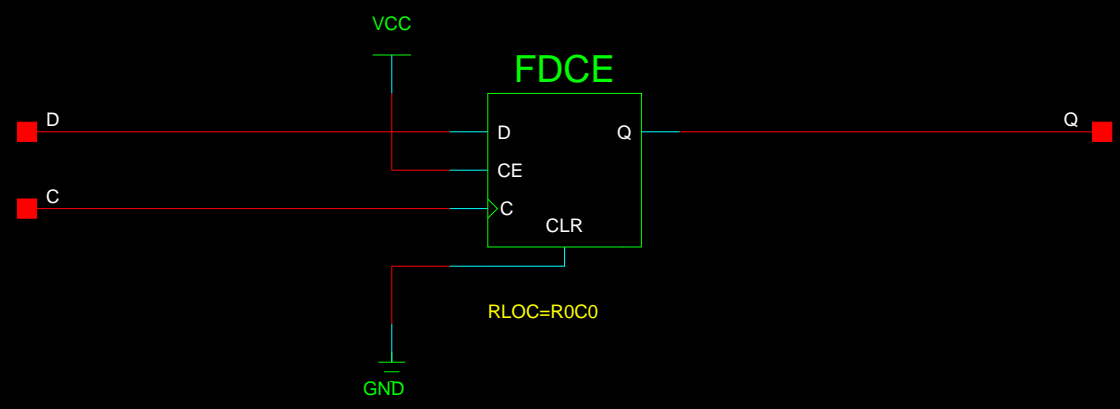
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M2_1 Macro

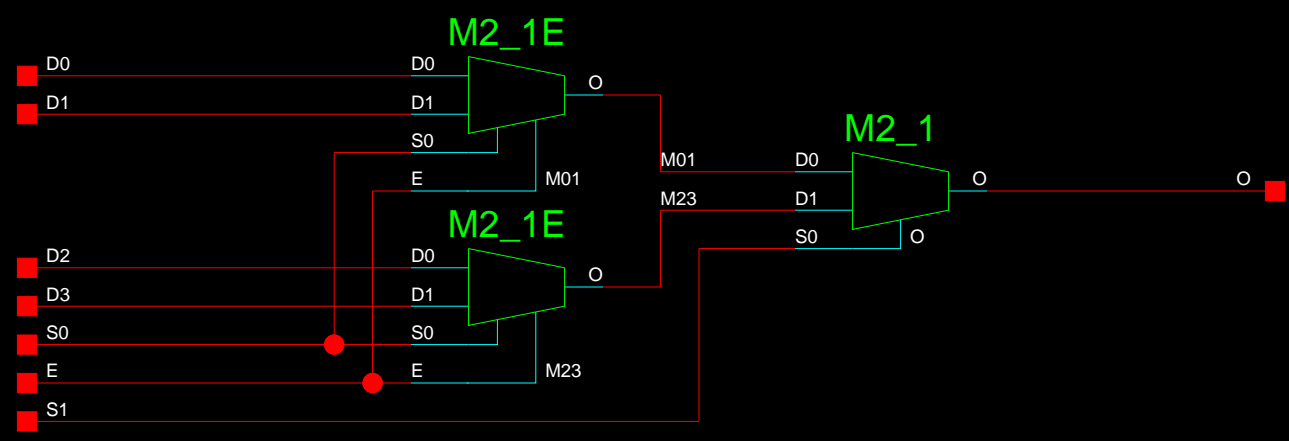
Comments: 2-to-1 Multiplexer

Date: 14th December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M4_1E Macro

Comments: 4-to-1 Multiplexer with Enable

Date: 1st December 1994

Ver: 1

Sheet Size: A

Rev: B



Title: XC4000 Family M2_1 Macro

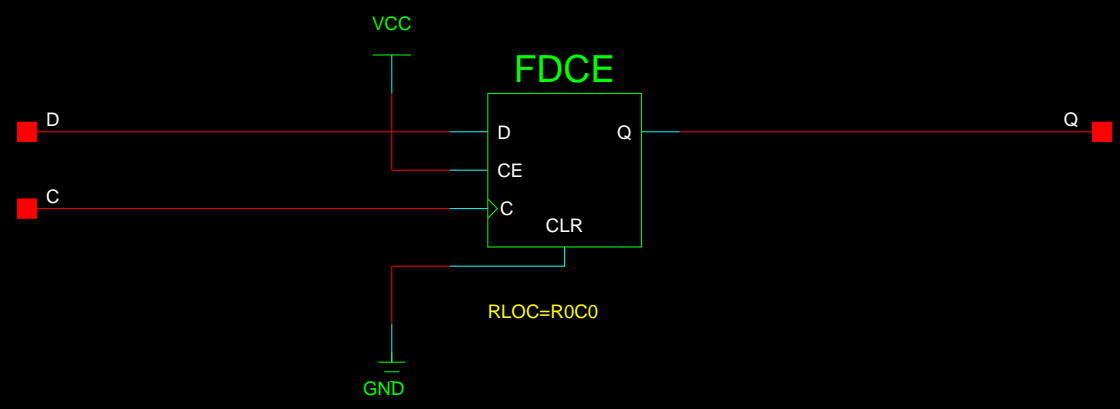
Comments: 2-to-1 Multiplexer

Date: 14th December 1992

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Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

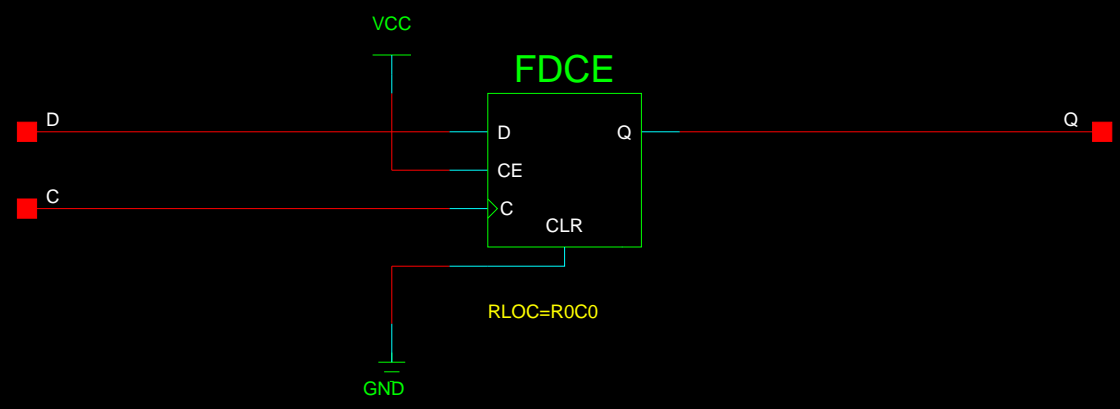
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

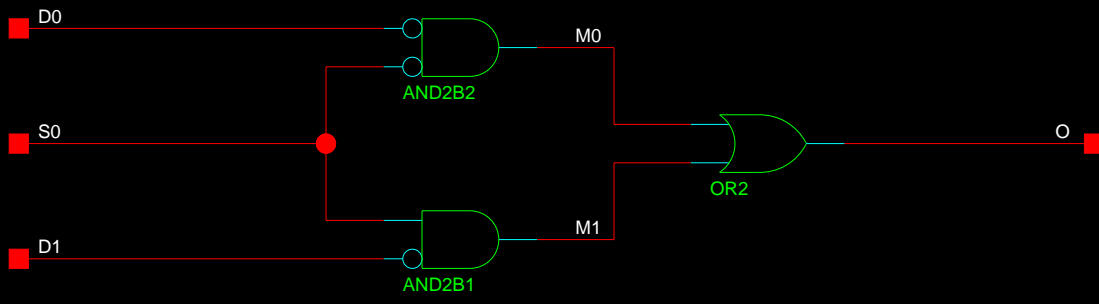
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M2_1B2 Macro

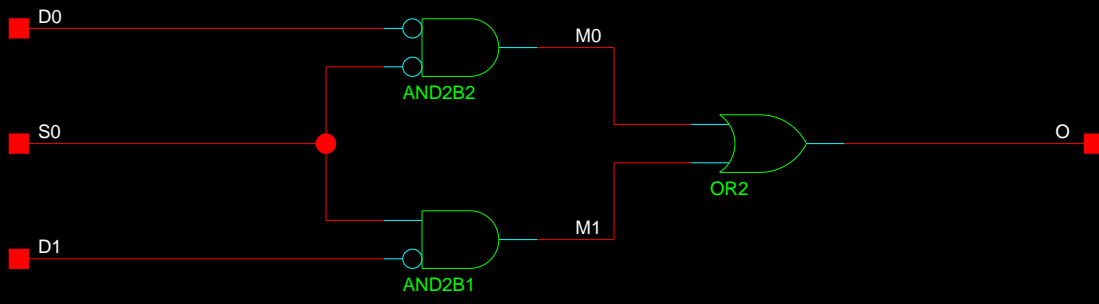
Comments: 2-to-1 Multiplexer w/
D0 & D1 Inverted

Date: 1st March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M2_1B2 Macro

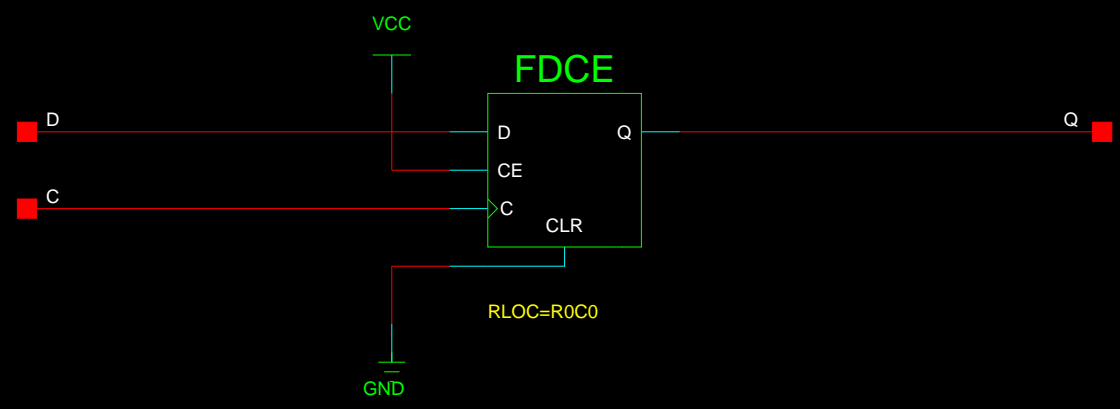
Comments: 2-to-1 Multiplexer w/
D0 & D1 Inverted

Date: 1st March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

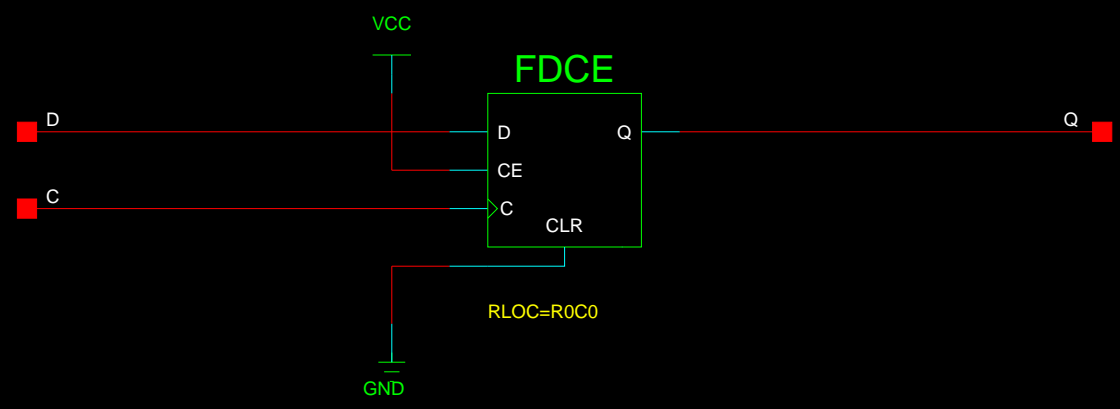
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

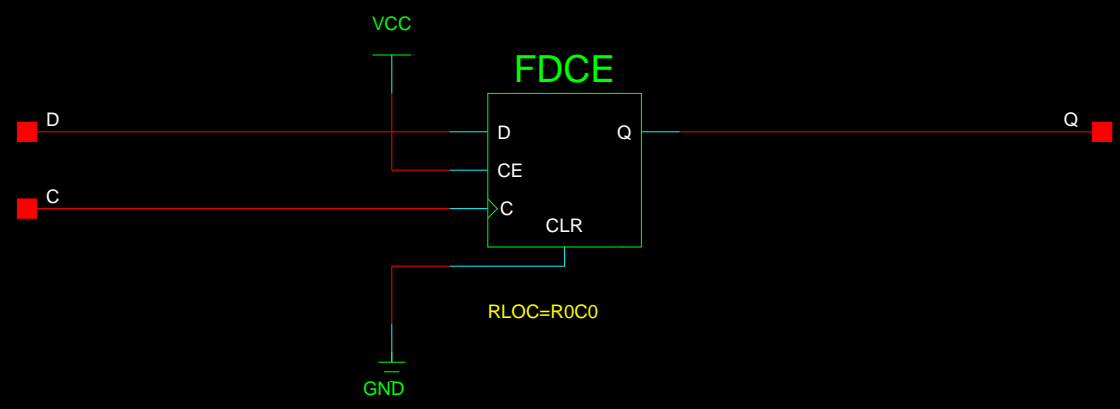
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

Comments: D Flip-Flop

Date: 23rd December 1992

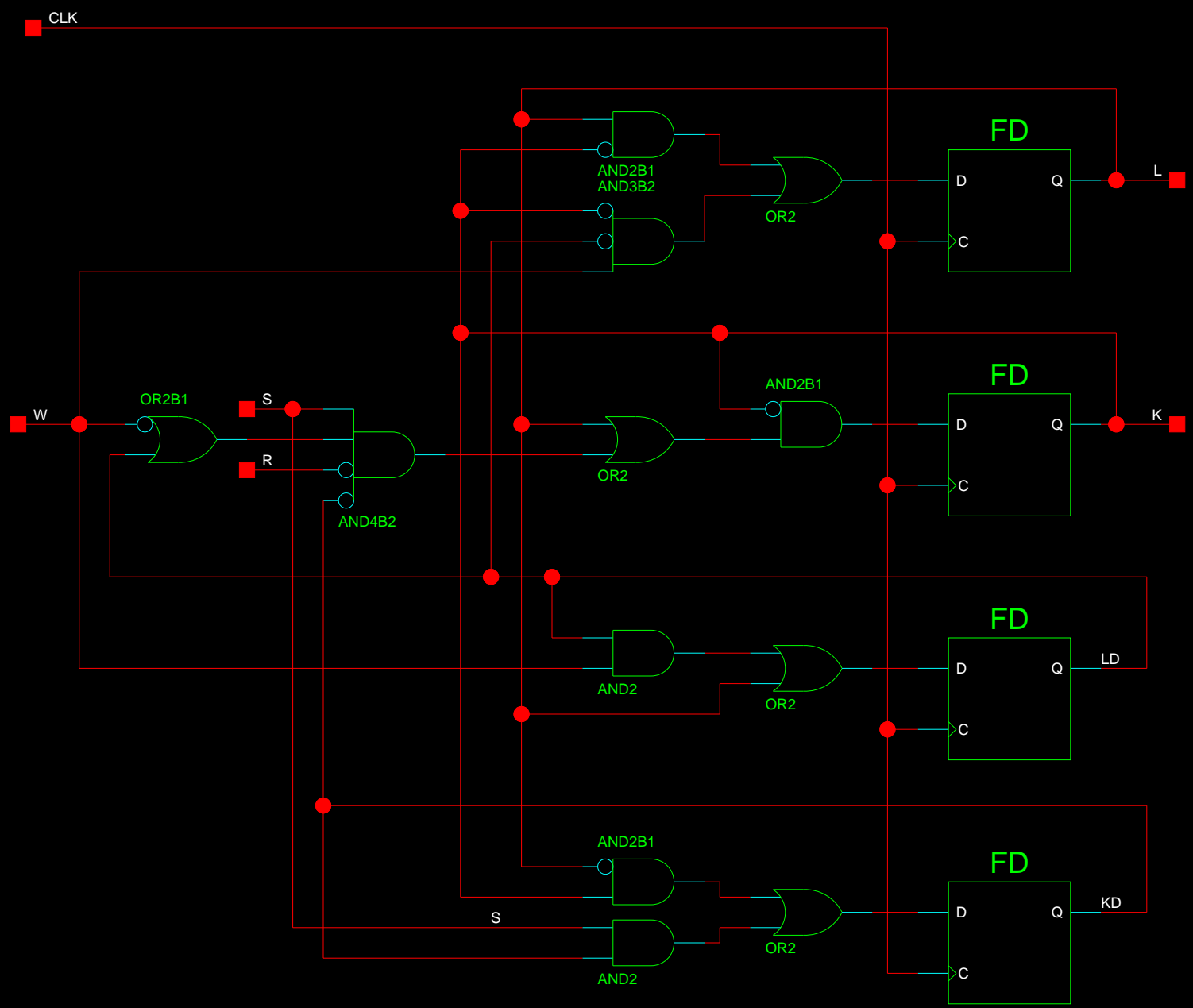
Ver: 1

Sheet Size: A

Rev: A

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B



HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE
CTSTATE.1
 State Machine for Counter Latch

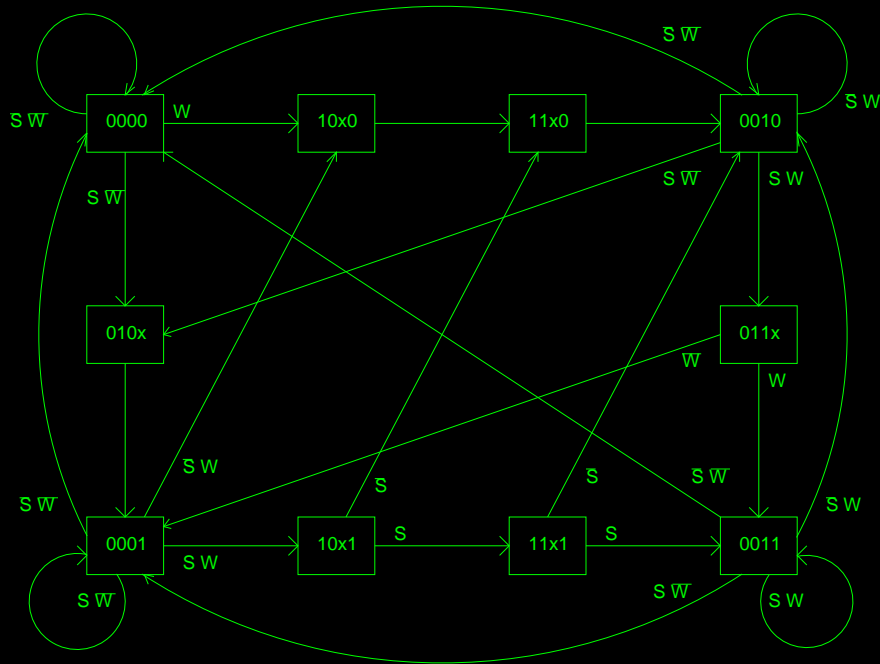
PREPARED		
CHECKED		
ENGINEER	NTA	

ASHEET
 4-20-2005_16:20

size A	Code ID No.	Drawing No. A34-7005	rev A
Scale	sheet 1 of 1		16 of 18

A

B



LEGEND:

S: Step Clock Input

W: Write Input

L: Latch Output

K: Counter Clock Output

LD: Latched State

KD: Clocked State

10x0

x in a state block indicates
"either" as a destination and
"both" as a source.

HARVARD UNIVERSITY
ATMOSPHERIC RESEARCH PROJECT

TITLE

CTSTATE.2

State Machine for Counter Latch

PREPARED		
CHECKED		
ENGINEER	NTA	

ASHEET

4-20-2005_16:20

size	Code ID No.	Drawing No.	rev
A		A34-7005	A
Scale	sheet 1 of 1		17 of 18

D

D

C

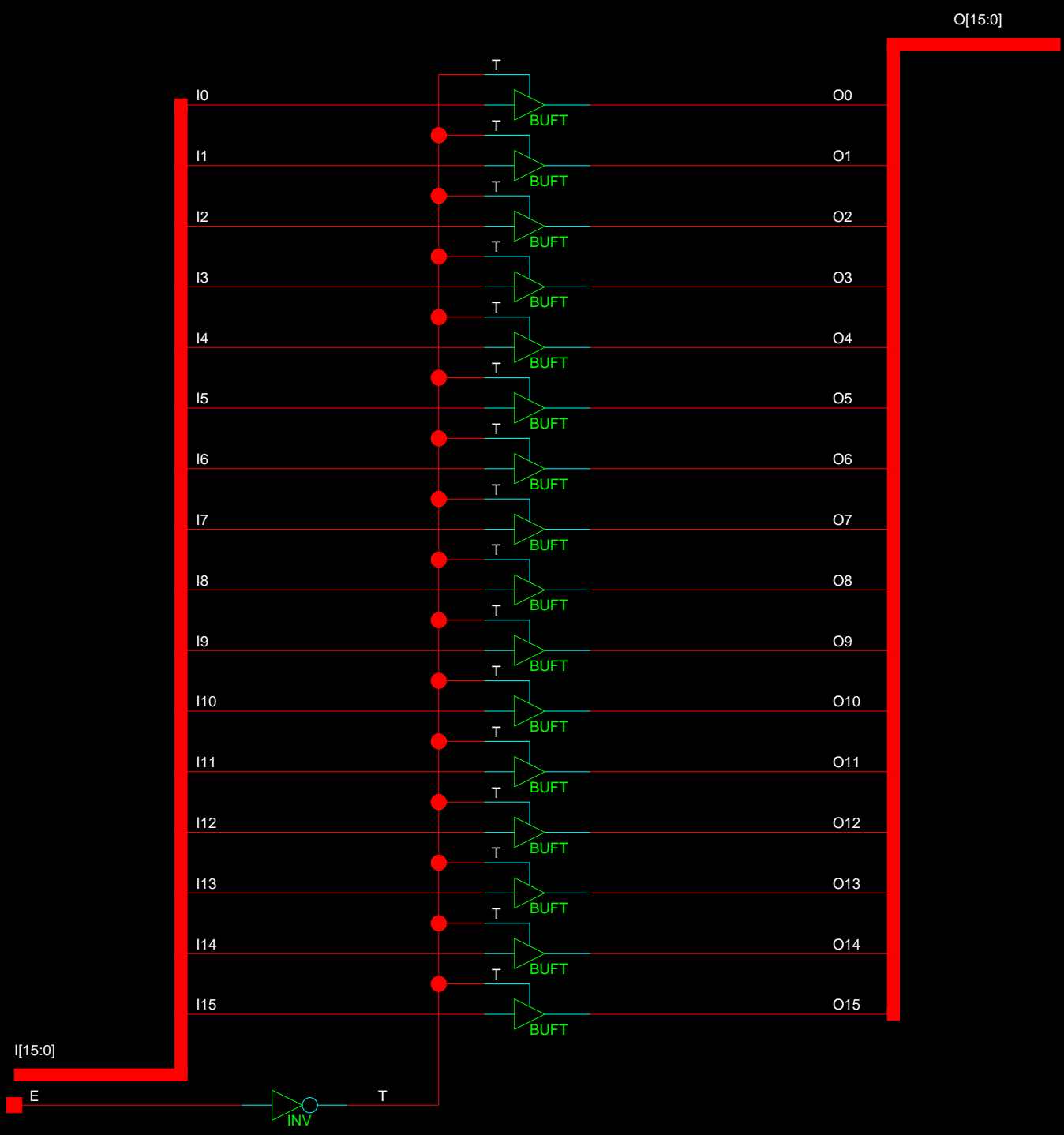
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B

B

A

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Title:	XC4000 Family BUFE16 Macro		
Comments:	16-Bit 3-State Buffer w/ an Active High Enable		
Date:	23rd February 1993	Ver:	1
Sheet Size:	A	Rev:	A

D

D

C

C

B

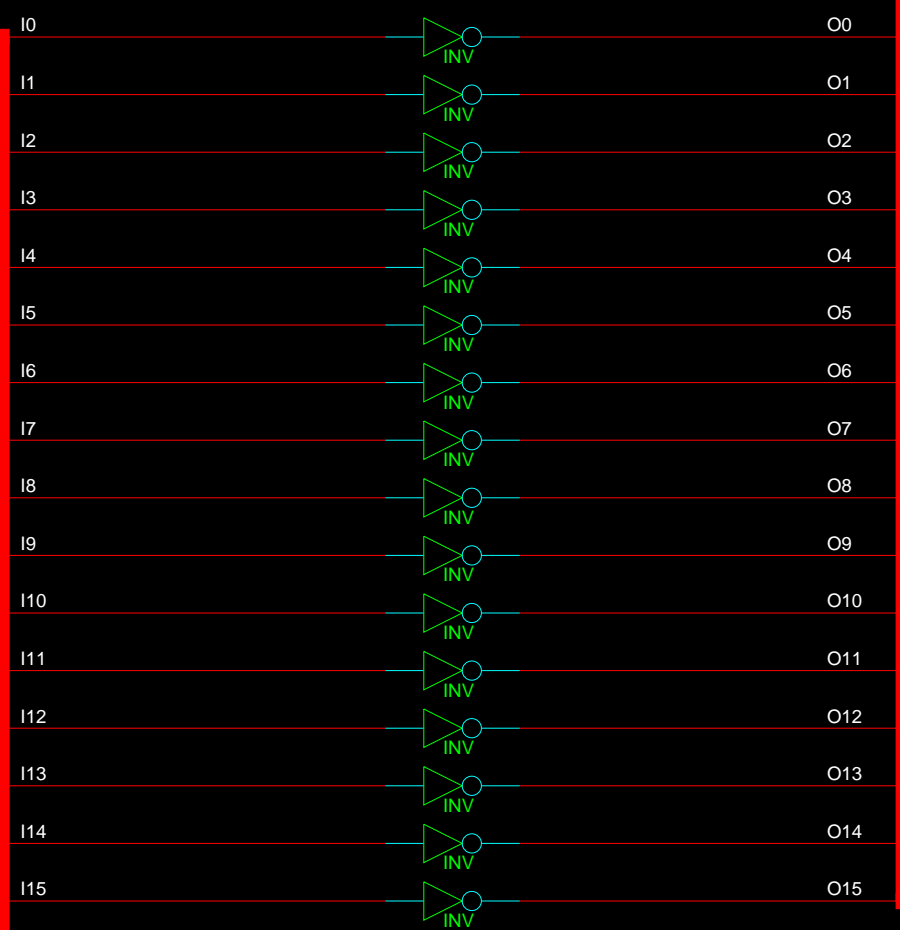
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O[15:0]

I[15:0]

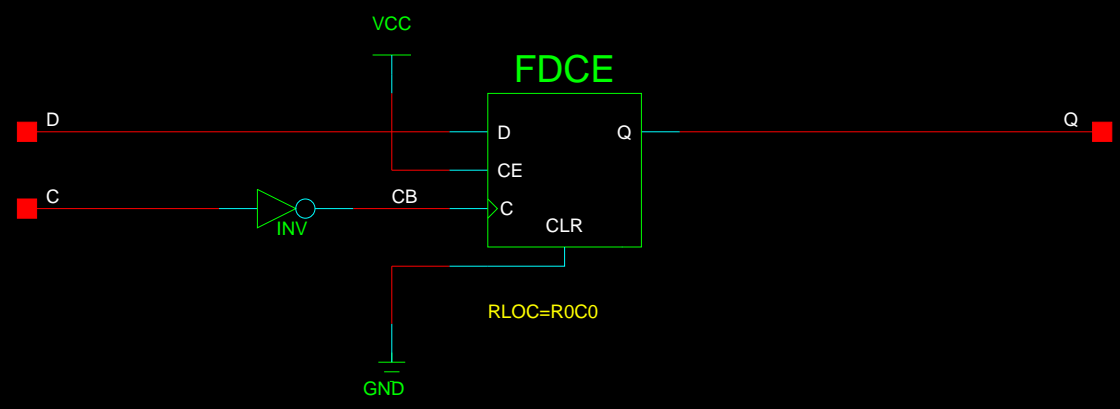


Title: XC4000 Family INV16 Macro

Comments: 16-bit Inverter

Date:	22nd February 1993	Ver:	1
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Sheet Size: A	Rev:	A
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Title: XC4000 Family FD_1 Macro

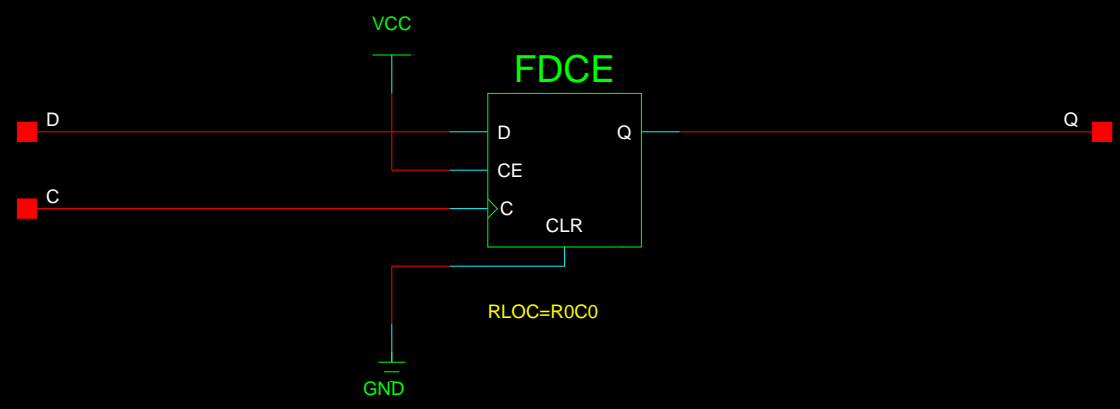
Comments: D Flip-Flop w/ an inverted Clock

Date: 1st March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

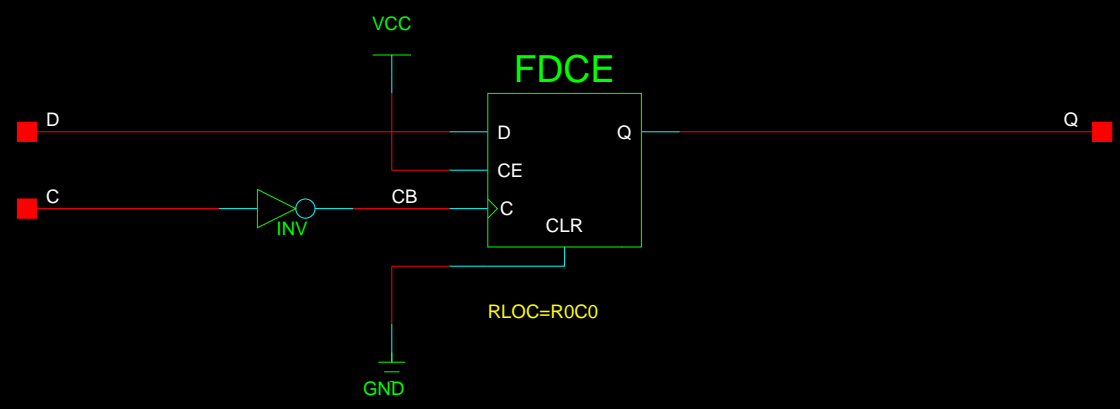
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Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD_1 Macro

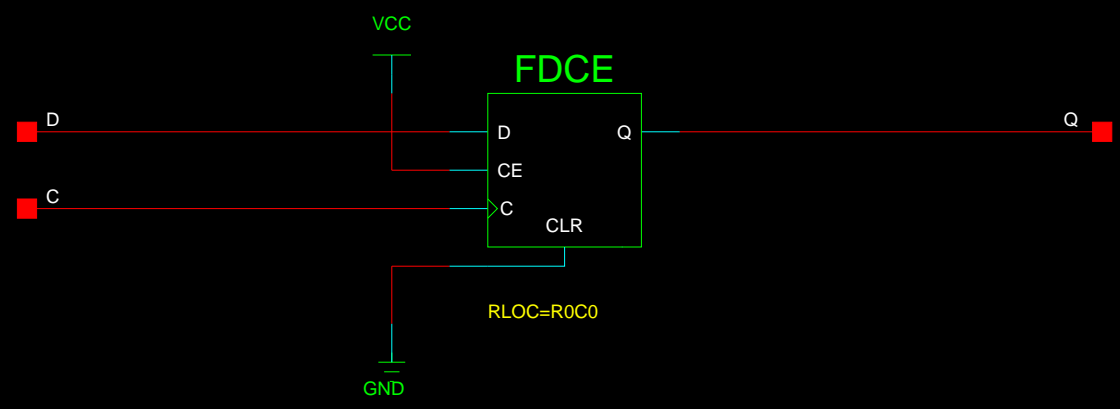
Comments: D Flip-Flop w/ an inverted Clock

Date: 1st March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

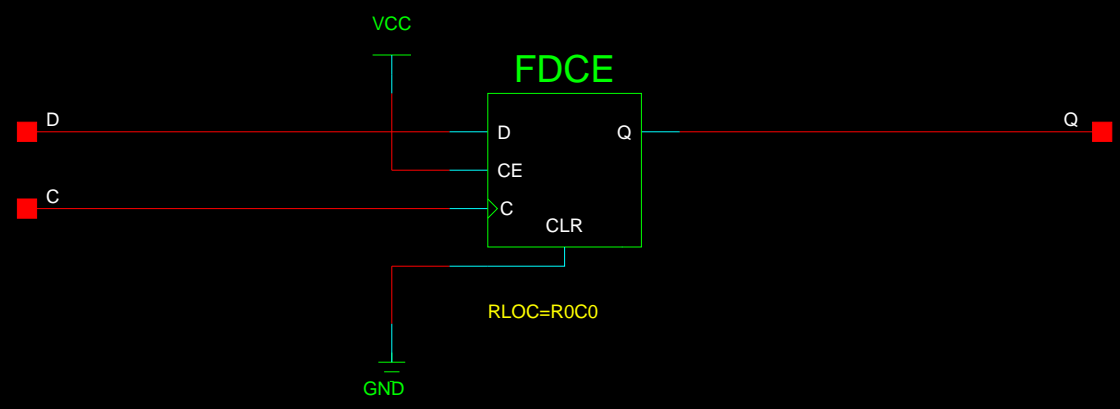
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



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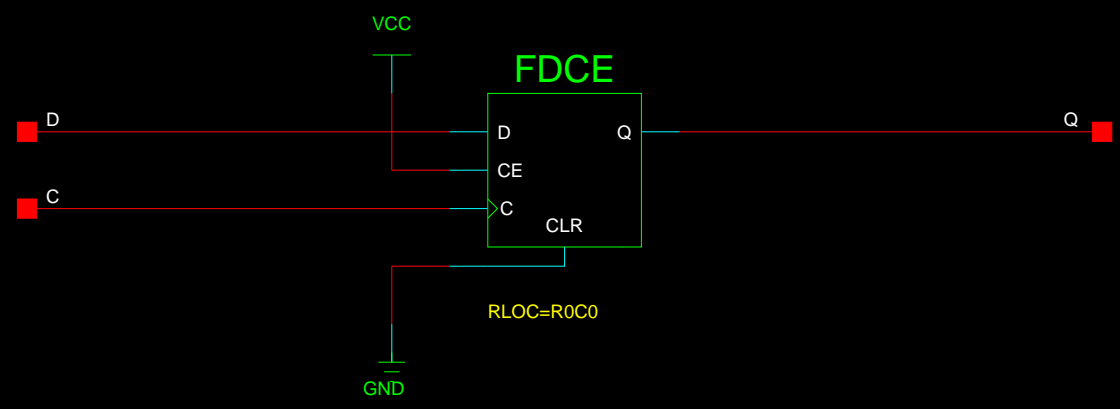
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Rev: A



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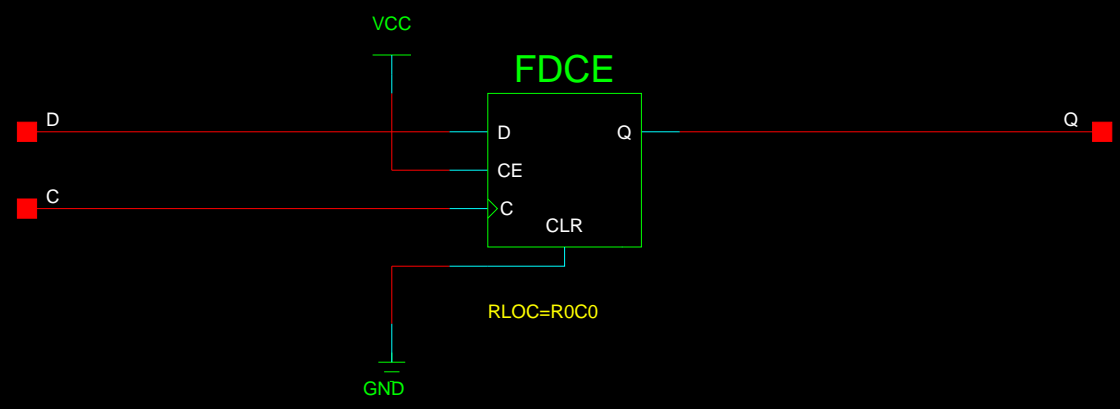
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Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M2_1 Macro

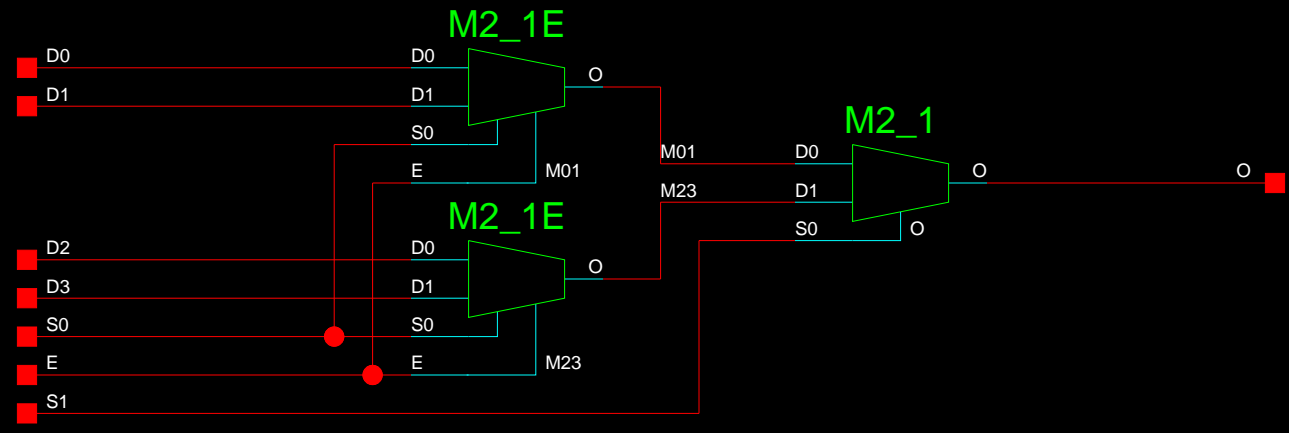
Comments: 2-to-1 Multiplexer

Date: 14th December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M4_1E Macro

Comments: 4-to-1 Multiplexer with Enable

Date: 1st December 1994

Ver: 1

Sheet Size: A

Rev: B



Title: XC4000 Family M2_1 Macro

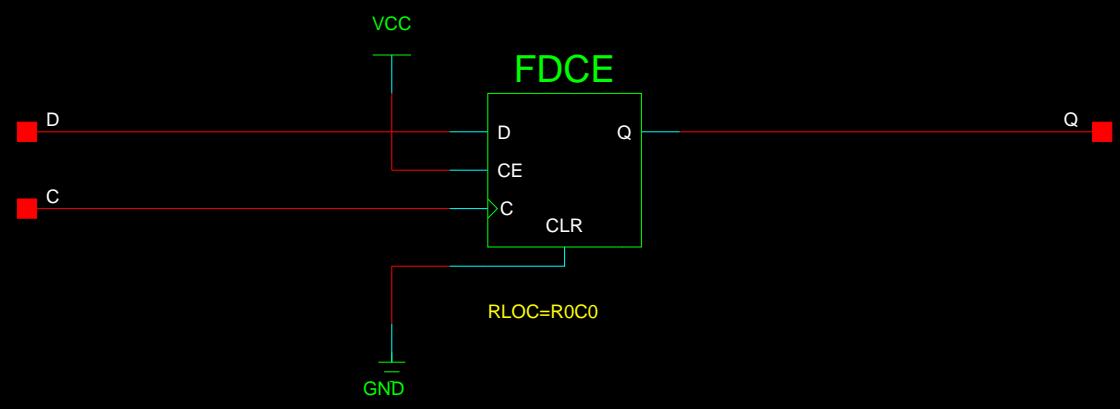
Comments: 2-to-1 Multiplexer

Date: 14th December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family FD Macro

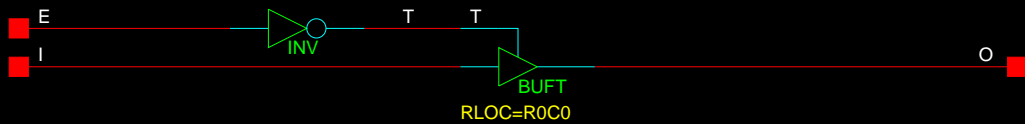
Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

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Rev: A



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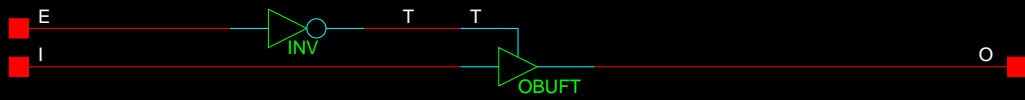
Comments: 3-State Buffer w/ Active High En

Date: 3rd March 1993

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family OBUFE Macro

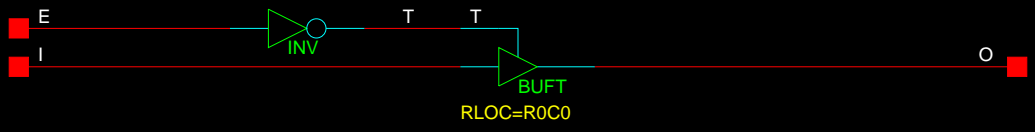
Comments: 3-State Output Buffer with Active High Enable

Date: 22nd February 1993

Ver: 1

Sheet Size: A

Rev: A



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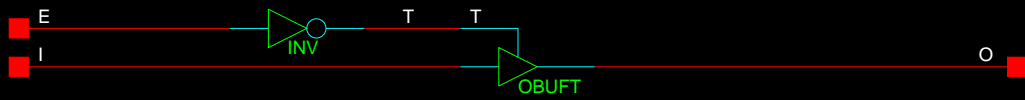
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Title: XC4000 Family OBUFE Macro

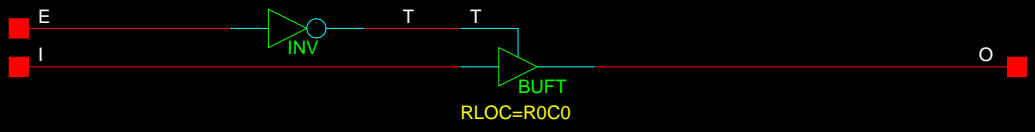
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Date: 22nd February 1993

Ver: 1

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Rev: A



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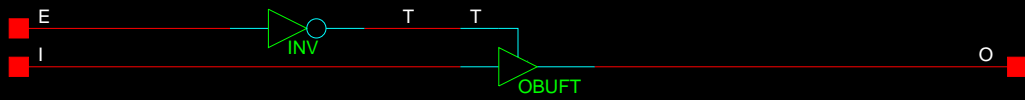
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Date: 3rd March 1993

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Rev: A



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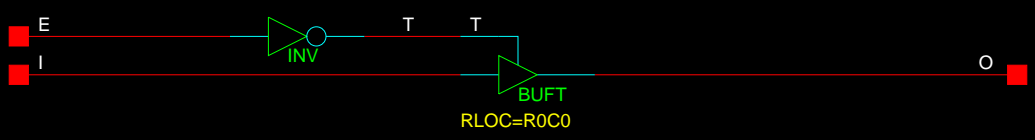
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Date: 22nd February 1993

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Rev: A



Title: XC4000 Family BUFE Macro

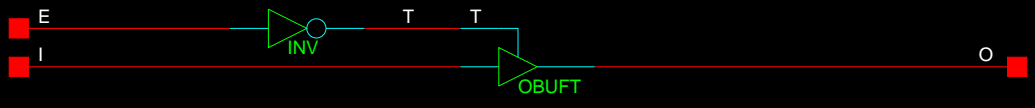
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Date: 3rd March 1993

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Rev: A



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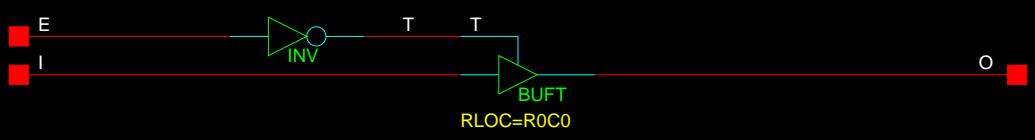
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Date: 22nd February 1993

Ver: 1

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Rev: A



Title: XC4000 Family BUFE Macro

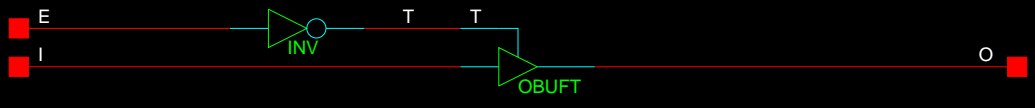
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Rev: A



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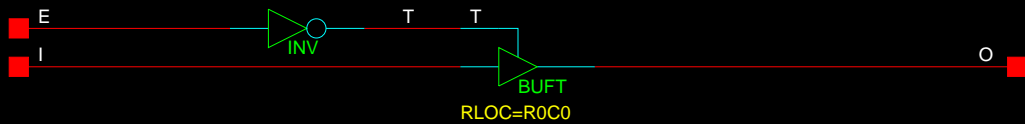
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Date: 22nd February 1993

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Rev: A



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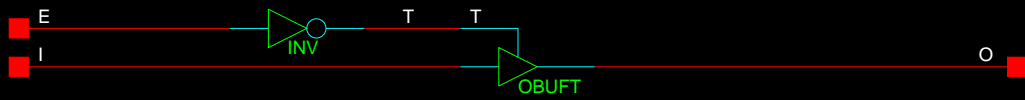
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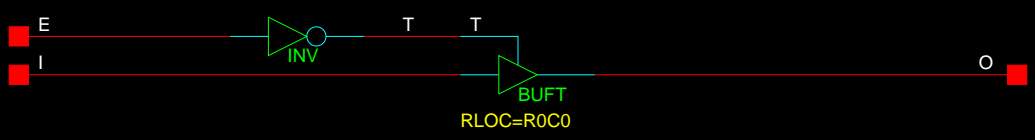
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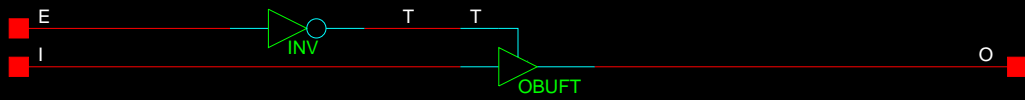
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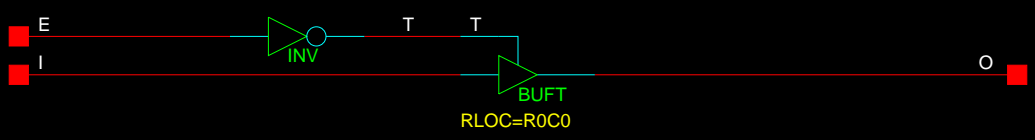
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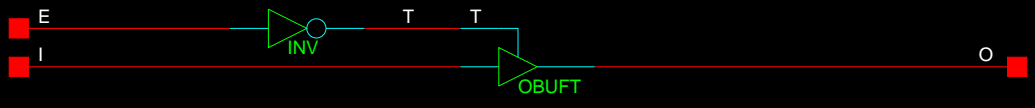
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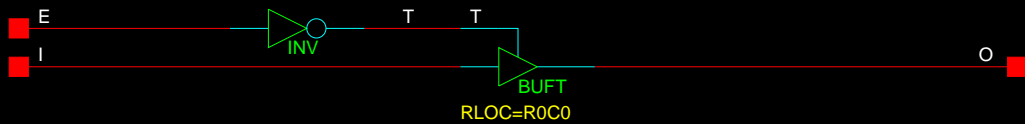
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Date: 22nd February 1993

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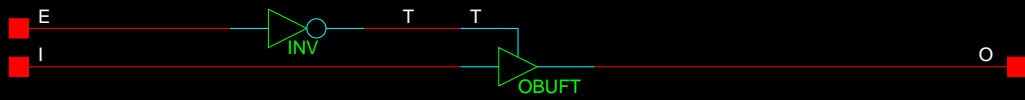
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Date: 3rd March 1993

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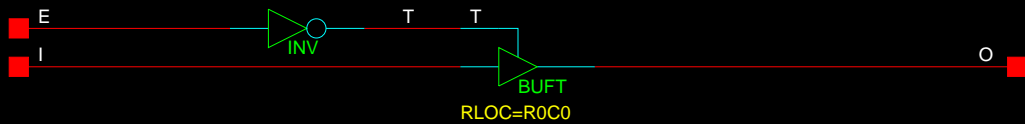
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Rev: A



Title: XC4000 Family BUFE Macro

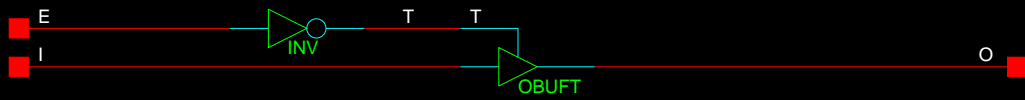
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Title: XC4000 Family OBUFE Macro

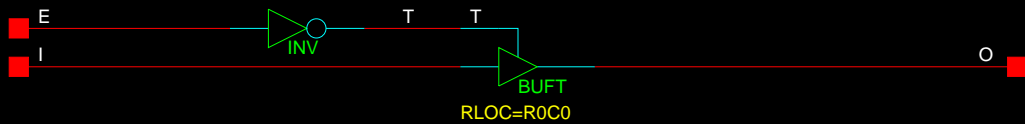
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Rev: A



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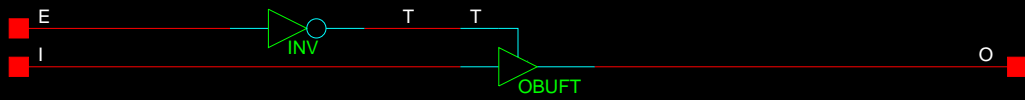
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Date: 3rd March 1993

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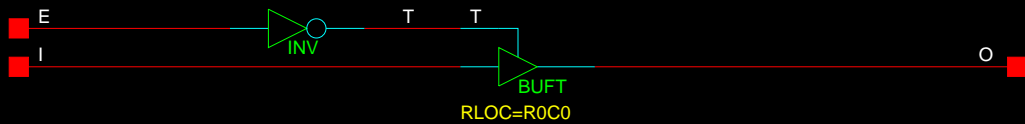
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Date: 22nd February 1993

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Rev: A



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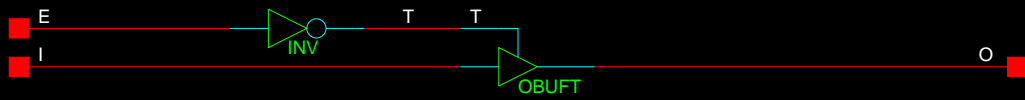
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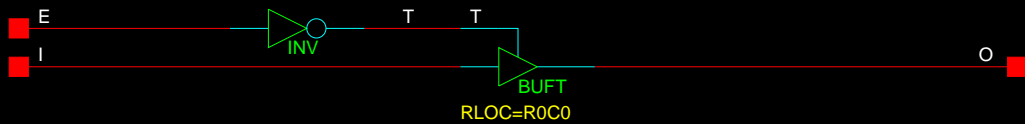
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Date: 22nd February 1993

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Rev: A



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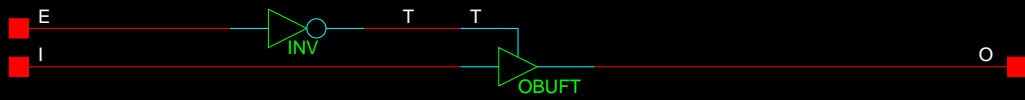
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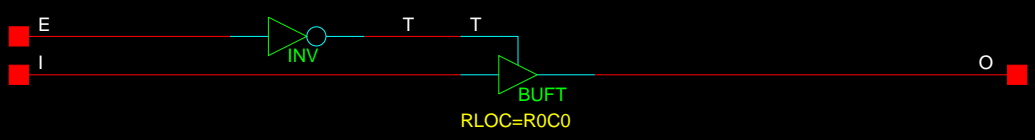
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Date: 22nd February 1993

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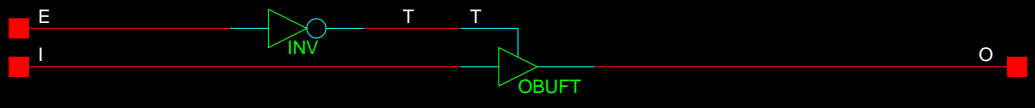
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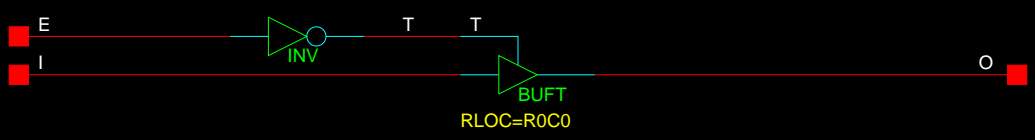
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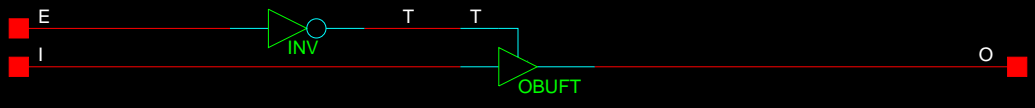
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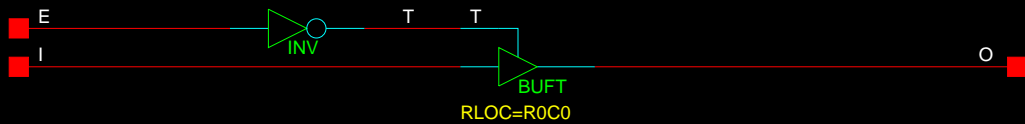
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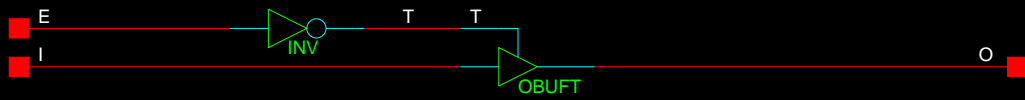
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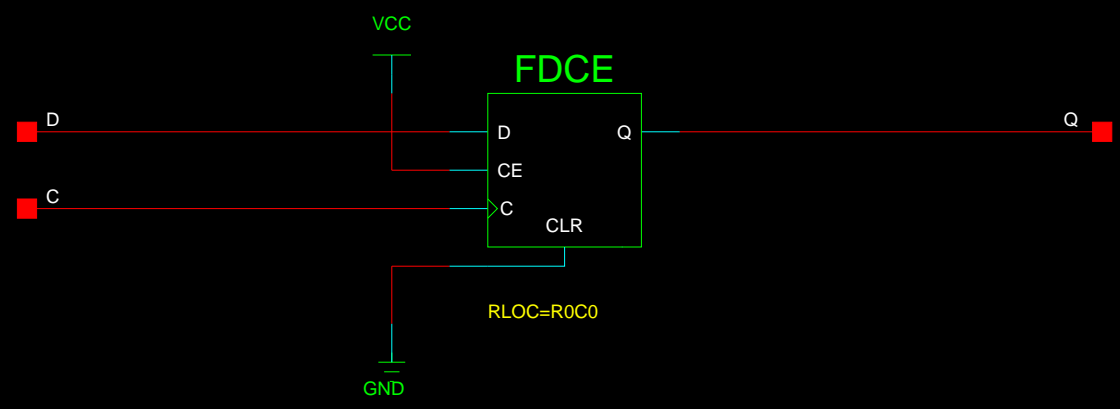
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Date: 22nd February 1993

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Rev: A



Title: XC4000 Family FD Macro

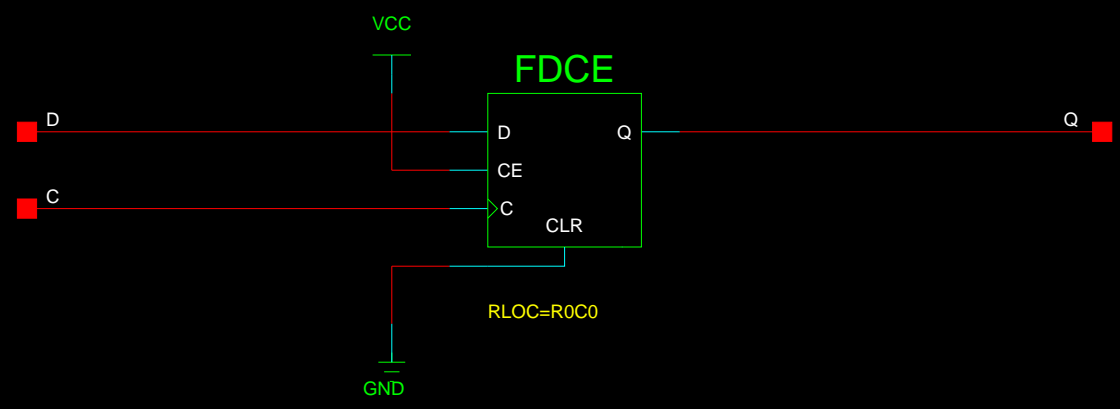
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Date: 23rd December 1992

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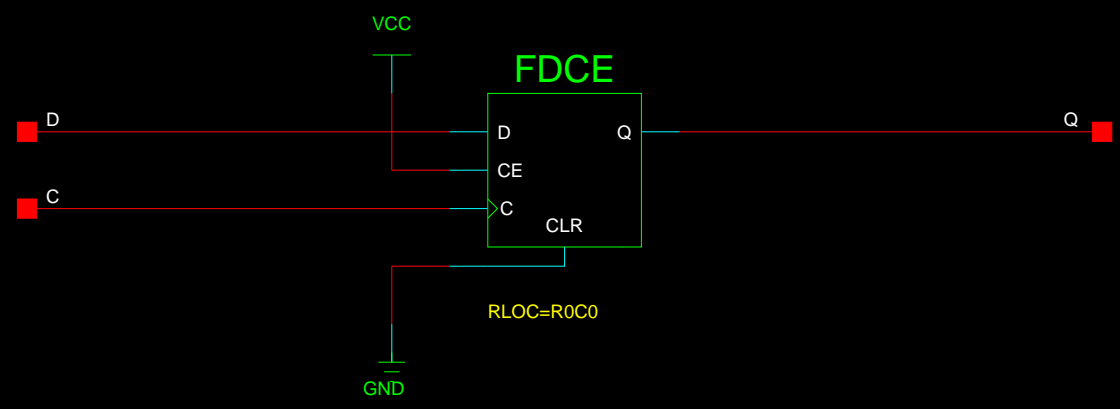
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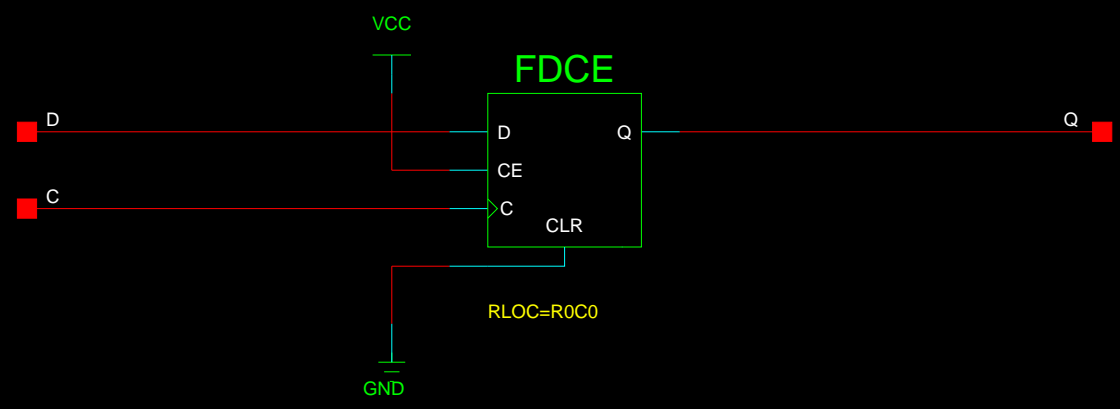
Comments: D Flip-Flop

Date: 23rd December 1992

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Rev: A



Title: XC4000 Family FD Macro

Comments: D Flip-Flop

Date: 23rd December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M2_1 Macro

Comments: 2-to-1 Multiplexer

Date: 14th December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M2_1 Macro

Comments: 2-to-1 Multiplexer

Date: 14th December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M2_1 Macro

Comments: 2-to-1 Multiplexer

Date: 14th December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M2_1 Macro

Comments: 2-to-1 Multiplexer

Date: 14th December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M2_1 Macro

Comments: 2-to-1 Multiplexer

Date: 14th December 1992

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Comments: 2-to-1 Multiplexer

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Comments: 2-to-1 Multiplexer

Date: 14th December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M2_1 Macro

Comments: 2-to-1 Multiplexer

Date: 14th December 1992

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Comments: 2-to-1 Multiplexer

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Ver: 1

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Title: XC4000 Family M2_1 Macro

Comments: 2-to-1 Multiplexer

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Date: 14th December 1992

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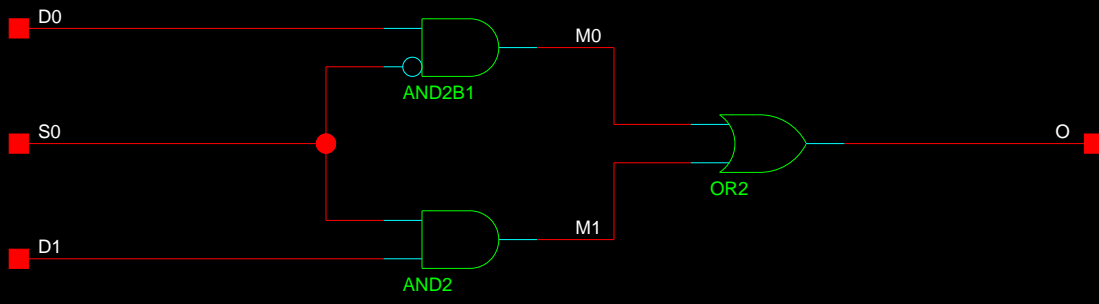
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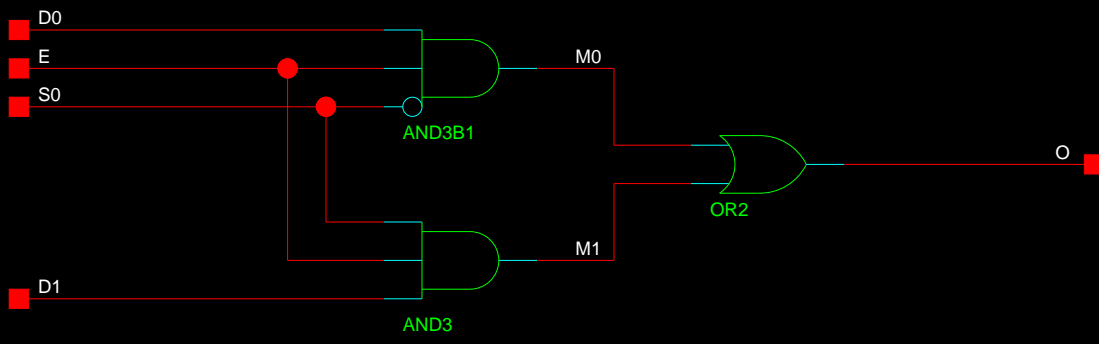
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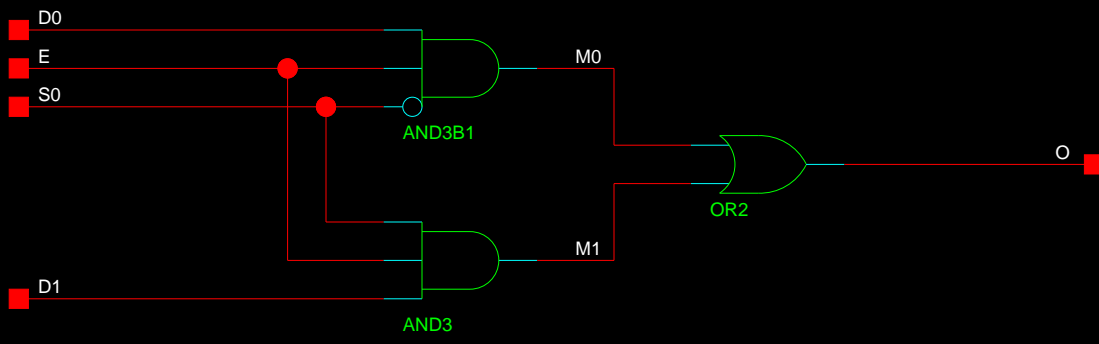
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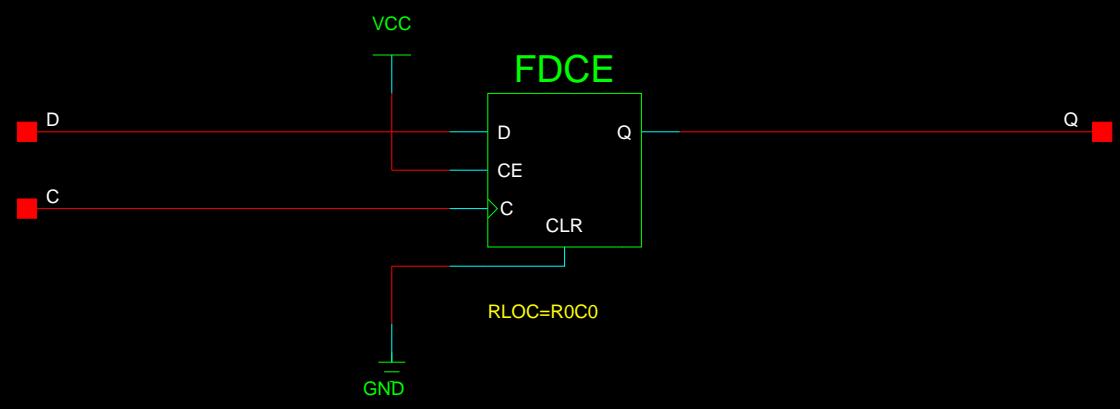
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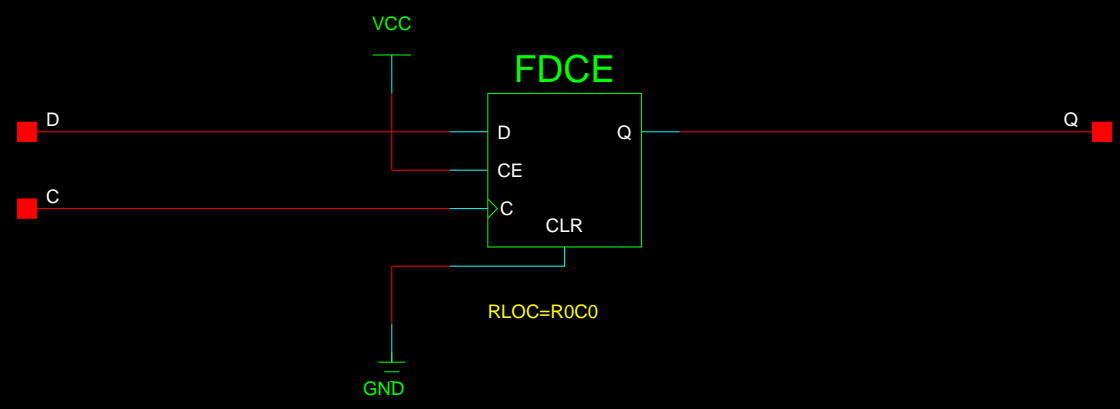
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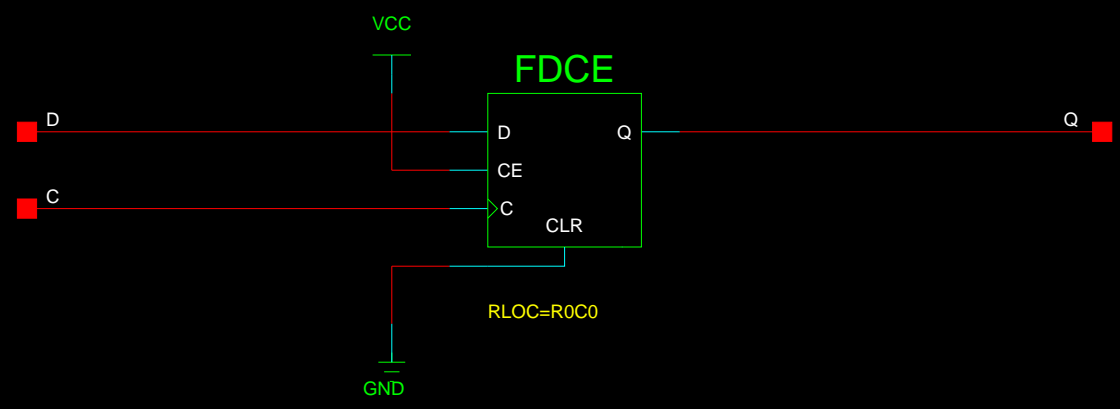
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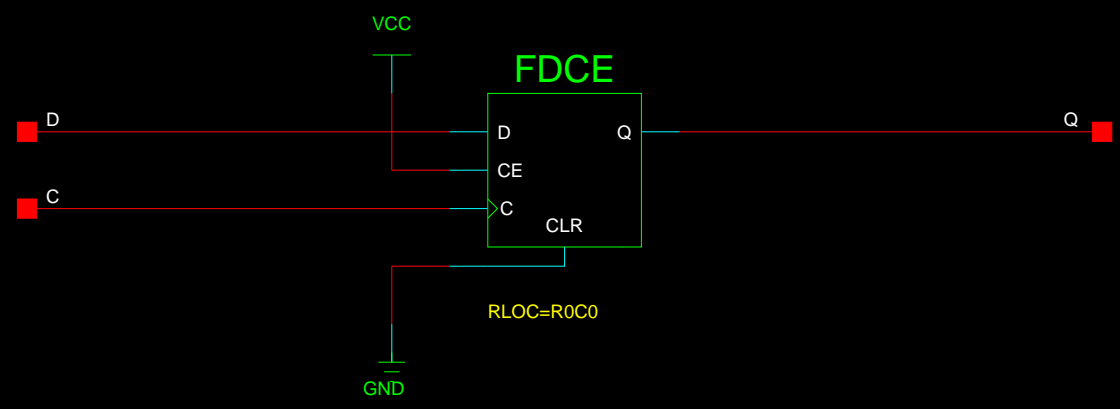
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Date: 14th December 1992

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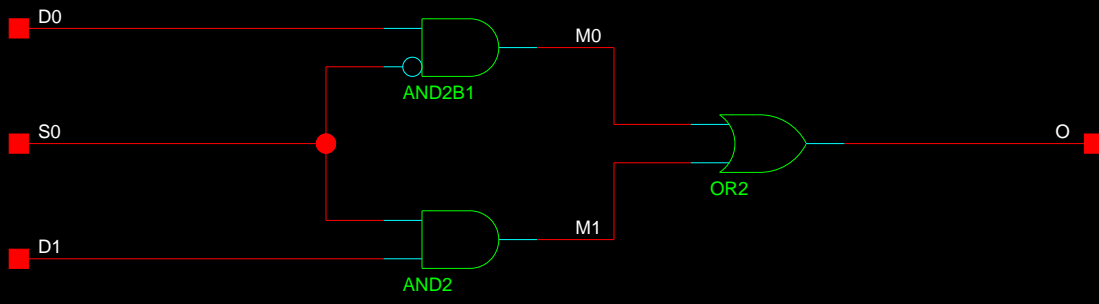
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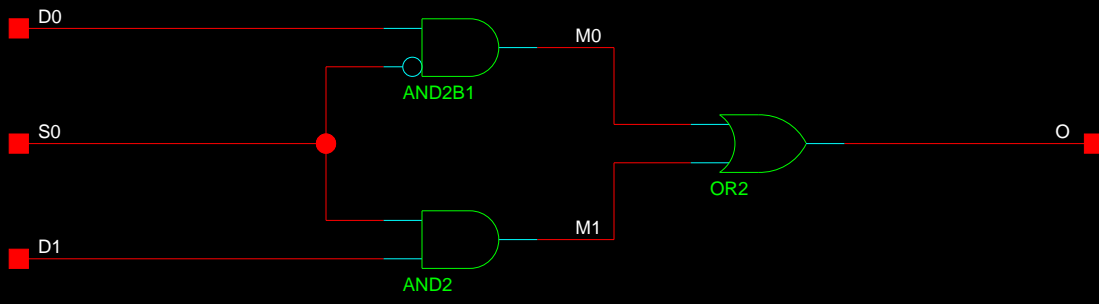
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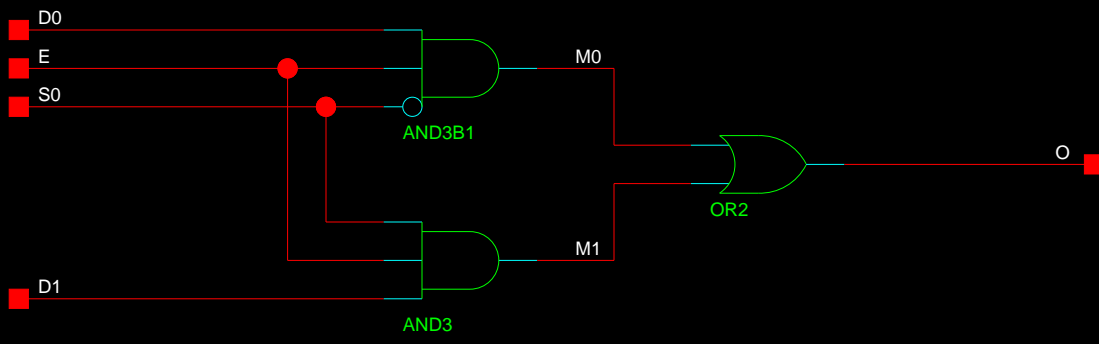
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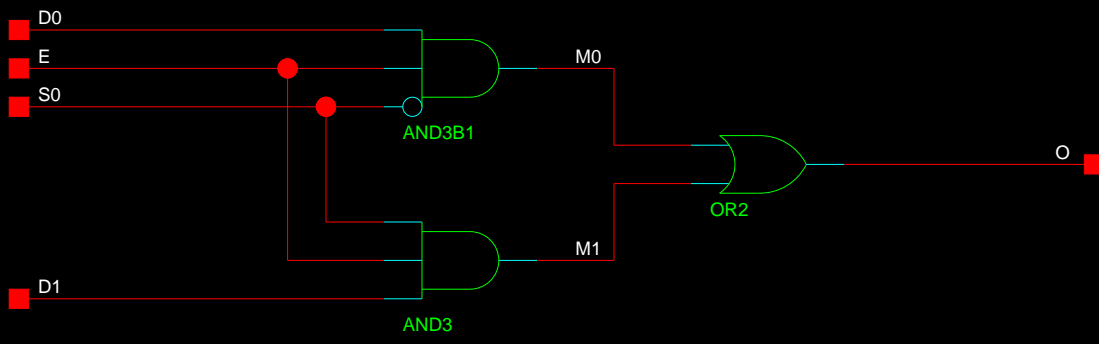
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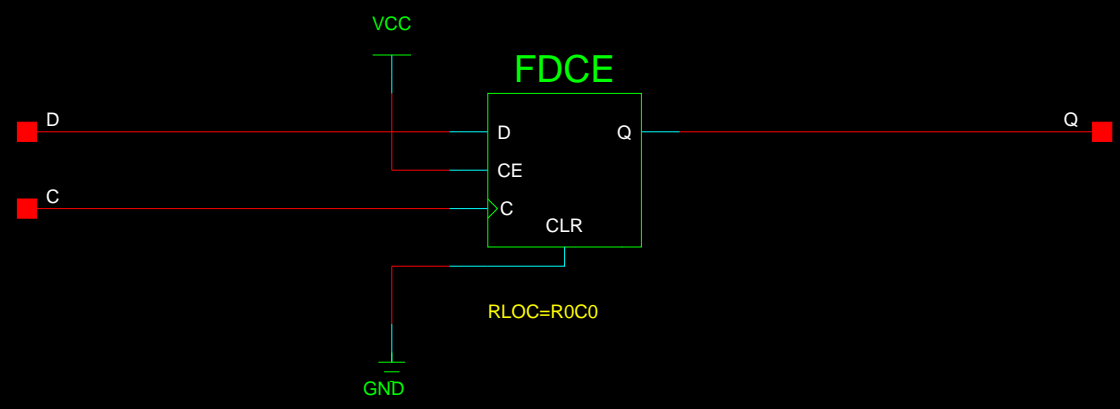
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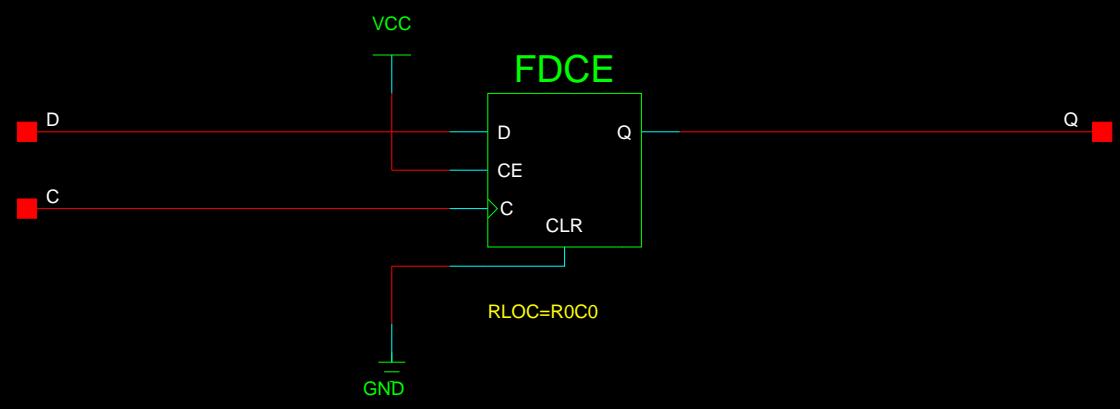
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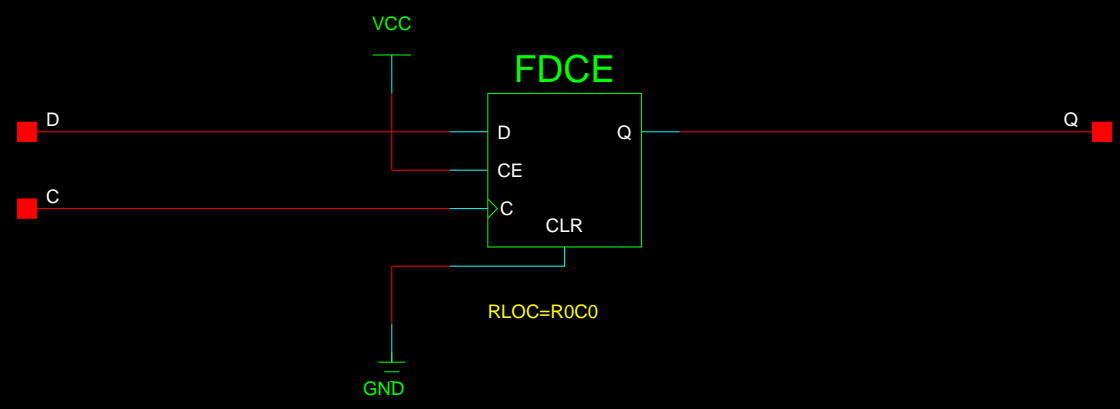
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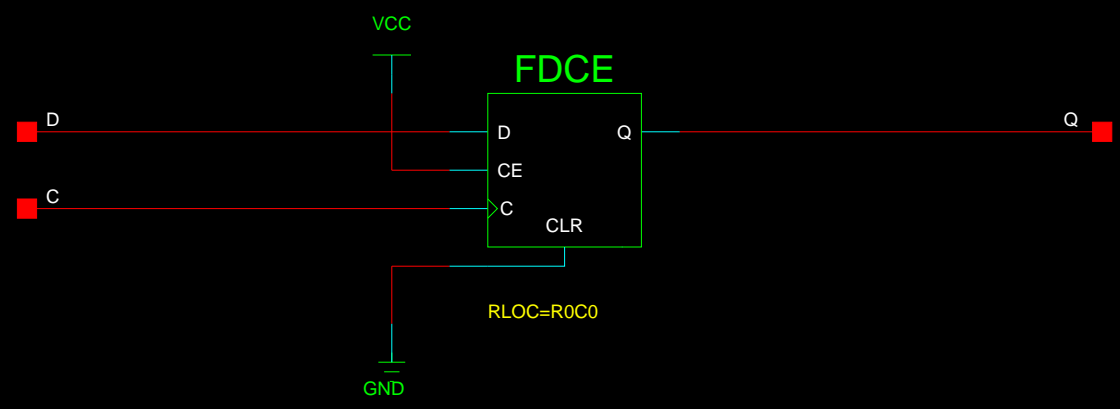
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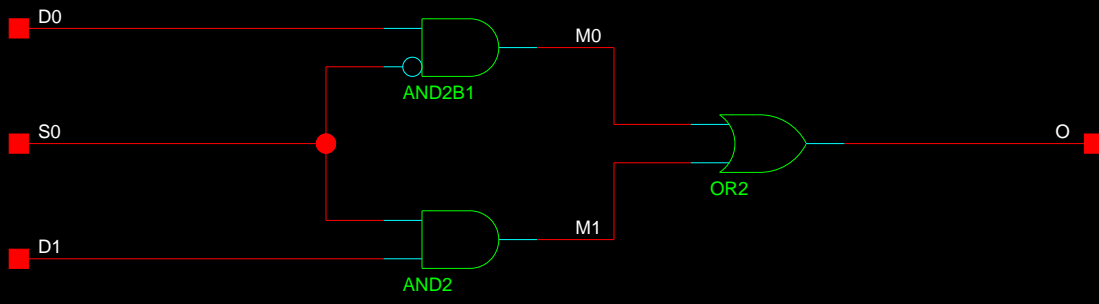
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Title: XC4000 Family M2_1 Macro

Comments: 2-to-1 Multiplexer

Date: 14th December 1992

Ver: 1

Sheet Size: A

Rev: A



Title: XC4000 Family M2_1 Macro

Comments: 2-to-1 Multiplexer

Date: 14th December 1992

Ver: 1

Sheet Size: A

Rev: A



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Ver: 1

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Date: 14th December 1992

Ver: 1

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Rev: A



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Date: 14th December 1992

Ver: 1

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Rev: A



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Date: 14th December 1992

Ver: 1

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Date: 14th December 1992

Ver: 1

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Date: 14th December 1992

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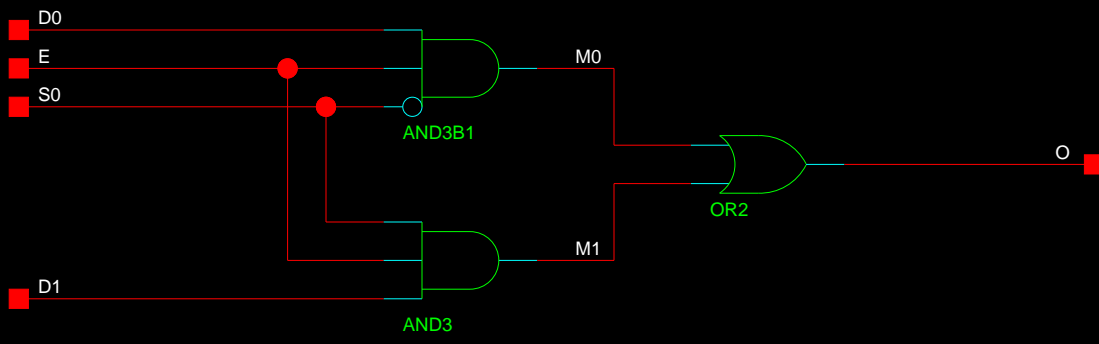
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Rev: A



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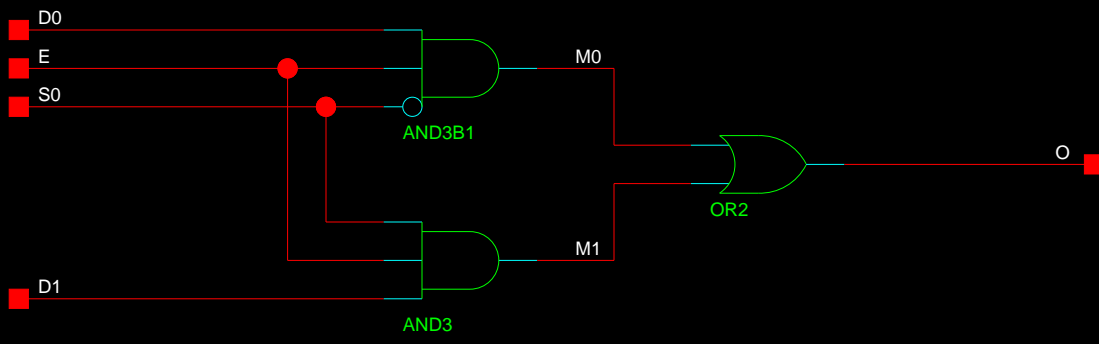
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Rev: A



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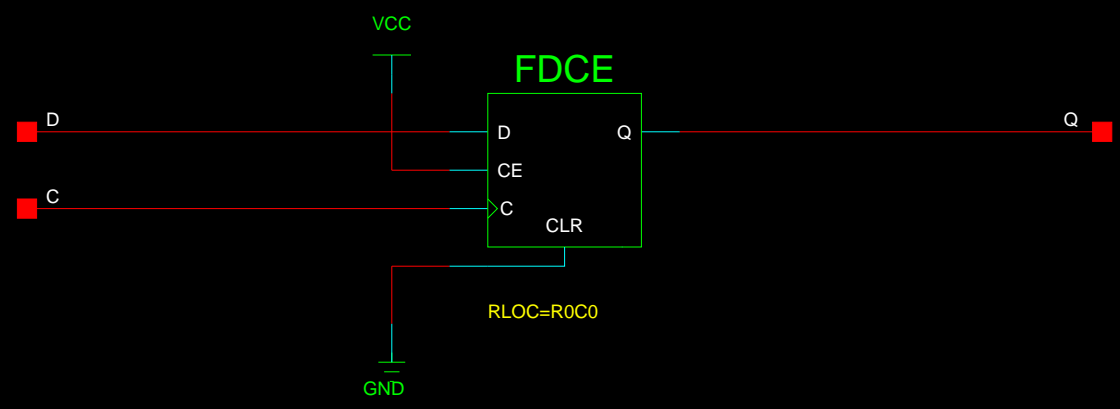
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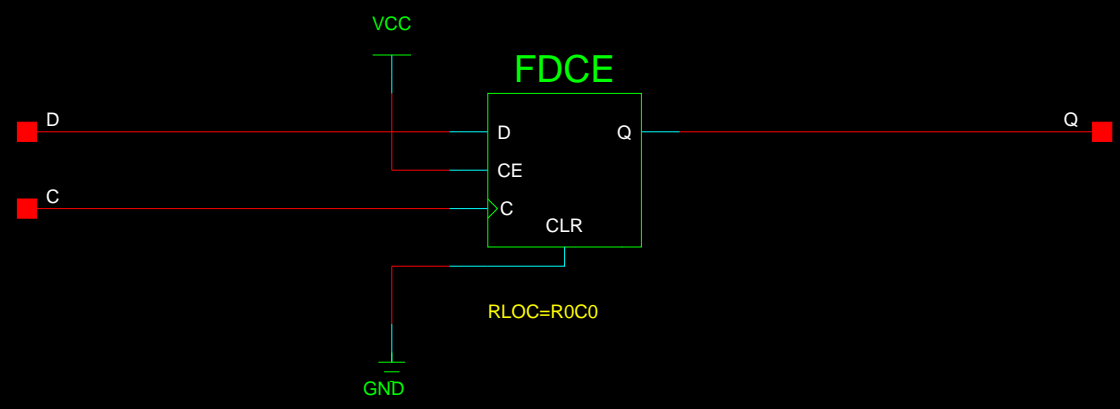
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Rev: A



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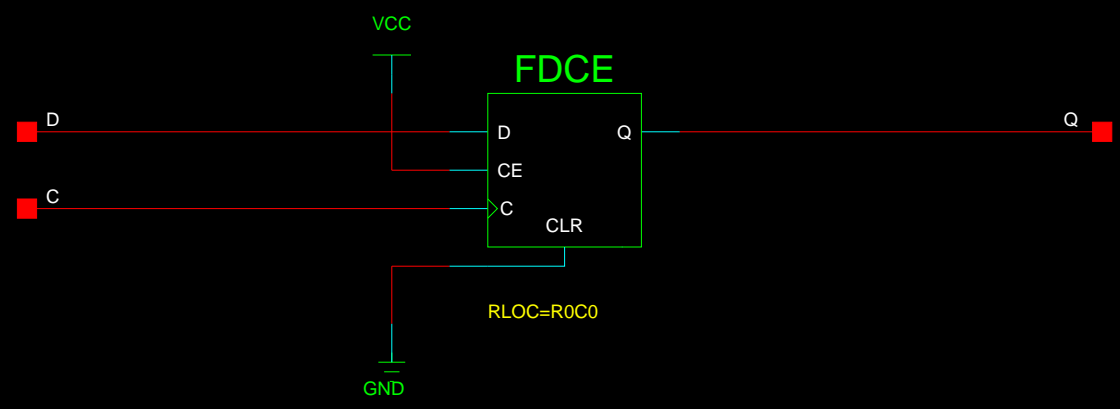
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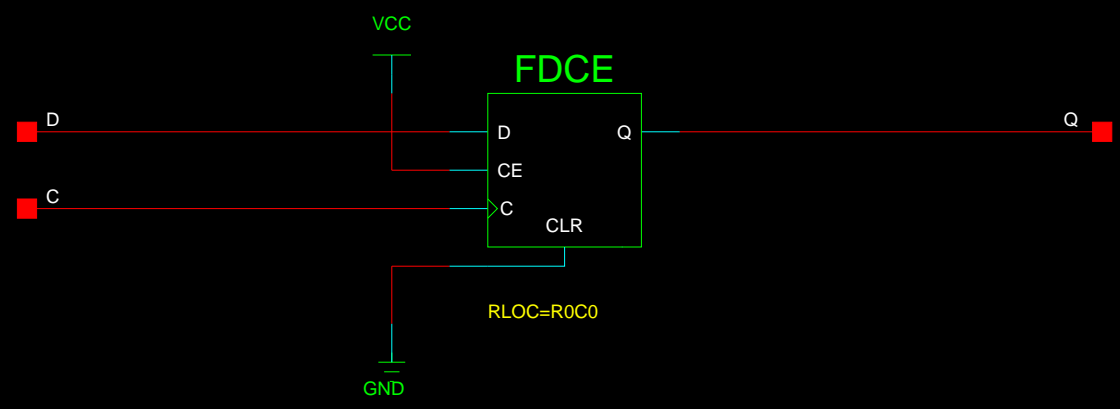
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Rev: A



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Ver: 1

Sheet Size: A

Rev: A



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Comments: 2-to-1 Multiplexer

Date: 14th December 1992

Ver: 1

Sheet Size: A

Rev: A



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Comments: 2-to-1 Multiplexer

Date: 14th December 1992

Ver: 1

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Date: 14th December 1992

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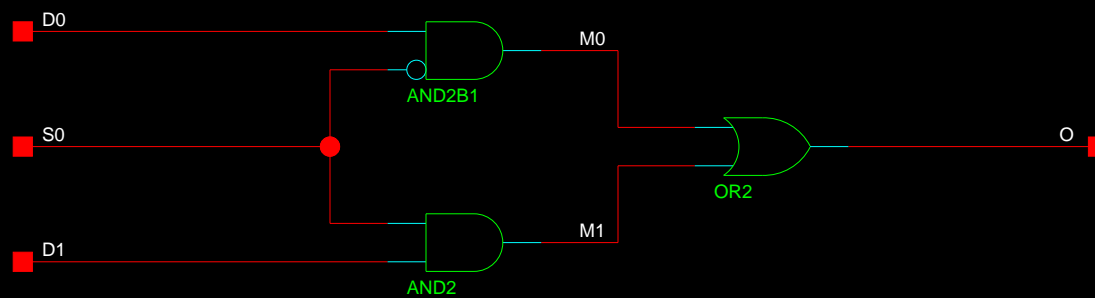
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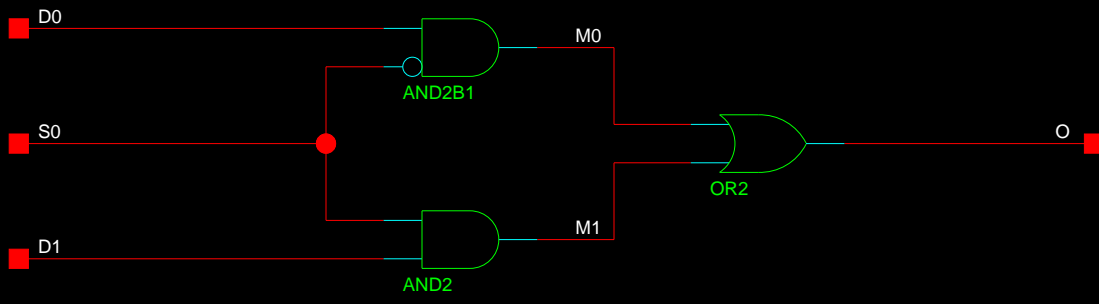
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Rev: A



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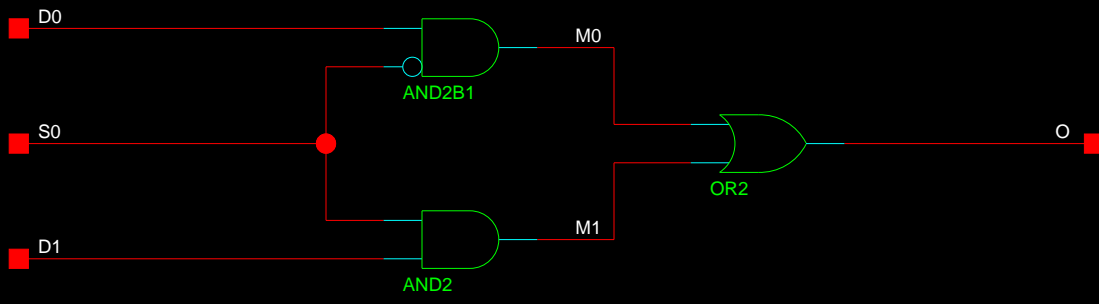
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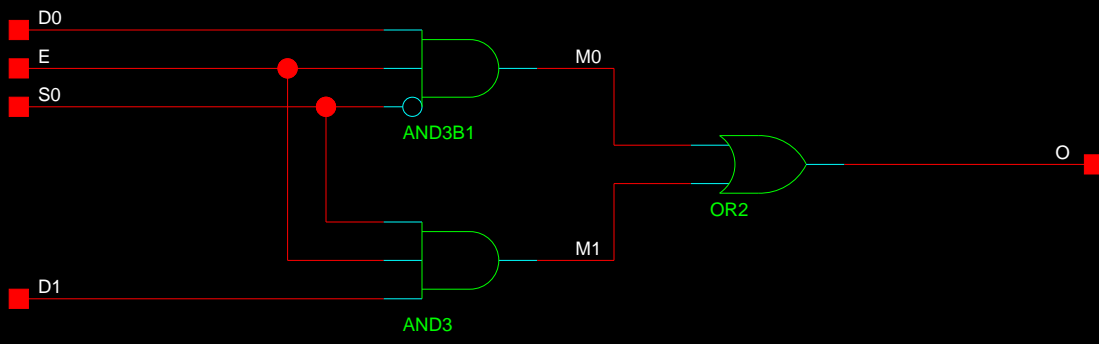
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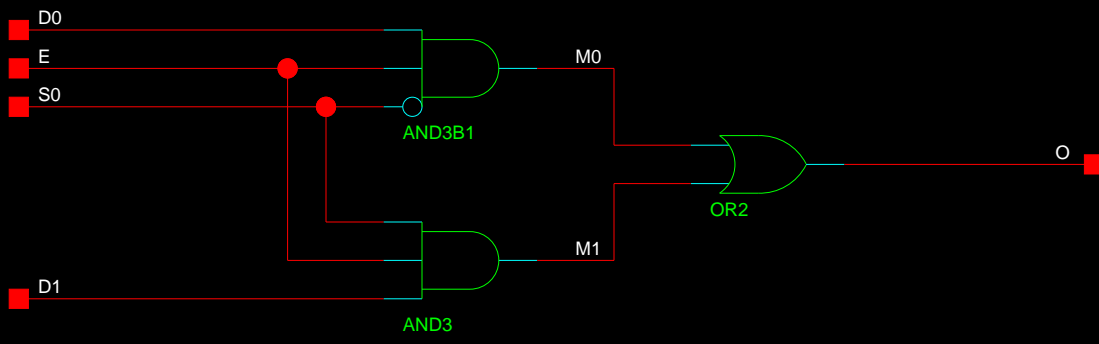
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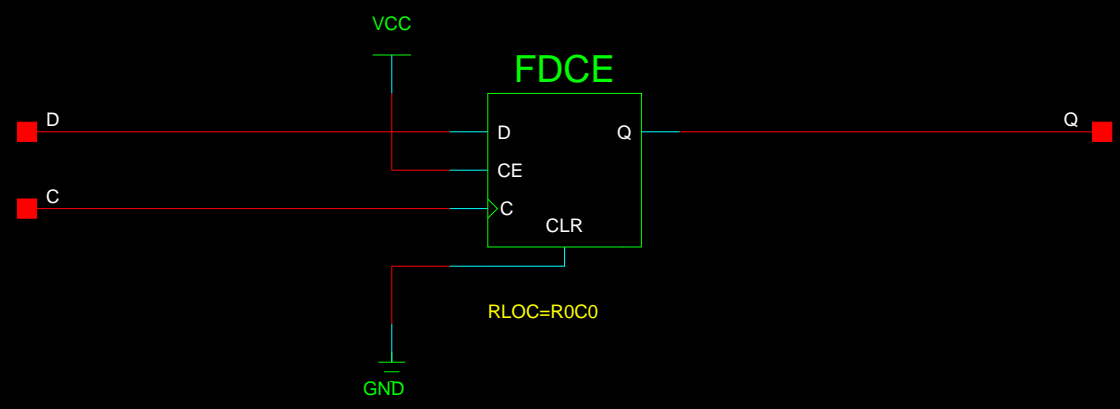
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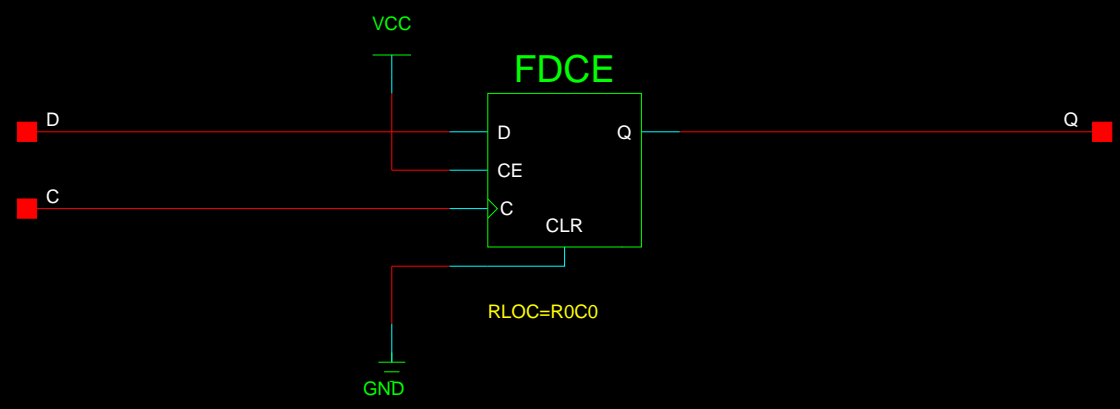
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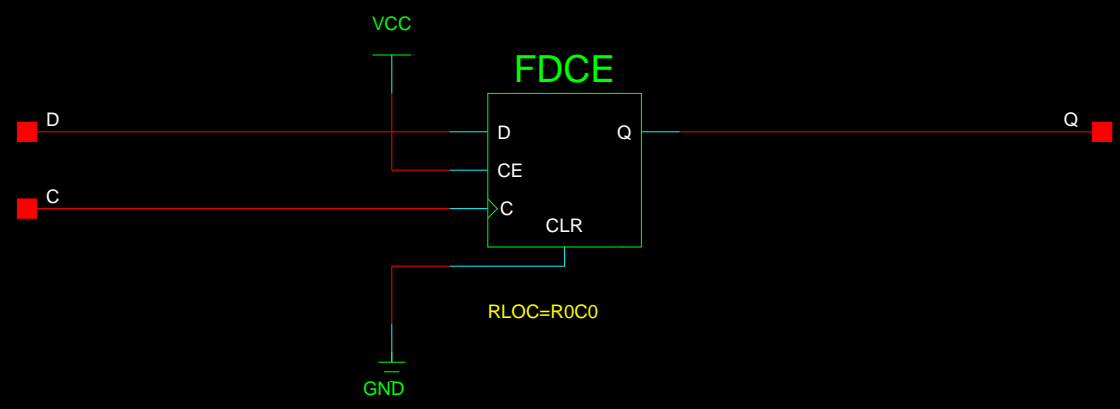
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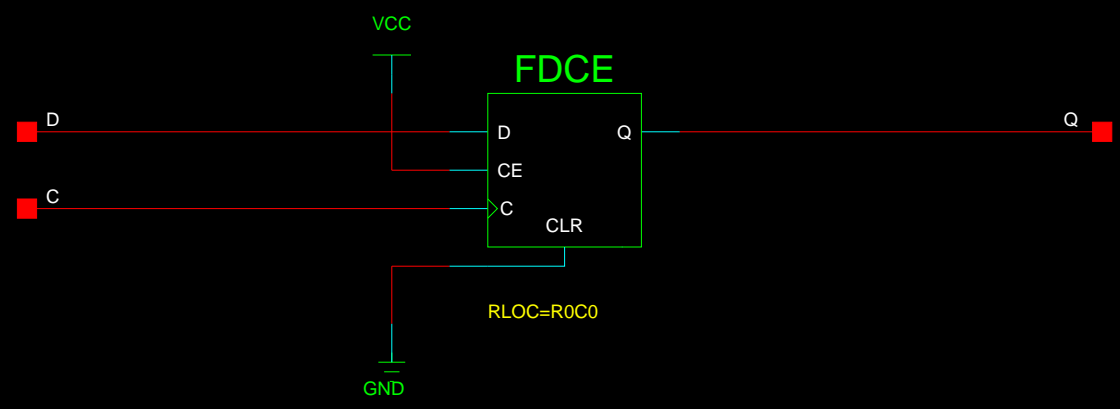
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Ver: 1

Sheet Size: A

Rev: A



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Comments: 2-to-1 Multiplexer

Date: 14th December 1992

Ver: 1

Sheet Size: A

Rev: A



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Date: 14th December 1992

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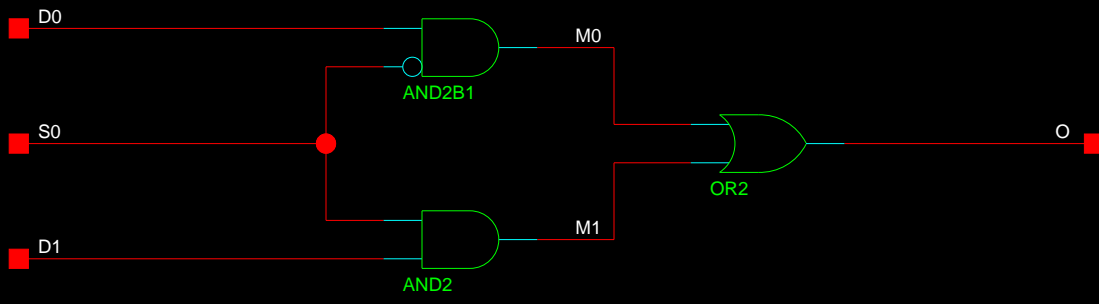
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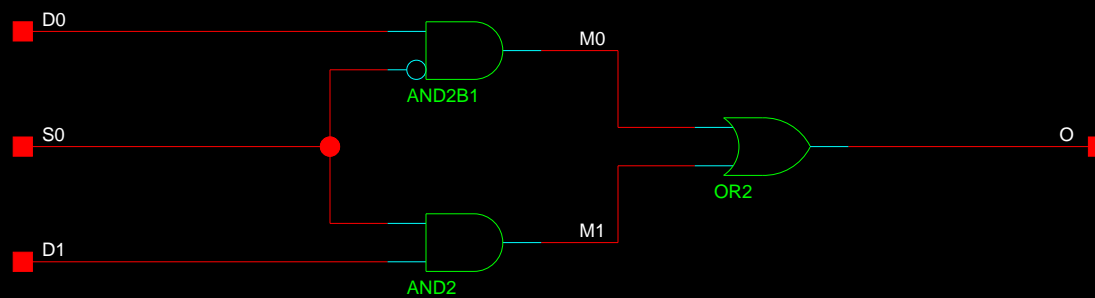
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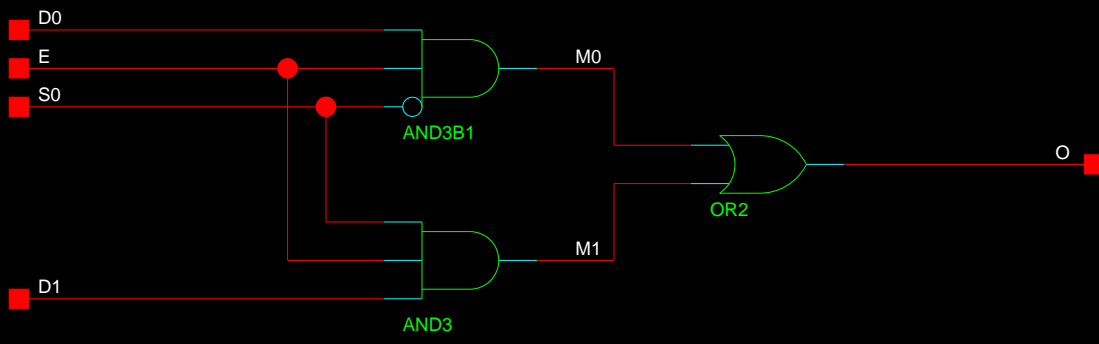
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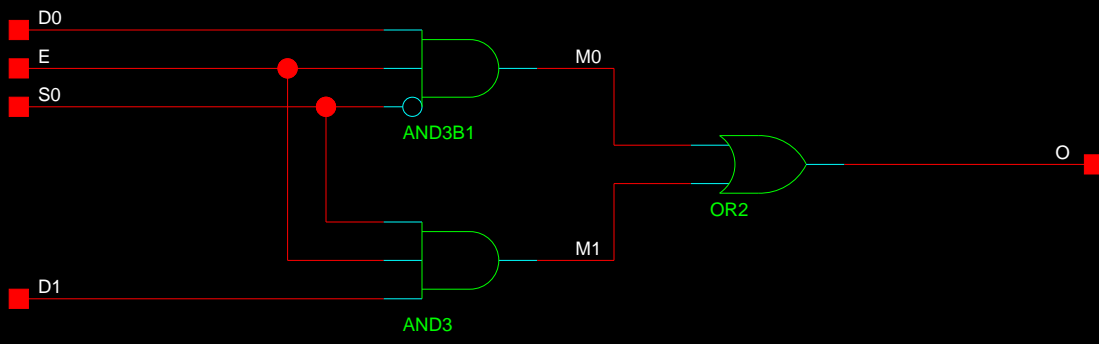
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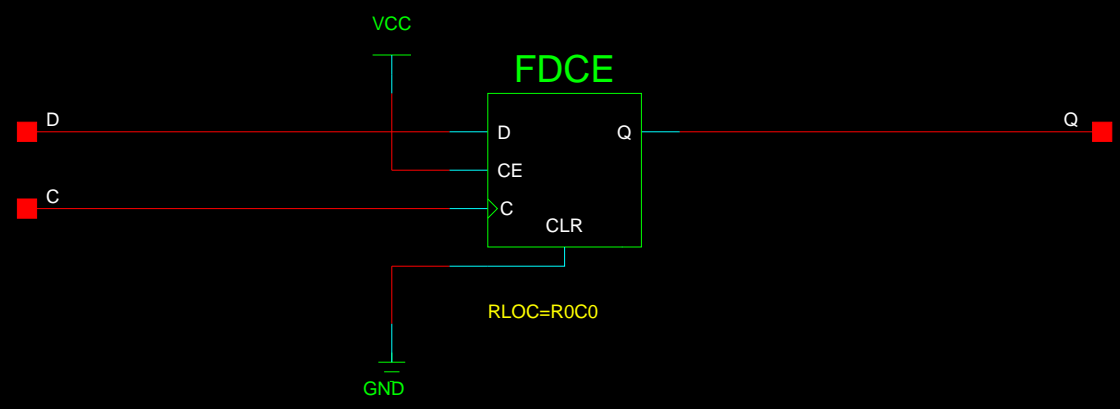
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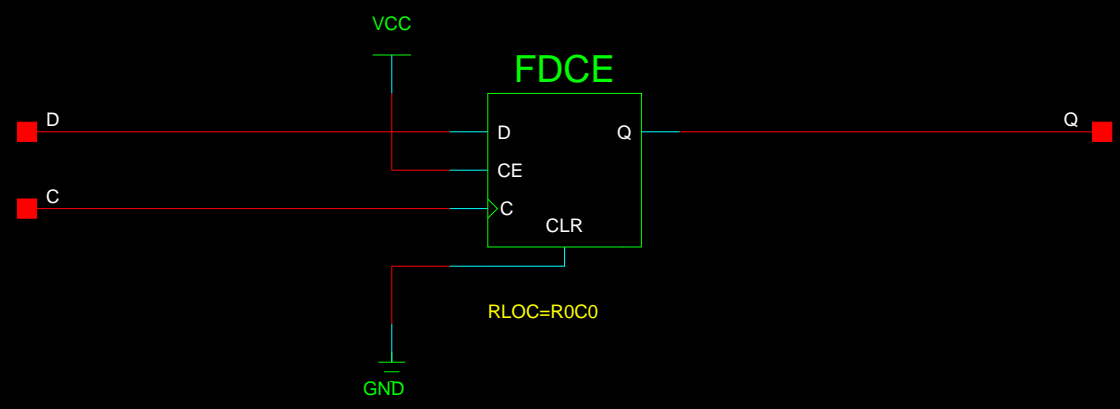
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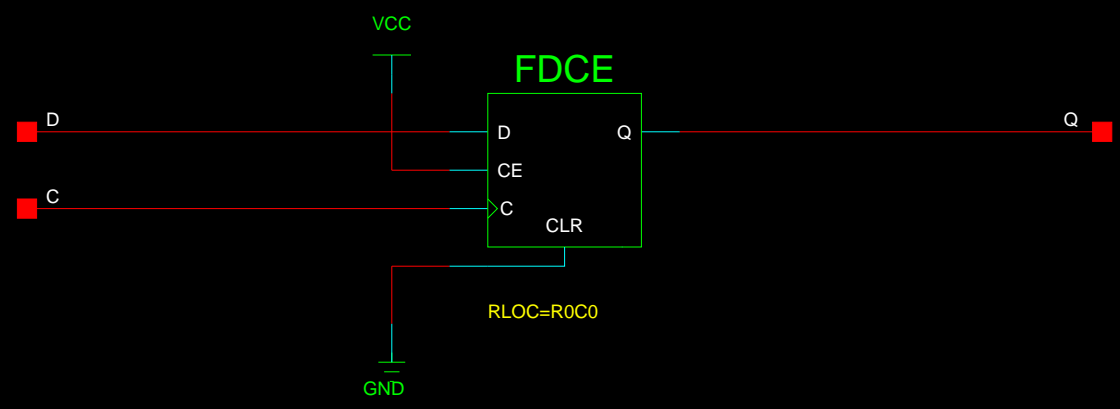
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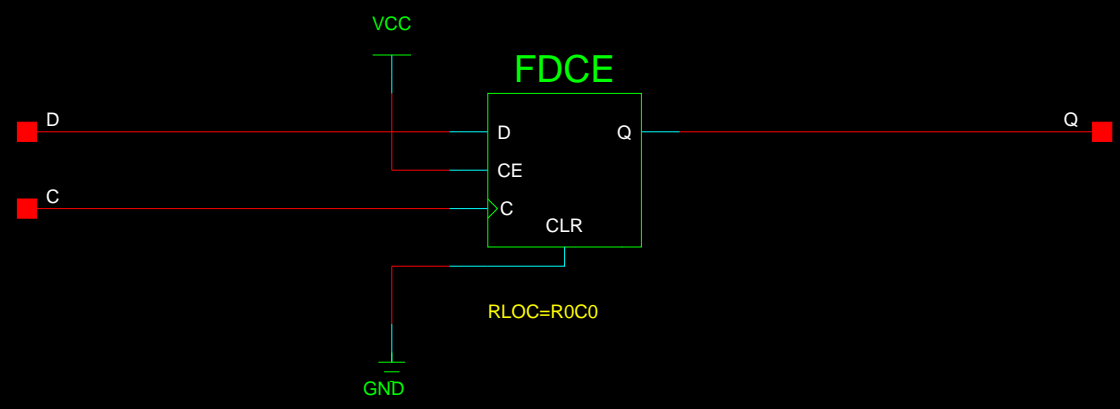
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Comments: D Flip-Flop

Date: 23rd December 1992

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Title: XC4000 Family M2_1 Macro

Comments: 2-to-1 Multiplexer

Date: 14th December 1992

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Title: XC4000 Family M2_1 Macro

Comments: 2-to-1 Multiplexer

Date: 14th December 1992

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Title: XC4000 Family M2_1 Macro

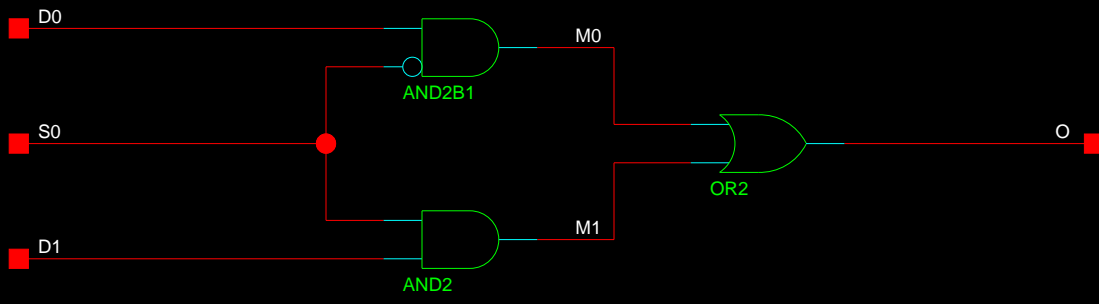
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Title: XC4000 Family M2_1 Macro

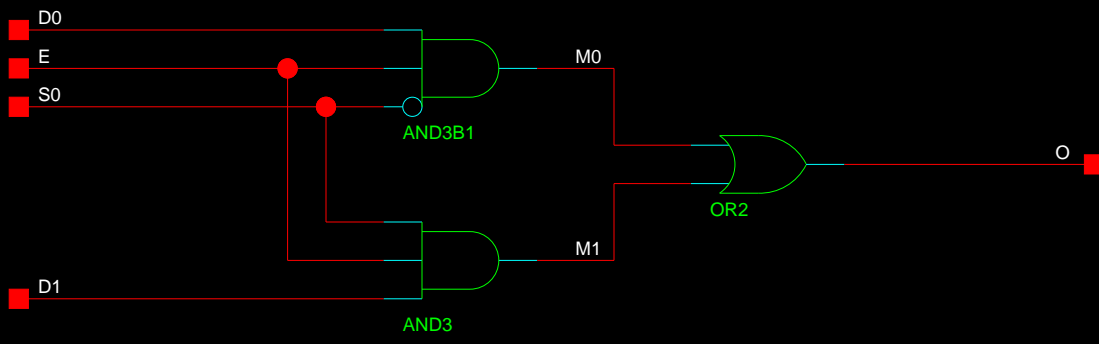
Comments: 2-to-1 Multiplexer

Date: 14th December 1992

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Title: XC4000 Family M2_1E Macro

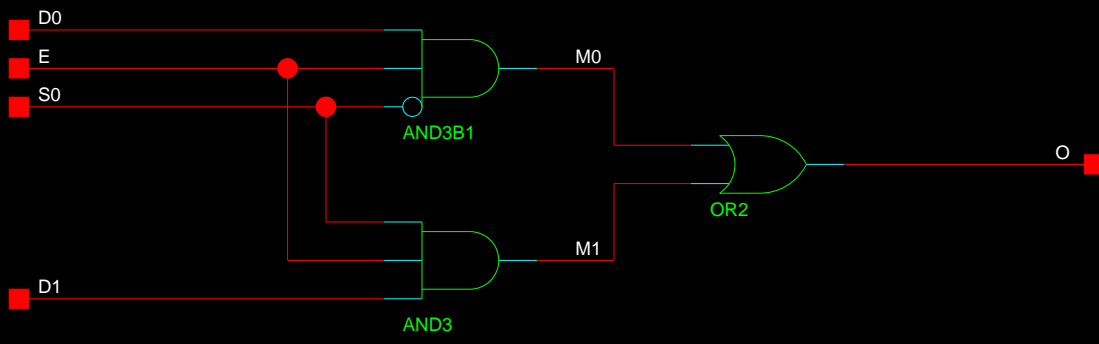
Comments: 2-to-1 Multiplexer with Enable

Date: 16th December 1992

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Title: XC4000 Family M2_1E Macro

Comments: 2-to-1 Multiplexer with Enable

Date: 16th December 1992

Ver: 1

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